PATTERNING STRATEGIES IN NANOFABRICATION: FROM PERIODIC PATTERNS TO FUNCTIONAL NANOSTRUCTURES

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General introduction

1

Nanofabrication is central to the development of several emerging technologies by facilitating the structuring of matter in the 1-100 nm regime. From a technological standpoint, the ability to fabricate structures at these spatial dimensions guarantees the essential continuation in the miniaturization of functional devices, also known in integrated circuit (IC) manufacturing as Moore's Law.^[1] The advantages of generating smaller devices are numerous, including lower costs, lower power consumption and higher performance. From a scientific standpoint, nanofabrication also allows for the exploration of fascinating (and in some cases technologically important) occurrences

that only occur at nanometer dimensions. Revolutionary devices including quantum dot lasers,^[2] quantum cascade lasers,^[3] and single-electron transistors^[4] are the outcome of fundamental studies on electronic processes as the quantum size effect (QSE)^[5] and single-electron tunneling (SET).^[6]

The drive towards the miniaturization of electronic circuits comes at the penalty of higher resistances and higher levels of power dissipation. The advantages of using light instead of electrons as the information carrier account for the efforts that are undertaken to progress the high-density integration and system performance towards all-optical circuits by the incorporation of photonic crystal (PhC) structures.^[7] PhC structures represent a novel class of optical materials (also present in nature^[8]) that contain periodic modulations in dielectric contrast resulting from two-^[9] and/or three-dimensional^[10] structuring of matter at the scale of the optical wavelength. The resulting characteristic photonic band gap (PBG) for a specific range of frequencies^[11] is responsible for the corresponding strong confinement and localization of light,^[12] and for the control^[13] and redistribution^[14] of spontaneous emission of light inside the PhC structures. These are essential elements in the development of future all-optical circuits. Several examples of devices that illustrate the full potential of PhCs include lossless waveguides,^[9c] zero-threshold lasers^[15] and photonic crystal fibers.^[16,17]

The major driving force for these technological developments is the demand for nanofabrication techniques that are capable of generating sub-100 nm features. Several state-of-the-art techniques such as deep UV and extreme UV photolithography, electron-beam writing, and ion-beam lithography, are capable of generating nanostructures. However, research and development of new (unconventional) fabrication strategies^[18] is crucial despite the enormous success and continuous efforts to extend the fabrication capabilities of these conventional techniques in terms of cost-effectiveness and high-throughput processing of sub-100 nm structures. This thesis contributes to the continuous development of unconventional fabrication strategies in several areas of nanofabrication.

Chapter 2 presents a procedural review of unconventional nanofabrication for lowcost and fast prototyping of nanostructures in the sub-100 nm range (instead of dealing with extensions of conventional nanofabrication). Of particular interest are potential patterning principles that result in significant advancements in terms of resolution, reproducibility and uniformity of the structures. Many examples of the fabrication of periodic and functional nanostructures are given in this chapter, with a preference for the fabrication of potential nanostructures in photonics.

The first part of this thesis (Chapters 3, 4 and 5) deals with several interesting patterning strategies of matter (molecules and polymers). The main purpose of this research is to investigate in detail the characteristics of supramolecular patterning of molecules and capillary force patterning of polymers in terms of resolution, reproducibility and uniformity by patterning periodic patterns on surfaces.

Chapter 3 describes a patterning strategy by using multiple supramolecular interactions in the transfer of high-stability patterns of functional molecules at a lateral resolution of 60 nm onto molecular printboards. Printing and competitive rinsing experiments have been carried out to assess the influence of the valency of the guest molecules on the pattern stability and to illustrate the concept of supramolecular microcontact printing (μ CP) and dip-pen nanolithography (DPN) on molecular printboards. Additionally, the molecular patterns of dendrimer-stabilized nanoparticles have been further exploited as catalysts in the electroless deposition of metals on top of printboards.

The next two chapters (Chapters 4 and 5) discuss a facile fabrication strategy for the high-resolution patterning of polymer films by capillary force lithography (CFL). Additionally to investigate the characteristics of capillary rise in edge lithography, the resulting CFL patterns have been exploited (for the first time) as functional templates in high-resolution lithography. Structural analysis has been carried out by atomic force microscopy (AFM) and scanning electron microscopy (SEM) to assess the mechanical stability and integrity of CFL templates and the corresponding derivatives (in the form of masters, molds and etch masks) during processing.

Chapter 4 illustrates the potential of these templates as masters in the fabrication of second-generation stamps for application in high-resolution soft lithography. This has been shown by several different μ CP experiments on gold and subsequent wetchemical etching in the generation of metal patterns on silicon substrates. Structural analysis has been carried out to investigate the discrepancy between the lateral dimensions of the metal features and the indentations present on the second-generation stamps. Additionally, these metal patterns have been exploited as etch

masks in the generation of nanochannels into silicon substrates by reactive ion etching (RIE).

Chapter 5 describes two potential extensions of the use of CFL templates as masters in nanoimprint lithography (NIL) and as etch masks in the wet-chemical etching of metals by incorporating different polymers into the CFL procedure. Transfer of stamp residues has been shown by X-ray photoelectron spectroscopy (XPS) to occur during CFL processing, and corresponding measures have been undertaken to increase the processability of the CFL templates. Finally, the basic concept of overetching is shown as a strategy to feature reduction.

The second part of this thesis (Chapters 6 and 7) illustrates a novel strategy for the accurate pattern inversion of polymer patterns into silicon oxide (SiO₂) by incorporation of local oxidation of silicon (LOCOS) into NIL processing. The final goal of this research has been to develop a low-cost replication procedure of two-dimensional photonic crystal waveguides (2D PhCWGs).

Chapter 6 describes the entire engineering technology to set the design rules for optimal LOCOS inversion in terms of layer thickness and etch selectivities. The partial conversion of silicon nitride (Si_3N_4) to silicon oxynitride (SiN_xO_y) during the oxidation step of the LOCOS procedure is shown by etch rate measurements. The use of guidelines is shown to systematically locate the optimal parameters for silicon etching at high anisotropy and high etch selectivity using ultrathin SiO₂ etch masks.

Chapter 7 presents the successful fabrication and replication of 2D PhCWGs on silicon-on-insulator (SOI) substrates by elaborating on the technological advancements in NIL processing set in Chapter 6. Local examination using photon scanning tunneling microscopy (PSTM) has been carried out to assess the guiding properties of the resulting 2D PhCWGs (in terms of confinement and loss of the propagating light) at wavelengths in the telecommunication range.

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Unconventional strategies in nanofabrication: patterning principles

2

This chapter gives a current overview of technological advancements (from 1995 to mid-2008) within the field of unconventional nanofabrication for accurate patterning in the sub-100 nm range. The focus lies on patterning principles that result in advancements in terms of resolution, pattern fidelity and capability to structure different materials, and on the fabrication of nanophotonic structures.

2.1 Introduction

1965 is the year that the co-founder of Intel[®] Gordon Moore came with his prediction, now known as Moore's Law, that the number of transistors on a chip doubles every two years.^[1] Intel's commitment to Moore's Law has become a source of inspiration for several technological developments resulting in an exponential increase in the number of transistors of computer microprocessors: from 2200 in 1965 to about one billion in 2007 with the Quad-Core Intel[®] processor (Figure 2.1).



Figure 2.1 Intel's 45 nm high-k metal gate silicon technology is the next-generation Intel[®] CoreTM micro architecture. With about twice the density of the preceding 65 nm technology, this technology packs about double the number of transistors into the same silicon space (to about one billion for a quad-core processor).^[2]

The International Technology Roadmap for Semiconductors (ITRS)^[3] is an assessment of the Semiconductor Industry Association (SIA)^[4] to chart the necessary advancements in technology by identifying the technological challenges for the continuation of Moore's Law (Fig. 2.2).



Figure 2.2 Graphical representation of the International Technology Roadmap for Semiconductors and the necessary technological advancements for maintaining the exponential decrease of the half-pitch for dynamic random-access memory (DRAM) components (dark grey line).^[3]

The main challenge is to meet the stringent industrial requirements of cost-effective manufacturing with excellent critical dimension (CD) control in combination with accurate control of positioning (overlay) at ever-increasing resolution.^[5] As shown in Fig. 2.2, the roadmap forecasts a 45-nm node target in 2010 for deep-UV (DUV, 193 nm) lithography with the ArF excimer laser, corresponding to transistor gate lengths down to 18 nm and a spacing of 45 nm.

The inherent limitations of photolithography (in general) with respect to resolution (R) and depth-of-focus (DOF) are set by two scaling laws:

$$\mathbf{R} = k_1 \cdot \frac{\lambda}{NA} \tag{2.1}$$

$$DOF = 0.5 \cdot k_2 \cdot \frac{\lambda}{NA^2}$$
(2.2)

where R is the critical dimension, λ is the exposure wavelength, *NA* the numerical aperture of the lens system, and k_1 and k_2 are the imaging constants. Previous strategies to extend the lifetime of optical lithography in terms of ultimate resolution with acceptable levels of DOF were aiming towards illumination sources with shorter wavelengths, optical systems with higher numerical apertures and smaller k_1 values.^[6] Shorter exposure wavelengths (< 193 nm) induce many complications to imaging optics and resist systems. The latest strategies focus on resolution enhancement techniques^[7] such as off-axis illumination, phase-shifting masks, optical proximity correction, and immersion lithography in order to meet the key industrial requirements for cost-effective manufacturing up to the 45-nm node target. Beyond the 45-nm node, the semiconductor industry will soon run out of economically feasible strategies for DUV (193 nm) lithography thereby necessitating the research and development of alternative, next-generation lithography (ML2)^[9,10,11,12] and imprint technology^[13] (see Fig. 2.2).

NGL techniques are capable of fabricating high-resolution features; however, the development of these techniques still requires ingenuity to overcome several specific technological challenges to become feasible for cost-effective semiconductor

manufacturing. One of the potential NGL techniques, i.e., imprint lithography, is a direct result of the technological accomplishments in the field of unconventional nanofabrication.^[14] The limitations of conventional nanofabrication in many (scientific) areas, e.g., chemistry, biology, and materials science, act as a driving force for many scientists to invent unconventional approaches to nanofabrication. These limitations include (i) low accessibility for fundamental scientific research due to high capital and operating costs, (ii) difficulty of operation, (iii) low applicability to several important classes of (bio)organic^[15] and organometallic materials, (iv) low control over surface functionality, and (v) few opportunities for innovation due to the high stage of development of conventional nanofabrication.

This chapter attempts to present a (procedural) review of unconventional nanofabrication for low-cost and fast prototyping of nanostructures, instead of dealing with extensions of conventional nanofabrication. The purpose is to illustrate only the most promising patterning principles in unconventional nanofabrication in terms of resolution, pattern fidelity (reproducibility, uniformity), and the capability to structure different materials. Conceptually, the generation and replication of patterns are two different approaches in terms of process characteristics and requirements and in fact accentuate different aspects of nanofabrication. This chapter reviews the current state-of-the-art of unconventional nanofabrication by categorizing the techniques into one of the two major tracks: generation of patterns (Section 2.2) and replication of patterns are given in these two sections, with the emphasis to a very active and fascinating field of application: nanophotonics.

2.2 Generation of patterns

The conventional approach of generating high-resolution patterns is to use directwrite technologies that usually exploit a beam of photons or high energy particles to pattern matter in a subtractive and serial manner. Examples of these techniques include ion-beam lithography (IBL),^[9] electron-beam lithography (EBL),^[10] protonbeam writing,^[16] and multiphoton lithography (MPL).^[17] These techniques comply with the stringent requirements for implementation in semiconductor manufacturing and are applied in the fabrication of high-resolution masks. The unconventional techniques covered in this section are still in an experimental stage of development.

2.2.1 Pattern formation by size reduction

At present, there exists a large set of potential techniques for size reduction in case of generating periodic high-resolution features.

2.2.1.1 Edge lithography

The concept of edge lithography is to exploit the topographical edges of micrometer-scale features in order to fabricate nanometer-scale features using a cost-effective patterning strategy. A summation of the edge lithographic techniques available is given in Table 2.1.

Technique	Patterning principle	Resolution*
near-field phase-shifting photolithography	constructive and destructive interference of light using transparent elastomeric masks as the optical element	30 nm
undercutting by isotropic wet-chemical etching	lift-off to reproduce the outlines of the original template by the undercut	50 nm
nanoscale spacer lithography	sidewall deposition on a sacrificial template	< 10 nm
capillary force lithography	capillary rise of polymer melts by wetting on elastomeric stamps and successive break down of the meniscus at temperatures above Tg	< 100 nm
edge-spreading lithography	diffusion of substrate-compatible ink by guidance of a topographical template	30 nm
patterning local disorder in monolayer resists	introduction of defects by formation of SAMs at the transitions of a topographical template	50 nm

 Table 2.1 List of potential strategies in edge lithography

* The resolution values in this table represent the smallest feature dimensions that have been shown to date with the respective technique, but do (in most cases) not represent the ultimate limit.

The first edge-lithographic technique, near-field phase-shifting photolithography,^[18] shares its patterning principle with contact-mode photolithography. In contrast to projection photolithography, contact-mode set ups use a transparent mask instead of projection optics. The concept is to minimize the mask-resist gap, allowing the resists to interact with the evanescent waves, i.e., nonpropagating electromagnetic modes, that modulate the intensity of the illuminating light in a region close to the mask at much higher spatial frequencies than is feasible by far-field diffraction.

The corresponding phase shift generates small regions of constructive and destructive interference of the light with lateral dimensions on the order of one-fourth of λ (or less), reaching its optimum at mask-resist gaps that are equal to the ratio of λ to twice the change in refractive index (Δ n) between air and the optical mask.^[18b] Near-field phase-shifting photolithography exploits this interference by projecting only the edges of the transparent mask onto the surface of the resist (Fig. 2.3a). Additionally, pattern inversion of the resulting pattern (i.e., trenches in positive-tone resist) is within the capabilities of this technique simply by changing to negative-tone resists (Fig. 2.3a).



Figure 2.3 (a) Schematic representation of near-field phase-shifting photolithography for positive-tone and negative-tone resists.^[18c] (b) Scanning electron microscope (SEM) image of 30 nm-wide rings in positive-tone resist.^[18c] (c) SEM image of 40 nm-wide trenches in palladium after wet-chemical etching using the resist as the etch mask.^[18c] (d) SEM image of ~2 cm long and 40 nm-wide silicon nanowires after lift-off from the SOI substrate (scale bar: $2 \mu m$).^[19]

The disadvantages of conventional contact-mode photo-lithography of using elaborate vacuum systems to bring hard masks in close contact with the resist over large areas – potential damage and contamination of the mask – are overcome by incorporation of elastomeric poly(dimethylsiloxane) (PDMS) masks. These masks are also optically transparent, facilitate conformal contact over large areas without application of any external force and, by this means, take advantage of the full potential of the near-field optics. Fig. 2.3b illustrates this fact by the fabrication of features in positive-tone resists as small as ~30 nm.

As this technique is only applicable to photosensitive materials, Whitesides et al. have taken the resulting resist patterns to the next level by patterning different metals on surfaces using lift-off^[18a] and selective wet-chemical etching (Fig. 2.3c) and by generating 80 nm-wide single-crystalline silicon nanowires with lengths over 2 cm (Fig. 2.3d). Several potential applications in optics of the resulting metal nanowires on surfaces include nanowire polarizers,^[18a] optical notch filters, optical memories, and diffraction gratings with subwavelength features. By addition of amplitude modulating components in the form of thin metal layers onto the elastomeric mask, Schmid et al. have found a means to attain more flexibility in the generation of different types of patterns in resists.^[20] Additional resolution improvements are foreseen by reducing the thickness of the resist, by increasing the index of refraction of the resist and by using surface-sensitive resists.

The second edge-lithographic technique also exploits a well-known strategy in semiconductor manufacturing – undercutting (overetching) by isotropic wet-chemical etching.^[21] The concept is to use one resist mask to perform two sequential processes: (1) overetching to create a small undercut, and (2) metal lift-off to reproduce the edges of the resist pattern. The procedure of undercutting metal films by isotropic wet-chemical etching^[22] as a means to nanostructure films over large areas (> 4 cm²) is shown in Fig. 2.4a. Wet-chemical etching of polycrystalline metal films is isotropic by nature, resulting in convex sidewalls. For this reason, the minimum undercut depends on the film thickness and the grain size of the metal. For chromium films (with grain sizes of ~15-25 nm), the minimum feature size increases from 50 nm (for a film thickness of 10 nm, Fig. 2.4b) to 100 nm (for a film thickness of 35 nm).^[22] Additionally, AFM studies have shown that most etch systems are sensitive to grain

boundaries of polycrystalline metals,^[23] therefore suggesting that the grain size is a limiting factor to high-resolution patterning with respect to the line-edge roughness (LER) of the final features. Concerning the impact of the material aspects in undercutting, higher resolution and LER are within reach in case of thin films of amorphous materials (e.g., alloys, glass, etc.) using this technique. A technical advantage of undercutting is the lateral dimension control by the etch time. Strong bandpass filters represent one application of the resulting patterns in optics, as shown in Fig. 2.4c in the case of two-dimensional arrays of slots in chromium on optical grade Si/SiO₂.



Figure 2.4 (a) Schematic representation of undercutting by isotropic wet-chemical etching. (b) SEM image of 50 nm-wide trenches in chromium and (c) infrared (IR) transmission spectrum of an array of loops in aluminum (see inset) on a substrate of optical grade Si/SiO₂.^[22]

The next edge-lithographic technique originates from the area of microelectromechanical systems (MEMS) in the fabrication of gate sidewall spacers: spacer lithography. Several forms exist in edge lithography,^[24,25,26] with one common concept: conformal deposition of a thin film of a material over a topographical template of a sacrificial material in combination with successive etching of the sacrificial support to release the final nanostructures (Fig. 2.5a).

The main advantages of this technique are its ultimate resolution with accurate control over the lateral feature dimensions by means of the film thickness during conformal deposition. Additionally, this technique enables iterative processing by

alternating the two materials in order to generate high pattern densities by multiplication.^[24a,25] This is an exclusive feature for edge lithographic techniques and overcomes the main disadvantage of low pattern density. Sub-10 nm resolution is within the capabilities, however, at the expense of materials choice for patterning. Potential applications of the resulting structures include the use as hard molds for nanoimprint lithography (see Section 2.3.2)^[24,26] and as nanochannels (Fig. 2.5b). Recently, a gas-sensing device for H₂ and CO was reported using this technique for the fabrication of the responsive 70-nm wide ZnO nanowires (Fig. 2.5c).



Figure 2.5 (a) Schematic representation of spacer lithography.^[26] (b) Fluorescent microscopy images of a continuous flow of water containing a fluorescent dye at three positions along a 1.5 cm-long 30 nm-wide nanochannel.^[27] (c) SEM image of the ZnO nanowires (scale bar: 250 nm) and sensitivity curves of the corresponding devices and ZnO thin films at various concentrations of CO in the range of 500-5000 ppm at 200 °C.^[28]

Capillary force lithography $(CFL)^{[29]}$ is a technique that combines the essential feature of nanoimprint lithography $(NIL)^{[13]}$ – molding a polymer melt – with the key element of soft lithography^[30] – molding with elastomeric stamps for large-area patterning (> 16 cm²)^[29b] of thin polymer films (with respect to the feature height of the stamp) without the use of external force (Fig. 2.6a). The concept of this technique is the capillary rise of a polymer at temperatures above the glass-transition temperature due to a lowering of the total free energy upon wetting of the elastomeric mold.

Several theoretical studies have been carried out on the kinetics of the resulting capillary rise,^[31] the transient formation and breakdown of the meniscus in microchannels^[32] during the patterning of thin polymer films using permeable molds. These have shown that under certain conditions (polymer film thickness, dimensions on the elastomeric stamp, and film-substrate interactions) the minimum height at the center of the meniscus can reach a minimum level at which London forces induce instability across the channel, resulting in the breakdown of the meniscus and partial exposure of the substrate (Fig. 2.6b).^[32a] This was proven by the selective electroless deposition of copper on palladium surfaces.^[29a]



Figure 2.6 (a) Schematic representation of capillary force lithography.^[29a] (b) AFM images at two stages of the process illustrating the meniscus breakdown.^[32a]

Several potential applications of these polymer templates in edge lithography can be found in Chapters 4 and 5 of this thesis, illustrating the sub-100 nm patterning capabilities of CFL. Additionally, as Sections 2.2.3 and 2.3.2 will illustrate, CFL is capable of generating also more complex patterns in polymers.

Edge-spreading lithography $(ESL)^{[33]}$ also originates from the combination of features from two different techniques, that is microcontact printing^[30] – printing molecular inks with elastomeric stamps – and nanosphere lithography $(NSL)^{[34]}$ – self-assembly of colloidal particles on surfaces (Fig. 2.7a). These colloidal particles facilitate the diffusion of ink molecules from a planar elastomeric stamp by guidance along its surface to form a self-assembled monolayer (SAM) on the ink-compatible surfaces.

The guidance is not restricted to colloidal particles, as was shown soon after by the use of resist patterns. As reactive spreading^[35] is responsible for the SAM formation on coinage metals, control over the lateral feature dimension depends essentially on the ink supply by means of the contact time of the planar stamp and the ink concentration.^[33] As with any technique that takes advantage of molecular diffusion for patterning surfaces, the diffusion is also a source of the loss of resolution.^[36] ESL in its present form could profit from using low-diffusion inks^[37] and/or nanoparticles^[38] to increase the resolution, up to now 140 nm features in gold after wet-chemical etching, to the sub-100 nm range and has more potential in the generation of multiple SAMs (Fig. 2.7b)^[39] and chemical gradients (Fig. 2.7c).^[40] Additionally, the resulting molecular patterns were functional as suitable templates for site-specific adsorption of nanoparticles on surfaces.^[41] Extensions of this technique include substituting flat stamps for stamps with a topographical surface in order to generate more complex patterns.^[33b]



Figure 2.7 (a) Schematic representation of edge-spreading lithography.^[33a] (b) Lateral force microscopy (LFM) image of concentric rings after consecutive printing of three inks with different functionalities (scale bar: 500 nm).^[39] (c) LFM image of a gradient pattern, consisting of two SAMs of different functionalities.^[40]

Finally, the last strategy in this class for patterning nanostructures relies on the generation of local disorders in SAMs on topographical patterns (Fig. 2.8a).^[42] Generally, SAMs are excellent etch masks to protect surfaces against wet-chemical etchants owing to their high surface coverage, high order and low density of defects (as low as ~5 defects/mm²),^[43] especially in combination with defect-tolerant and

directional wet-etch systems.^[44] The concept of this technique is to introduce step edges in the substrate by means of lithography in order to control the positions of transition zones with lower coverage and order during SAM formation. These zones are found to provide nucleation sites for the preferential etching and deposition of materials, allowing respectively the generation of features as small as 50 nm (Fig. 2.8b) in a range of materials (gold, silver, SiO₂/silicon) and the crystallization of calcite crystals from a CaCl₂ solution.^[42a]



Figure 2.8 (a) Schematic representation for the generation of local disorder in alkanethiol monolayers on topographical substrates. (b) SEM image of 50 nm trenches by selective etching silver-on-silver substrates (i.e., $M_1 = M_2 = Ag$).^[42a]

Additionally, these zones are also more susceptible to exchange with molecules in solution, resulting in pattern reversal during wet-chemical etching. The incorporation of a thin layer of titanium in between the two metal layers is another strategy to generate a transition zone to facilitate the preferential electrodeposition of copper into nanowires with lateral dimensions as small as 70 nm.^[45]

2.2.1.2 Additional strategies to size reduction

A number of techniques are capable of sub-100 nm patterning by application of size reduction strategies. A summation of the size reduction techniques available is given in Table 2.2.

Technique	Patterning principle	Resolution*
maskless photolithography	constructive and destructive interference of light using the resist pattern as the optical element	50 nm
lithographic molding	anisotropic etching of <100> silicon	40 nm
replica molding	molding against elastomeric stamps under isotropic deformation (compression, stretching, bending or a combination of these deformations)	30 nm
sectioning with a microtome	sectioning thin metal films in an encapsulating matrix of a polymer	20 nm
superlattice nanowire pattern transfer	transfer of metal nanowires using a superlattice as a supporting template	8 nm
nanopantography	focusing of a broad-area ion beam using arrays of circular features in a metal/insulator layer as microlenses	10 nm

Table 2.2 List of additional s	strategies to size reduction
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* The resolution values in this table represent the smallest feature dimensions that have been shown to date with the respective technique, but do (in most cases) not represent the ultimate limit.

The first in line is maskless photolithography,^[46] a technique that exploits the direct interaction of resist patterns upon flood-illumination with incoherent and polychromatic light. This technique is the maskless form of near-field phase-shifting photolithography (Section 2.2.1.1). As a substitute, the resist patterns serve as optical elements to phase-shift and direct the incident light into the resist during exposure (Fig. 2.9a). As the resist pattern is the optical element, the final patterns after resist development strictly depend on the contours of the initial resist patterns.^[46] The same resolution improvements for near-field phase-shifting photolithography also apply for this technique.

The procedure for lithographic molding^[47] starts with the fabrication of a grating by anisotropic etching of <100> silicon in an aqueous KOH solution. As the procedure relies on the anisotropic etching along the <111> crystal plane of the silicon, the resulting template combines an ultra-smooth surface with excellent feature dimension control over large areas. The triangular profile with its sharp radius of curvature at the tip of the profile (~40 nm)^[47] is practical in the molding of elastomeric stamps for printing^[47,48] and for the application as hard molds in nanoimprint lithography.^[49] Fig. 2.9b is illustrative for the feasibility of nanocontact printing using the stamps to print high-resolution molecular patterns on surfaces with high reproducibility, in this case of a fourth generation poly(amidoamine) dendrimer (G4-PAMAM) on silicon. The accuracy of printing with high-molecular-weight inks at these dimensions demonstrates that only the sharp tips of the composite stamp come into contact with the surface.



Figure 2.9 (a) SEM images of the resist pattern before and after flood-exposure and development in maskless photolithography, with the drawings illustrating the interaction of the resist with the light.^[46] (b) AFM images of 42 nm-wide dendrimer lines on a silicon surface after microcontact printing, including a high-resolution image and average cross-section.^[48] (c) Series of AFM images of the master structure (top) and replica in PU (bottom) after one cycle of deformation by bending.^[50b]

Replica molding is the basis of any soft-lithographic technique and comprises the solidifying of a liquid precursor against hard molds in high accuracy over large areas (see Section 2.3.2). The elasticity of PDMS facilitates the manipulation of the feature dimensions by mechanical deformation of the stamp (compression, stretching, bending or a combination of these deformations). The concept is to perform several cycles consisting of two basic molding steps: (1) replica molding of UV-curable polyurethane (PU) against PDMS stamps under deformation, (2) replica molding of PDMS against

the PU replica.^[50] The ability to make positive as well as negative polymeric replicas and to reduce the lateral feature dimensions adds a lot of flexibility to this potential technique. Fig. 2.9c illustrates the effect on the lateral feature dimensions after one cycle of replica molding under mechanical deformation, resulting in lateral feature dimensions as small as 30 nm.^[50b] Additionally, the isotropic deformation of the PDMS mold also permits the fabrication of structures with gradients in lateral feature dimensions and of complex structures, such as diffraction gratings on planar and bent surfaces and features on hemispherical objects. The principal challenge with exploiting uniform deformations is to attain a high level of uniformity at the sub-100 nm scale.

The next two techniques take advantage of deposition techniques that are capable of growing thin films over large areas by translating the resulting films into structures with lateral dimensions corresponding to the thickness of the film (see Fig. 2.10). One strategy to expose the cross-section of a metal film is to incorporate the thin film into a soft encapsulating matrix and subsequent sectioning the matrix with a microtome (Fig. 2.10a).^[51] The last step is the most challenging aspect of this technique in order to minimize the delamination at the matrix-metal interface. The second strategy is more generally applicable and results in ultrahigh-density arrays of metal and/or semiconductor nanowires on any surface. The technique, superlattice nanowire pattern transfer (SNAP),^[52] owes its name from exploiting a superlattice of alternating materials grown by molecular beam epitaxy (MBE) in order to transfer metal nanowires onto the surface of a substrate (Fig. 2.10b). The technological advantage is the direct control over the spacing and the lateral dimensions of the resulting nanowires by the thicknesses of the alternating layers. Successful implementation of these arrays in nanofabrication has been shown in the fabrication of nanoimprint molds.^[53a] The highest density to date consists of an array of 20 platinum nanowires, 8 nm in diameter with a spacing of 16 nm. The nanowires have high aspect ratios (up to 10⁶) and, in addition, it is possible to perform the transfer process multiple times to generate simple circuits with nanowire junction densities of over 10¹¹ junctions cm². Promising nanomechanical devices incorporating such high-density arrays of nanowires include high-frequency resonators^[52] and high-performance nanowire fieldeffect transistors (FETs).^[53c]



Figure 2.10 (a) Schematic representation of sectioning thin films with a microtome and a SEM image of 50 nm-wide gold features in a continuous epoxy matrix.^[51] (b) Schematic representation of the SNAP technique^[52] and a SEM image of an inverter with silicon nanowire source-drain regions in connection with large silicon bars (bottom inset) that contain the metal –wire contacts (top inset) (scale bars: 500 nm).^[53c]

The most recent technique in this section (nanopantography) exploits arrays of circular features in a metal/insulator multilayer as ion-focussing microlenses to write nanopatterns in parallel into the underlying substrate by applying appropriate voltages to the lens electrodes during exposure of a broad-area ion beam.^[54] Using this technique, the ion beam focuses in spots that are about 100 times smaller than the lateral dimension of the microlenses.^[54a] As the lens array is part of the substrate, nanopantography is not susceptible to misalignment errors due to vibrations or thermal expansion. Strategies to generate a range of patterns include continuous tilting the substrate during exposure to the beam and addressing individual microlenses by patterning of the top metal layer for isolation.^[54b]

2.2.2 Pattern formation by writing

Writing is the main process in the semiconductor industry for generating arbitrary patterns on surfaces and in most cases involves serial techniques that require large capital investments. Examples of serial techniques include ion-beam and electron-beam lithography.^[9,10] Unconventional techniques, also denoted by the general term of scanning-probe lithography (SPL),^[12] employ a rigid stylus for generating high-resolution structures by means of material modification, deposition, and removal of material in a mechanical, physical, optical, electrical, or chemical fashion. SPL in general has several strengths, including nanoscale spatial resolution (on the order of 10 nm), excellent reproducibility, low-cost facilities, basic operating procedures and the capability of real-time imaging of the patterns for accurate alignment at the nanometer level.^[12b] For most of the SPL techniques, however, the main challenge remains the intrinsic low writing speed. Among the best known examples of parallel writing operations is the Millipede system of IBM to read-and-write in thin polymer layers for data-storage.^[55]

This section will cover dip-pen nanolithography (DPN)^[56] as the most prevalent technique for add-on writing by means of probes in parallel over the surface to deliver chemical reagents in nanoscopic regions on the substrate (Fig. 2.11).



Figure 2.11 (a) Schematic representation of the DPN process of writing structures of molecules by means of scanning an ink-containing atomic force microscopy (AFM) probe across the surface. The water meniscus is responsible for the molecular transport to surfaces.^[56a] (b) LFM image and scaling analysis for patterning dots (by holding the probe in stationary contact with the surface) of octadecanethiol (ODT) on gold.^[63b]

DPN is possibly the only lithographic technique that offers high-resolution and registration with direct-writing capabilities and is accessible to anyone that can use the

AFM. This is the reason for the fast development of DPN after its introduction in 1999,^[56a] as shown by the number of contributions within the first 3 years.^[57] During this time, DPN was found to be compatible with small organic molecules,^[56,58] organic polymers,^[59] biological molecules (DNA^[60a] and collagen^[60b]), colloidal particles^[61] and metal ions^[62] for patterning surfaces ranging from metals to insulators and on top of functional monolayers. Of high importance for future biological device applications (e.g., detection in biomolecular assays) and studies on hierarchical assembly processes of biological systems was the experimental proof that writing by DPN preserves the structure, functionality and activity of biomolecules on surfaces.[60b] The probesubstrate ink transport process is a complex process influenced by a range of parameters, including temperature, ambient humidity, water solubility of the ink, condition and chemical composition of the surface and the ink, distribution and mobility of the ink on the probe, and the shape of the probe. Understanding of these parameters is important and would facilitate efforts for developing new ink-substrate combinations and inking systems, for improvement of the ultimate resolution, and for the fabrication of custom probes. Several systematic studies illustrate that the dependence of the deposition rates on temperature and ambient humidity originate from the water solubility of the ink.^[63,64] Additionally, the correlation of time with deposition rate, as shown in Fig. 2.11b for ODT on gold, is a characteristic feature applicable to a large range of different molecules (only with different deposition rate constants) and is consistent with the probe serving as a source of constant ink flux.^[65] As is the case for SAMs made from solution, the resulting monolayers by DPN are of high quality and can function as excellent etch resists in the etching of different metals and semiconductors.[66]

DPN offers several exclusive features of industrial importance in terms of site selective exchange^[58b] to fabricate complex nanostructures and also erasure to allow elimination or repair of defective nanostructures (Fig. 2.12).^[67] DPN also allows the direct patterning of metal nanostructures by electrochemical DPN (E-DPN),^[62a] in which a conductive probe serves as the electrode with the water meniscus as a nanoscale electrochemical cell to deposit metals (Pt, Au, Ag, Cu, Pd) by means of electrochemical reduction of metal ions.



Figure 2.12 LFM images showing the selective elimination of 16-mercaptohexadecanoic acid (MHA) dots by conductive AFM. The initial array consists of 500 nm in diameter dots of MHA in a background of ODT. Successive selective electrochemical elimination of MHA in two areas on the surface is shown by scanning a conductive probe at a potential of -5 V containing ODT over the specific areas on the surface.^[67]

Scientific research has put a lot of effort into shifting the paradigm of DPN in terms of the low intrinsic writing speeds of a few micrometers per second by high-throughput solutions. Starting from the implementation of passive probe arrays of 8,^[56b] 26 (Fig. 2.13 a),^[68a] and 55000 probes (Fig. 2.13c)^[69] by micromachining technology,^[70,71a] new types of elastomeric probes^[71] to active parallel-probe arrays by thermomechanical probe actuation and microfluidic ink delivery systems,^[72] the scientific community has come with a lot of interesting solutions toward high-throughput writing. NanoInk^[73] is a company that takes DPN writing to the commercial level by providing several potential production tools (see Fig. 2.13).



Figure 2.13 SEM images of different technological advancements in DPN.^[73] (a) Passive multi-probe array, consisting of 24 identical writing probes with two flanking probes of the same length - the left one is the reader probe, the right one is allows for better leveling of the array. (b) ActivePen[™] option for addressing individual probes in one multi-probe array, include features as multilayer writing with separate probes and selective parallel writing. (c) 2D nano PrintArray[™] consisting of 55000 probes across 1 cm². (d) Inkwell[™] arrays for selective inking of the probes.

Polymer pen lithography (PPL)^[74] is a very recent technique that merges feature size control and registration capabilities of DPN with large-area capability of contact printing by using soft elastomeric probe arrays (up to 11 million probes) and allows lateral feature dimensions ranging from 90 nm to hundreds of micrometers by changing the force and contact time.

2.2.3 Pattern formation by self-assembly

Self-assembly is the spontaneous organization of two (or more) constituents into larger structures in two or three dimensions by noncovalent forces and is ubiquitous in nature.^[75] The concept of self-assembly is that the final structure is close to or at thermodynamic equilibrium owing to the dynamic and reversible interactions of the self-assembling constituents. The characteristics of these constituents predetermine the final structure, therefore control over the self-assembly process into the final structure is within reach by tailoring the properties of the constituents in terms of surface topography and functionality. From a technological perspective, self-assembly is low-cost, fast, self-healing for irregularities and scalable, and several of the selfassembling systems have large potential in nanofabrication.

2.2.3.1 Block copolymer lithography

Block copolymers (BCPs) are a special class of polymers with two (or more) polymer chains bound together through one covalent bond.^[76] Generally, most combinations of polymers are immiscible for entropic reasons and have a strong tendency to phase separate.^[77] Owing to the constraints by the covalent bond, the different polymer chains in BCPs can only intermolecularly phase separate to a distance compatible with the size of the polymer chains (i.e., ~10-100 nm)^[78] resulting in the self-assembly of a variety of high-resolution periodic domains at equilibrium. For coil-coil diblock copolymers, the molecular weight, volume fraction of the components and the degree of polymer incompatibility determine the domain structure in the bulk state (Fig. 2.14).



Figure 2.14 Generic phase diagram for a diblock (A-B) copolymer illustrating several of the possible domain structures (lamellar, bicontinuous gyroid, hexagonal cylindrical, cubic), as a function of the volume fraction and degree of polymer incompatibility (in terms of the Flory-Huggins parameter χ).

Two additional factors that affect the domain structure of resulting patterns in thin films of diblock copolymers are the polymer-air and polymer-substrate interactions and the film thickness relative to the natural domain period of the bulk due to the 2D confinement in thin films. Systematic control over the domain structure and lateral dimensions by these parameters illustrates the potential of diblock copolymers for nanofabrication with respect to CD control.^[79]

Two intrinsic difficulties concerning the application of BCPs for large-scale nanofabrication are the inherent short-range lateral order and the unfavorable domain orientation with respect to the substrate. Strategies for long-range lateral and orientational order are found in film thickness control,^[80] interfacial interaction control,^[81] and application of external fields such as electrical fields.^[82] These strategies, in combination with conventional lithography, allow the precise alignment of the overlaying BCP patterns on substrates and spatial control of the domain patterns.^[83] Additionally, the incorporation of multiple functionalities into the components of the BCP facilitates the spatial control by large-scale patterning. The most interesting example comprises the incorporation of high-resolution poly(4-hydroxystyrene) (PHS) and poly(α -methylstyrene) (P α MS) into one BCP and multi-step patterning with DUV lithography.^[84]

BCP nanostructures serve in most applications as templates in the fabrication of functional nanostructures. The primary means of achieving these nanoporous templates is by selective removal of one of the components of the BCP by chemical etching,^[85] selective degradation,^[85] or chemical modification (e.g., crosslinking^[86]). Owing to developments in BCP chemistry, novel high-resolution Fe-containing^[87] and Si-containing^[88] BCPs are available with larger etching contrasts within the BCP. As shown in Fig. 2.15, the extremely high feature density of BCP templates (up to ~10¹¹ holes·cm⁻²)^[85] makes BCP lithography useful for the fabrication of a variety of functional nanostructures, including magnetic storage media,^[87a] PDMS stamps,^[89] semiconductor capacitors,^[90] quantum dots (Fig. 2.15a),^[91] nanoparticles,^[92] nanowires (Fig. 2.15b),^[93] nanotubes,^[94] nanopillars,^[95] nanoporous materials^[85,96] and in particular photonic crystals.^[97] As shown in Figs. 2.14 and 2.15c/d, BCPs have the capability to form 1D (lamellar), 2D (hexagonal cylindrical), and 3D (bicontinuous gyroid) photonic crystals and are therefore extremely useful as photonic band gap (PBG) materials.^[98]



Figure 2.15 Several examples of BCP lithography in the fabrication of different functional nanostructures: (a) SEM image of a dense array of GaAs quantum dots with a mean lateral dimension of 23 nm (\pm 3 nm) (scale bar: 100 nm),^[91] (b) cross-sectional SEM image of a dense array of Co nanowires within a block copolymer matrix,^[93a] (c/d) SEM images of photonic band gap materials, a lamellar structure of a poly(styrene-b-isoprene) film (molecular weight of 390 kDa, symmetric block copolymer, including 40% of homopolymer) with a periodicity of 140 nm^[99a] and bicontinuous gyroid structure of another poly(styrene-b-isoprene) film (molecular weight of 300-450 kDa and 38 % of poly(styrene)) after selective UV etching of isoprene, resulting in freestanding styrene networks.^[99b] (e) Series of non-regular structures: 90° bends, arcs, and T-junctions.^[79a]
Preferential sequestering of optically transparent nanoparticles (e.g., CdSe nanoparticles) into one of the BCP domains can substantially increase the intrinsic small dielectric contrast within the BCP, resulting in larger band gaps.^[100] The practical short wavelength limit for polymer PBG materials is around 300 nm due to the strong absorption in the ultraviolet regime. The ease of tailoring the domain size (on the order of 10-50 nm) of BCP domain patterns to $\lambda/4n$ (with n the refractive index of the domain material) by the molecular weight as well as by addition of homopolymers or selective non-volatile solvents allows the practical extension of BCP PBG materials to the visible region.^[101]

BCPs are also attractive for general nanolithography because of the potential improvements in CD control and line-edge roughness (LER) of the templates. The latter is in theory proportional to the degree of polymer incompatibility within the BCP and therefore in principal allows tailoring of the LER by polymer chemistry.^[102] Groundbreaking work in BCP lithography was shown recently in the fabrication of non-regular and complex structures (see Fig. 2.15e)^[103] by blending pure block copolymers with homopolymers. The combination of resulting flexibility in pattern generation with precise CD control and low LER of these polymer templates illustrates that BCP lithography has the potential to become part of the next-generation lithographic techniques.^[79a]

2.2.3.2 Physical self-organization of polymers

This technique exemplifies the potential of CFL (Section 2.3.2) and concerns the capillary-driven self-organization of polymers into multilevel polymeric structures during the annealing of existing polymer microstructures in the presence of elastomeric stamps.^[104] This strategy overcomes the requirement of surface modification procedures, as in the case of (anisotropic) spinodal dewetting.^[105] Fig. 2.16a illustrates the time-line during the formation of the most interesting structures by capillary dynamics. By rotating the stamp 90° with respect to the polymer template, holes form in the void spaces in between the elastomeric mold and the polymeric pattern owing to the formation of two menisci within a certain time interval.



Figure 2.16 (a) AFM images of transient polymeric structures as a function of annealing time (130 °C) using molds with a 1 μ m line-and-space pattern on top of a 700 nm line-and-space polymeric structure (height scale 300 nm).^[104a] (b) AFM image of the resulting structure in case of using molds with a 3 μ m line-and-space pattern (height scale 500 nm).^[104a] (c) Schematic illustration of the assembly process for generating aggregates of nanoparticles under physical confinement of the polymeric template, using the interplay of several forces acting on the nanoparticles during the assembly process: capillary force (F_c), gravitational force (F_g), and electrostatic force (F_c).^[107] (d) SEM image of two linear chains of 150 nm PS particles on a polymeric template made by optical near-field phase-shifting lithography (see Section 2.2.1.1).^[107]

As shown by the transient structures in Fig. 2.16a, the second meniscus starts to form at the time the elastomeric mold contacts the surface of the substrate (in this particular case, at 40 min) and depending on the lateral dimensions of the mold with respect to the half-width of the polymer meniscus, capillary dynamics can result in one or two rows of small holes (Figs. 2.16a/b). The lateral dimensions of these holes are about one third of the channel width of the mold and monotonically decrease at higher annealing temperatures. By performing this procedure on silicon-on-insulator, one could generate a mask for the fabrication of 1D photonic waveguides in silicon by successive etching of the substrate.^[106] Additionally, these multilevel polymeric structures are useful to act as physical templates to direct the assembly of single nanoparticles (Figs. 2.16c/d)^[107] and the multilayer assembly of functional polymers.^[108]

2.2.3.3. Nanosphere lithography

The easy access of large quantities of monodisperse colloids has been a stimulant for exploiting the self-assembly of colloidal aggregates^[109] as building blocks for the generation of systems with complexities that are useful for photonic applications.^[110,111]

Nanosphere lithography (NSL)^[34] exploits the self-assembly of colloids in the costeffective generation of mono- and bilayer colloidal crystal masks for various postprocessing steps, including metal deposition,^[34] SAM formation^[112] and reactive ion etching.^[113] After removal of the colloids by lift-off, large-area arrays of functional nanostructures are left on the surface of the substrate. For NSL, most contributions exploit the resulting metal nanostructure arrays after metal deposition as templates in the transformation to metal nanoparticles by thermal annealing,^[114] as catalysts in the generation of silicon nanopillars by molecular beam epitaxy,^[115] carbon nanofibers (CNFs)^[116] and ZnO nanopillars^[117] by chemical vapor deposition (CVD), as etch masks in the fabrication of nanoimprint molds^[118] and 2D photonic crystal patterns^[119] by reactive ion etching, and as wet-etch masks in the generation of silicon nanopillars with different orientations by successive anisotropic silicon etching.^[120]



Figure 2.17 Several examples of NSL in the fabrication of different functional nanostructures: (a) SEM image of 2D photonic crystal patterns (scale bar: 250 nm),^[119] (b) cross-sectional SEM image of silicon nanowires (SiNWs) after etching in HF using the silver pattern as a catalyst (scale bar: 1 μ m).^[120] (c) Effect of thermal annealing of colloidal poly(styrene) particles by microwave pulse heating in water/ethanol/acetone (3:1:1) on the dimensions of the aperture in between the particles and SEM image of the resulting array of Co magnetic particles by deposition with lateral dimensions down to 30 nm (scale bar: 200 nm).^[121b]

Control over the lateral dimensions and the periodicity of the metal structure arrays is feasible by O_2 plasma,^[113] thermal annealing,^[121] or ion polishing^[122] and by

adjusting the diameter of the colloidal particles, respectively. According to computer simulations,^[121] it is within the capability of NSL to generate several different feature shapes by adjusting the relative position of the evaporation source during deposition. However, with the restriction that clogging of the nano-apertures limits the total time accessible for metal deposition. Although NSL offers large-area patterning in a parallel fashion, on the macroscopic scale this technique remains susceptible to defects, distortions and grain boundaries that form during the self-assembly of colloidal particles on surfaces.

2.2.4 Pattern formation by anodic oxidation (anodization)

The electrochemistry of elements^[123] offers possibilities for the generation of nanoscale patterns in a large set of materials. Since the growing demand for high-resolution templates to fabricate magnetic, electronic and photonic devices, the anodic oxidation (or anodization) of thin aluminum films in acidic solutions^[124] has received considerable interest during the last 25 years owing to the formation of high-density arrays (up to 10¹¹ holes·cm⁻²) of cylindrical nanopores in a triangular lattice in anodic aluminum oxide (AAO) films (Fig. 2.18).^[125]



Figure 2.18 (a) Configuration diagram of porous AAO membranes. The hexagonal array of cells contains a cylindrical pore in the center of each cell. These pores are perpendicular to the surface of the aluminum film and extend down to a non-porous aluminum oxide barrier layer. The pore diameter is between one third and half of the lateral dimensions of the cell. Pore widening by etching in acidic solutions is optional to fine-tune the lateral dimensions of the pore.^[124] (b) SEM images illustrating the effect of pre-texturing by means of a hard SiC mold on the formation of defect-free AOO membranes over surface areas on the order of millimeters (scale bar: $1 \mu m$).^[127b]

The balance during the anodization process of the AAO membranes between oxide dissolution at the electrolyte/oxide interface and oxide formation at the oxide/ aluminum interface is crucial for steady-state pore propagation into the aluminum film.^[126] Fundamental research on the pore formation suggests that repulsive forces at the oxide/aluminum interface between adjacent pores promote the self-organization of the pores into hexagonal arrays.^[126] As the regularity of the pores and pore arrangement increase with time, the resulting membranes in a conventional one-step anodization process are far from perfect. A two-step anodization process,^[127a] annealing and electropolishing of the aluminum layer, and pre-texturing the aluminum film by imprinting with hard molds^[127b] are measures taken to overcome the irregularities of the conventional anodization process for the generation of defect-free AAO membranes over large areas (Fig. 2.18b).

Precise control of the pore dimensions in the range of 10-200 nm (with corresponding cell dimensions of 25-420 nm) is feasible by adjusting the electrolyte composition (sulfuric acid for small-pore membranes (10-30 nm), oxalic acid for medium-pore membranes (30-80 nm), and phosphoric acid for large-pore membranes (> 80 nm)), anodization voltage and temperature.^[124] The extent of this control and the regularity of the patterns illustrate the applicability of the AAO membranes as excellent templates in the fabrication of arrays of one-dimensional nanostructures, such as nanowire and nanotube arrays of metals,^[128] semiconductors,^[129] carbon,^[130] and replication into metal membranes,^[131] PDMS stamps^[132] and imprint molds.^[133]

The development of ultra-thin AAO membranes (with thicknesses of about several hundreds of nanometers) overcomes the difficulties of the conventional AAO membranes to fabricate zero-dimensional nanostructures (e.g., nanoparticles and nanoholes) directly on the substrate due to the oxide barrier layer and aluminum backside. Owing to the small aspect ratio of the pores, these (freestanding) membranes^[134] are excellent deposition and etch masks for the fabrication of semiconductor, oxide and metal nanoparticle^[135] and nanohole^[136] arrays, respectively, and structures from processes that initiate from nanoparticle arrays onto any surface (e.g., nanotubes^[137a] and nanopore arrays^[137b]).

Fig. 2.19 illustrates the general applicability of AAO templates. Several contributions integrate the AAO templates in the fabrication of functional structures

by conventional lithography,^[138] e.g., by the FIB sputtering of AAO templates for the selective closure of part of the nanochannels.^[138b] As in the case of BCP lithography (Section 2.2.3.1), the ultra-high density of the AAO templates opens up the possibility to fabricate photonic crystal devices with this technique (Fig. 2.19c).^[139]



Figure 2.19 Several examples of AAO membranes in the fabrication of different functional nanostructures: (a) cross-sectional SEM image of a AgNW/a-CNT heterojunction array within the AAO membrane,^[140] (b) SEM image of uniform metal nanotube arrays after dissolution of the AAO template and the metal core,^[128c] (c) SEM images of a GaAs substrate after dry etching using the AAO template as the etch mask (scale bar: 1 μ m), and the resulting 2D photonic crystal waveguide after writing of the line-defect by FIB (scale bar: 500 nm).^[139]

The tunable structural parameters of the AAO membranes, the cost-effective large area patterning capability and the general applicability in the fabrication of different types of nanostructures for a range of materials makes this also a potential technique for nanofabrication.

2.2.5 Summary

The current set of techniques in this section is capable of structuring a wide range of materials at the sub-100 nm scale in a parallel and cost-effective fashion for many potential applications. The degree of development of these techniques differs to a large extent and this is essentially dependent on the potential of generating arbitrary structures. On the one side there is the set of edge lithography techniques, in principle incapable of generating arbitrary features. The high-resolution features are, however, very useful in the cost-effective fabrication of nanowires and nanochannels. On the

other side there are DPN and (to a less extent) BCP lithography, capable of generating arbitrary structures. A lot of research is done for these particular techniques to meet the stringent requirements of IC manufacturing in terms of feature perfection (CD and defect control), registration (overlay) and integration into complete circuit layouts. For this reason, the convergence of several unconventional techniques with or without the interference of conventional lithography is foreseen in the fabrication of complete IC layouts.

2.3 Replication of patterns

Pattern replication is the cost-effective reproduction of structural information present on the surface of a master into a different material, and with high fidelity. The generation of high-resolution masters with serial direct-write technologies, e.g., electron- and ion beam lithography, is only a practical protocol for high-throughput IC manufacturing in combination with a cost-effective and high-throughput replication technique. This section presents a selection of the most powerful techniques in unconventional nanofabrication for replicating high-resolution (< 100 nm) features of a master.

2.3.1 Pattern replication by imprinting

Nanoimprint lithography (NIL) is the revelation of the 90s for high-throughput patterning of polymers^[141] owing to its low capital costs and potential in high-resolution patterning, as shown by the demonstration of 10 nm feature reproduction with a parallel-plate press soon after its inception.^[142] Since 2003, NIL is part of the emerging technologies of the ITRS for future IC manufacturing at the 32-nm node and beyond (Fig. 2.2). The concept of thermal NIL is the compression molding of a thin polymer film cast on a substrate with a hard mold at temperatures above the glass-transition temperature (T_g), in order to force the polymer to conform to the nanoscale surface-relief features of the mold (Fig. 2.20). For most applications, anisotropic O₂ plasma etching is essential to remove the polymer residual layer and therefore enable successive processing of the substrate. A lot of fundamental research can be found in literature addressing the critical and industrially relevant process issues of thermal NIL due to the mechanical nature of the process (Table 2.3).



Figure 2.20 (a) Schematic representation of thermal nanoimprint lithography. (b, c) SEM images of a hard mold with a high-density array of pillars with lateral dimensions of 10 nm and the corresponding imprint in poly(methylmethacrylate) (PMMA).^[142]

As shown in Table 2.3, considerations for selecting mold materials include hardness (for preservation of the nanoscale features), durability (for high-throughput), thermal expansion coefficient (for accurate registration and pattern fidelity during imprinting at high temperatures), and compatibility with IC manufacturing processes (for production of the molds).

Molding/	Processing cycle time: fast heating systems with negligible resist cooling times by
demolding	application of heat to the stamp ^[143] or of a single laser pulse, ^[144] room temperature
	systems, ^[145,146,147] and pressure control, ^[148,*]
	demolding implications ^[149] and incorporation of step-and-repeat systems
	presence of residual layer ^[150a]
	multilevel processing (alignment) ^[151]
	contamination control: intermediate polymer stamps (IPS) ^[152]
	real-time monitoring systems ^[153] and feature analysis ^[150b]
Resist	visco-elastic properties ^[154]
	etch resistance and selectivity
	thermal stability during thermal imprinting and mechanical stability during
	demolding ^[155,156]
Mold	hardness, durability and surface energy control: anti-adhesive coatings ^[157,158] and novel
	anti-adhesive mold materials ^[147,159,160]
	mold design rules for optimal filling ^[161]
	implications by difference in thermal expansion coefficient between substrate/mold ^[162]
Transfer	pattern transfer fidelity of plasma etching ^[163]

Table 2.3 List of process issues in thermal imprinting

* Several efficient strategies to reduce the imprint pressure in nanoimprint lithography are part of soft lithography (see Section 2.3.2).

Several materials that are eligible for thermal NIL are Si, SiO₂, SiC, silicon nitride, and metals. The stringent material aspects for molds, however, do not exclude the use of composite molds consisting of flexible backplanes supporting thin molds to combine global flexibility with local hardness of the nanoscale features^[164] and the use of transparent molds (quartz, silica) for room-temperature NIL of UV-curable resists (UV-NIL).^[165]

Generally, the total surface area of high-resolution molds is known to cause strong adhesion of the resist at the mold during imprinting and results in severe damage to the resist patterns. Solutions to reduce this adhesion and increase the mold durability are (1) incorporation of internal release agents in resist formulations, (2) application of low-surface energy coatings (in thin film^[157] or monolayer^[158] of surfactants) to the mold, and (3) fabrication of a mold from materials with intrinsic low surface energy.^[147,159,160]

The ideal resist formulations for NIL have several characteristic properties for successful pattern transfer, including good deformability at the imprint conditions (pressure, temperature, and viscosity), sufficient mechanical strength and excellent mold-releasing properties to maintain the structural integrity of the polymeric features during demolding, and superior etching properties for successive pattern transfer into the substrate with high fidelity. Practically, imprint temperatures that are 70-90 °C above $T_{\rm g}$ of the thermoplastic polymer are chosen to ensure sufficient polymer flow.^[166] For reasons of high-throughput, current research focuses on approaches that lower the total processing time of imprinting and includes the development of new high-resolution resist formulations (other than the standard PMMA resist) that are more suitable for nanoimprint applications. The low structural integrity of resist features from resist formulations with a low T_g infer that thermally curable liquid resists are a good option for low-pressure imprinting that result in features with good structural integrity owing to thermal crosslinking. A very recent thermally curable resist^[167] makes use of the hydrosilylation chemistry of siloxane polymers and consists of four basic components (PDMS precursor, crosslinker, catalyst and inhibitor). The inhibitor keeps the catalyst inactive and in combination with the low viscosity of the resist allows low-pressure (0.2-0.6 MPa) imprinting at room temperature down to a resolution of 70 nm. Two additional features of this resist is the very fast curing time

of 10 s upon heating the system to 120 °C and the ability to tune the modulus by the molecular weight of the PDMS precursor and the reactive groups in the crosslinker. Siloxane block or graft copolymers^[168] also form a potential class of (in this case) thermoplastic polymers for NIL (conditions: 170 °C, 1.3 MPa) by providing dual surface properties owing to the phase separation at annealing (Section 2.2.3.1). This dual surface property results in superior mold-release performance without delamination from the substrate (impossible for homopolymer resists). For certain (thermo-sensitive) applications, UV-curable epoxysilicone resists^[147] have recently become available for room temperature imprinting and overcome the oxygen sensitivity and large shrinking problems during curing of conventional acrylic UV-curable resists. Owing to the high silicon content, the three new resist formulations possess excellent etch resistance to facilitate successive pattern transfer into the substrate by plasma processing. A last potential inorganic resist formulation for room temperature imprinting is one of several spin-on-glass types, hydrogen silsesquioxane (HSQ).^[145]

Real-time imprint monitoring systems (RIMS)^[153] are under development that allow ultimate control over the imprinting process (temperature, pressure, time). Fig. 2.21 illustrates the capability of RIMS for monitoring imprint processes on the nanosecond scale.



Figure 2.21 (a) Schematic of the measurement set-up for RIMS. The laser beam is incident on the mold and an ultrafast photodetector is collecting the negative first order diffraction signal for analysis by an oscilloscope. (b) Typical curve illustrating the intensity change as a function of time, with the blue arrows indicating the start and end point of the imprint.^[153]

The condition for short processing times in IC manufacturing has driven current research activities in thermal NIL to step-and-repeat patterning schemes that have a

strong similarity with the stepper system in conventional photolithography. These schemes concern the lateral confinement of heat transfer by application of a single laser pulse with a suitable wavelength as the heat source in order to melt the surface of polymeric^[143a] as well as hard materials (e.g., silicon)^[143b] in less than 1 ns, allowing ultrafast imprinting times of about ~200 ns (Fig. 2.21b). Additional advantages of these heating sources are the negligible resist cooling time and reduction of thermal expansion effects.

A very illustrative procedure in literature that has close operational similarities to microtransfer molding^[169] and that aims for enhancing the patterning capability is reverse NIL^[170] of true 3D patterns (Fig. 2.22). For reverse nanoimprint lithography, the successful transfer depends on the balance of the surface energies between the mold and the substrate and can result in two different reversal imprint modes.^[171] Analogs in GaAs of the 3D structure in Fig. 2.22 form potential photonic structures to control the emission of light.^[172]



Figure 2.22 Schematic representations of multi-level patterning by reverse nanoimprint lithography and a representative SEM image of a true 3D periodic structures consisting of a stack of three consecutive polymer structures by building up in a layer-by-layer fashion.^[170a]

Although research and development still need to address some of the important issues for integration in IC manufacturing, Fig. 2.23 illustrates that NIL has already found numerous applications. These applications include electronics (e.g., hybrid plastic electronics,^[173a] organic thin-film transistors,^[174] silicon nanoelectronic devices^[175]), photonics (e.g., organic near-IR lasers,^[176] nonlinear optical polymeric nanostructures,^[177] high-resolution organic light-emitting diodes (OLEDs)^[178], and high-performance polarizers^[179]), magnetics (e.g., high-density magnetic media^[180]), and biological applications (e.g., manipulation of DNA in nanofluidic channels,^[181] nanoscale protein patterning^[182]).



Figure 2.23 Several examples of photonic nanostructures by application of nanoimprint lithography: (a) SEM image of a 10 µm-long 2D photonic crystal waveguide in SOI and a transmission spectrum for quasi-TE laser light,^[183] (b) SEM image of a defect-free 2D photonic crystal in a nonlinear optical polymer,^[177b] (c) photographs of OLEDs with (left image) and without (right image) the presence of the photonic crystal.^[178]

2.3.2 Pattern replication by conformal contact (soft lithography)

Soft lithography is a collective name for a set of non-photolithographic techniques that rely on the use of elastomeric stamps as the key element for pattern transfer.^[30] Soft lithography offers immediate advantages over conventional lithographic techniques in the structuring of materials that are incompatible with corrosive etchants, high-energy radiation and high temperatures and in the structuring of fragile or non-planar substrates by large-area conformal contact with substrates without resorting to high pressures. Several of these advantages ensue from the properties of PDMS as the elastomeric stamp. The elasticity and low surface energy of elastomeric PDMS stamps (~21.6·10⁻³ J·m⁻²)^[184] allow conformal contact over large areas and facilitate easy release from the substrate after patterning, respectively. PDMS is a durable elastomer, chemically inert to most materials, resistant to many organic solvents,^[185] optically transparent down to ~280 nm, and moreover allows the

modification of its surface by SAMs to provide a wide range of surface energies.^[186] However, the low tensile modulus of common PDMS (Dow Corning, Sylgard 184) of 1.8 MPa compromises the dimensional stability of high-resolution features due to capillary forces, self-adhesion and mechanical stresses during patterning.^[187] Several formulations of PDMS with a higher tensile modulus extend the use of elastomeric stamps in high-resolution soft lithography and include thermally curable PDMS (h-PDMS)^[188a,b] and ultraviolet (UV)-curable PDMS,^[188c] with a tensile modulus of 8.2 MPa and 3.4 MPa, respectively, and combinations thereof in the form of composite stamps.^[188b]

Stamp material	Properties			
Teflon AF 2400 (DuPont) ^[159]	σ = 1.6 GPa , surface energy of 15.6 J·m ⁻² , resistant to any organic solvent, 10000 times larger gas permeability than PDMS (ideal for soft molding ^[209]), polymers feature with aspect ratios >6 in sub-100 nm range by imprinting, transparent from deep ultraviolet (UV) and near-infrared (IR)			
UV-curable urethane acrylate pre-polymer (Ebecryl 265, SK-UCB) ^[189a]	σ = 20–320 MPa (tunable by type and amount of modulator) for contact printing and nanoimprinting, surface energy of 23.0 J·m ⁻² , and resistant to most organic solvents			
UV-curable polymer (Ebecryl 280 by SK-UCB) ^[189b]	σ = 40 MPa, surface energy of 23.0 J·m ⁻² , optional self-replication by UV-curing, shrinkage of 0.7% during curing, and resistant to most organic solvents			
UV-curable perfluoropolyether (PFPE) ^[190]	σ = 4 MPa, higher resistance to organic solvents and lower intrinsic surface energy than PDMS			
UV-curable NOA 63 (Norland Products) ^[191]	good mechanical stability, with a softening transition above 48 $^{\circ}$ C for conformal contact (ideal for polymer spin transfer printing ^[203]), transparent from 360 to 1260 nm			
polyolefin plastomers (POPs) ^[192]	fast preparation by compression molding (~1 h), significant contact-printing performance in the sub-100 nm range, and no surface contamination during conformal contact with the substrate, as is the case with PDMS ^[193]			

Table 2.4 Potential alternative stamp materials in literature for high-resolution soft lithography

In Table 2.4, several other potential stamp materials are shown with a wide range of properties, depending on the application in high-resolution soft lithography

(e.g., contact printing, imprinting, etc.). Any soft-lithographic technique starts with replica molding (REM) of the surface-relief features of a master into elastomeric stamps by casting and curing of the elastomeric precursor against the master. REM is capable of replicating features with very high fidelity and accuracy down to lateral dimensions of ~1.5 nm,^[194a] i.e., the length scale of large molecules.^[194b] Additionally, the advantages of REM in soft lithography are numerous, from the procedural simplicity to the capability of generating multiple elastomeric copies from a single master.

The first set of techniques in soft lithography relies on the intimate conformal contact of elastomeric stamps with the surface of substrates for efficient transfer of matter on substrates. The first technique, microcontact printing, exploits elastomeric stamps as ink reservoirs to transfer molecules (initially alkanethiols) that are capable of forming SAMs on the surface of various metal films^[30] and is capable of patterning large surfaces in a single impression. A lot of studies have been done on several aspects of contact printing, including the transport mechanism of alkanethiols,^[195] formation and order of SAMs on metal films,^[36a] and different inking modes for stamps.^[36b] This type of contact printing is an additive process and the resulting SAMs on metal films have found many applications in nanotechnology,^[196] including the use as ultra-thin resists in the selective wet-chemical etching of metal films^[197a] and as passivating layers in selective deposition.^[197b] Additionally, microcontact printing is extremely useful for the direct patterning of a wide range of interesting species, such as colloidal particles,^[198a] proteins,^[198b] organic reagents,^[198c] catalysts,^[198d] dendrimers,^[198e] nanoparticles,^[38] and polymers^[198f] on a range of surfaces. Even with the application of resolution enhancement approaches,^[199] composite stamps^[188b] and low-diffusion inks,^[37] the resolution of microcontact printing encounters practical limits at about 100 nm due to surface diffusion of molecular inks and the isotropic nature of etching and deposition systems that convert SAM patterns into patterns of functional materials.

Recent research in soft lithography has come up with several potential contactprinting techniques that circumvent the intrinsic limitations of microcontact printing and include the direct patterning of organic and inorganic on surfaces at sub-100 nm resolution (Table 2.5). As shown in Table 2.5, these direct contact-printing techniques

are the result of ingenious engineering of the adhesion forces at the stamp/film and the film/substrate interfaces to facilitate the additive or subtractive transfer of matter by means of conformal contact of elastomeric stamps with substrates. The correct preparation of the stamp and the substrate is of critical importance, as shown in the case for the preparation of metal layers on elastomeric stamps by metal deposition without substantial formation of nanocracks that can deteriorate the integrity of the final nanostructures.^[200] nTP and MTP exploit the intrinsic low adhesion of metals (in particular gold) to PDMS and concentrate only on interfacial adhesion to the substrate.

Technique*	Туре	Patterning principle
nTP ^[201]	additive	transfer of metal patterns to surfaces by covalent bonding between metal and SAMs or by surface condensation between surface silanols (Si-OH) and titanols (Ti-OH) from the adhesion layer
MTP ^[202]	additive	transfer of metal patterns by the formation of intimate junctions between metal and polymer films upon annealing
STP ^[203]	additive	transfer of polymer patterns by selective surface functionalization of the stamp ^{$[203a]$} or by electrostatic interactions between the polymer and the polyelectrolyte adhesion promoter ^{$[203b]$}
DTL ^[204]	additive	transfer of PDMS from the PDMS stamp by UV/ozone exposure (oxidation) and successive cohesive mechanical failure at the surface of the PDMS stamp
detachment nanolithography ^[205]	subtractive	transfer of polymer from the substrate by formation of intimate junctions between the polymer and stamp upon annealing

Table 2.5 List of potential contact-printing techniques for high-resolution soft lithography

* nTP: nanotransfer lithography; MTP: metal transfer printing; STP: spin transfer printing; DTL: decal transfer lithography.

In contrast, a systematic study on detachment lithography has shown the interplay of the intrinsic (adhesion forces and wettability) and external (temperature, pressure) parameters for the engineering of polymer detachment.^[205c]

There are several immediate technological advantages arising from these direct contact-printing techniques over microcontact printing, such as (1) sub-100 nm resolution, owing to the absence of surface diffusion of molecular inks or edge disorders, and (2) reduction of the number of transfer layers, owing to the higher etch resistance of polymers and metals. Additionally, the absence of a residual layer eliminates additional post-processing steps as is the case of imprinting of polymers (Section 2.3.1). The examples in Fig. 2.24 illustrate the capability of direct-contact printing techniques in the fabrication of nanostructures.



Figure 2.24 Several examples of nanostructures by contact printing: (a) SEM image of a 2D photonic waveguide pattern in gold with close-up images of the metallic PDMS stamp (top image) and of the final pattern after nTP (bottom image),^[201a] (b) SEM images of arrays of nanochannels,^[206] (c) complex grating by two sequential application of DTL and successive reactive ion etching,^[204b] and (d) complex polymer structures by simultaneous of physical and chemical nanostructures by STP.^[203b]

The contact-printing techniques in this section are potentially useful for the direct patterning of functional materials on delicate surfaces, for example the patterning of conductive polymers (e.g., PEDOT, m-MTDATA) for high density organic electronic circuits.^[207]

The second set of techniques in soft lithography relies on the intimate conformal contact of elastomeric stamps with polymer films for efficient molding of polymers on substrates (Table 2.6). Contact-molding techniques take immediate advantage of the

latest development of novel mold materials (Table 2.4) in the accurate replication of patterns into polymers. The combination of conformal contact over large areas and suitable surface energy of the stamp eliminates the need of high external pressures as is the case in most nanoimprint lithography schemes. CFL has the capability of generating (Sections 2.2.1.1 and 2.2.3.2) and replicating nanoscale features in polymer layers by the concept of capillary rise. The fact that contact molding of nanometer scale features by capillary rise is feasible with impermeable molds,^[208] and not on the micrometer scale, illustrates that the capillary pressure increases with decreasing feature size. For this reason the resolution of techniques using capillary rise (CFL, soft molding) seems to depend on the availability of high-resolution masters, as shown by the feature replication of 30 nm.^[208] Additionally, contact-molding techniques have the capability to replicate polymer features with zero residual layer^[29a] and therefore allow the direct patterning of the underlying substrate.

Technique	Patterning principle
CFL ^[29]	capillary rise of polymer melts by wetting on elastomeric stamps at temperatures above Tg
soft molding ^[209,210]	capillary rise of solvent-containing polymer films (i.e., spin coating or dipping without baking) by wetting on elastomeric stamps at room temperature
soft imprint lithography ^[211]	low-pressure molding of a prepolymer/monomer film and successive UV curing

Table 2.6 List of potential contact-molding techniques for high-resolution soft lithography

An interesting technique to increase the aspect ratio of polymeric features is nanodrawing,^[212] i.e., the elongation of polymeric features upon removal of the stamp at higher temperatures (Fig. 2.24), again by engineering of the adhesive forces at the stamp/polymer interface.

An innovative development in post-processing that can significantly improve the line-edge roughness (LER) of nanostructures (of metals, semiconductors, dielectrics) was shown very recently by exploiting self-perfection by liquefaction (SPEL).^[213] SPEL exploits the use a single laser pulse for the selective melting of nanostructures on

the timescale of hundreds of nanoseconds in order to guide the self-perfection of the nanostructures before solidification, as shown in Fig. 2.25c for sub-100 nm silicon lines on SOI. The possibility of post-process enhancement of the LER of nanostructures is an option to meet one of the most difficult requirements for IC manufacturing (LER < 3 nm).



Figure 2.25 Several examples of nanostructures by contact molding: (a) high-resolution periodic lines in SiO₂ by soft molding and reactive ion etching,^[210b] (b) SEM images illustrating from top to bottom the effect of nanodrawing on the feature dimensions of PMMA nanohairs,^[212] and (c) optical image of a water droplet on a very hydrophobic surface consisting of long PMMA nanohairs (see SEM image) on 3M tape.^[212] (d) SPEL in action: 70 nm-wide silicon lines on SOI, before (top image) and after open-SPEL by application of a single laser pulse of 545 mJ·cm⁻² and the corresponding edge profiles illustrating a reduction of the 3 σ -LER from 19.5 to 3.6 nm.^[213]

2.3.3 Summary

The current set of techniques in this section is capable of replicating the surfacerelief features of hard masters into a range of materials with high accuracy, in part by the recent developments of novel stamp materials that comply with the specific tasks for soft lithography at the sub-100 nm scale. Unconventional techniques offer immediate advantages in the fabrication of structures that are difficult to achieve by conventional photolithography, including patterning on non-planar surfaces, fabrication of 3D structures, structuring materials other than the resist, and modification of surfaces. Especially nanoimprint lithography has the potential to become of prime importance in future IC manufacturing.

2.4 Conclusions

Starting from the pioneering work of Whitesides' group, the technological advancements and the number of publications over the past 15 years are illustrative for the activities of a large number of scientific groups and for the current technological status of unconventional nanofabrication in the generation and replication of nanoscale patterns. The beauty of this field lies in its diversity. The practical utility, cost-efficiency and versatility of unconventional techniques to structure a wide range of materials on the nanometer scale are a large stimulus for fundamental studies in materials science, chemistry, biology and physics.

The focus of this chapter was to provide an up-to-date overview of the most potential patterning principles in unconventional lithography. For the purpose of developing next-generation lithography techniques, the recent performance of nanoimprint lithography (Section 2.3.1), block copolymer lithography (Section 2.2.3.1) and dip-pen nanolithography (Section 2.2.2) in the fabrication of high-resolution patterns indicates the promise for future IC manufacturing, in spite of the fact that several important technological issues are still open for investigation. Each technique has certain characteristic advantages, and it is unlikely that a single technique will ever dominate all areas of application. Additionally, the availability and simplicity of the remaining techniques in this chapter make the fabrication of highresolution features a practical reality for many researchers in nanoscience and technology.

2.5 References and notes

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Supramolecular microcontact printing and dip-pen nanolithography on molecular printboards^{*}

This chapter describes the transfer of functional molecules onto selfassembled monolayers (SAMs) by means of soft and scanning-probe lithographic techniques – microcontact printing (μ CP) and dip-pen nanolithography (DPN), respectively – and the stability of molecular patterns during competitive rinsing conditions. The formation of kinetically stable molecular patterns with a lateral resolution of 60 nm exemplifies the use of β -CD SAMs as molecular printboards for the selective immobilization of printboard-compatible guests at the nanometer scale through the use of specific, multivalent supramolecular interactions.

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3.1 Introduction

The ability to transfer functional molecules onto surfaces on the nanometer scale is one of the enabling principles in the field of nanotechnology. Generally, there are two strategies for the transfer of molecules onto surfaces: (i) the use of templates (e.g., patterns of self-assembled monolayers (SAMs) or polymers) to direct and control the selective deposition of molecules from solution; (ii) active deposition onto surfaces by means of a patterning element (e.g., a stamp or a probe). The advantage of the latter patterning strategy is that the transfer of molecules onto surfaces does not rely on expensive photolithographic procedures and does not require processing conditions that are incompatible with several interesting types of functional molecules (e.g., biomolecules) or susceptible to cross-contamination by nonspecific binding. Therefore, direct-patterning strategies in the micro- and nanometer regime such as soft lithography^[1,2] and scanning-probe lithographies (SPL)^[3] are extensively used for immobilizing functional molecules on surfaces. Examples include microcontact printing $(\mu CP)^{[1]}$ of proteins on glass^[4] or of polymers on reactive SAMs on gold^[5] and dip-pen nanolithography (DPN)^[6,7] of DNA on gold and glass.^[8] However, in most cases, the immobilization of molecules on the surface is accomplished either by covalent binding to the surface, or to SAMs consisting of anchoring molecules, or by nonspecific physisorption.

Supramolecular interactions combine the advantages of chemisorption and physisorption to immobilize functional molecules on surfaces. Supramolecular interactions are directional and specific and allow fine-tuning of the adsorption/ desorption processes at receptor surfaces,^[9] which is not feasible for conventional routes of immobilizing molecules on surfaces. Cyclodextrins (CDs) and their derivatives are attractive receptor molecules for application in receptor surfaces, as these molecules have been known to accommodate and form inclusion complexes with various organic guest molecules in aqueous solutions through hydrophobic interactions.^[10,11] β -CD adsorbates containing multiple attachment points form SAMs on gold with a high degree of order, in which the recognition sites are pointing toward the outer interface of the monolayer in a quasi-hexagonal lattice.^[12,13] This high degree of order and dense packing of the monolayer are prerequisites for the application of these SAMs in sensor applications in general,^[14] and specifically in the

present β -CD SAMs.^[9] These conditions render the (hydrophobic) recognition sites of all β -CD receptors on the surface essentially identical for guest complexation and minimize nonspecific adsorption.

The interaction strength of small, monovalent guest molecules to these β -CD SAMs has been studied by electrochemical impedance spectroscopy (EIS)^[13] and surface plasmon resonance $(SPR)^{[15]}$ spectroscopy. Such guest molecules bind to β -CD SAMs with a binding strength that is identical to the intrinsic interaction strength with native β -CD in solution. Application of guest molecules that can form *multivalent* interactions offer a tool to fine-tune the overall interaction strength from reversible to permanent binding to the surface. The use of β -CD SAMs as molecular printboards^[16] exploits this central property in order to position multivalent guests on the surface. For this purpose, a divalent printboard-compatible guest molecule^[17] has been specifically designed to study multivalent interactions at the printboard.^[18,19] The contrasting binding behavior of this molecule and, in general, multivalent molecules in comparison to monovalent guest molecules is that the former molecules are capable of forming additional interactions and, therefore, have higher overall binding strengths with the β -CD surface. The overall interaction strength of this divalent guest molecule to the β -CD SAM was five orders of magnitude higher than the binding strength of the monovalent guest molecule. The use of multivalent guests such as dendrimers^[9] and polymers^[20] are known to exhibit even stronger interactions with β -CD SAMs. The advantage of dendrimers is that the number of interactions with the printboard is tunable by adjusting the dendrimer generation.^[21]

The successful positioning of functional printboard-compatible guest molecules into molecular patterns on surfaces by supramolecular μ CP and supramolecular DPN has been reported previously.^[22] The reversibility of supramolecular binding allows, in principle, the preparation of stable molecular patterns (e.g., by printing or writing) under one set of conditions and erasure of these patterns under another set of conditions. This chapter represents the first systematic study that addresses the kinetic stability of molecular patterns on β -CD printboards towards (competitive) rinsing conditions in relation to the number of binding functionalities (i.e., valency).

3.2 Results and discussion

Self-assembled monolayers (SAMs) of the β -CD heptathioether adsorbate 1b (Scheme 3.1) were prepared on gold layers (20 nm) on silicon substrates, as described previously in the literature.^[13,23] Scheme 3.1 also depicts different adamantyl- (Ad-) functionalized guest molecules (2, 3, 4b) that were used in the present study for the selective transfer onto the β -CD printboard on gold by *supramolecular* μ CP and/or *supramolecular* DPN^[22] (Scheme 3.2).



Scheme 3.1 Chemical structures of the receptors, β -cyclodextrin (1a) and the β -CD adsorbate for attachment to gold (1b), and of the guests, 1-adamantyl-3-methyl-urea (2), bis(adamantyl)-functionalized calix[4]arene (3), and the fifth-generation adamantyl-functionalized poly(propylene imine) dendrimer (G5-PPI-(Ad)₆₄) (4b). The chemical structure of the G2-PPI-(Ad)₈ (4a) is shown for convenience.

on molecular printboards



Scheme 3.2 Schematic representation of the transfer of printboard-compatible guests on the β -CD printboard by supramolecular μ CP (left) and supramolecular DPN (right).

3.2.1 Supramolecular microcontact printing – pattern stability

For the supramolecular printing experiments, poly(dimethylsiloxane) (PDMS) stamps were fabricated by replica molding (REM) of a silicon master structure with periodic lines (3 μ m width, 5 μ m spacing, 1.2 μ m depth). Treatment of the PDMS stamps by mild oxidation in a UV/ozone plasma reactor for 60 min was carried out to render the PDMS stamps hydrophilic for subsequent immersion inking in an aqueous solution of the guest for about 15-30 min. Contact-printing experiments were carried out by manually placing the stamp on the surface and leaving the stamp in contact with the surface for 1-2 min. Prior to studying the stability of the resulting molecular patterns of 2, 3, and 4b by atomic force microscopy (AFM) friction-force imaging, X-ray photoelectron spectroscopy (XPS) analysis^[24] was performed to study the degree of guest deposition by adsorption from solution and by transfer from PDMS stamps (Table 3.1).

		XPS [percentage composition]			
	Guest deposition	C(1s)	N(1s)	O(1s)	S(2p)
imental	no guest	79.5 ± 0.9	2.6 ± 0.5	16.2 ± 0.4	1.7 ± 0.1
	(bare β-CD)				
	adsorption	77.8 ± 0.6	4.0 ± 0.3	17.1 ± 0.6	1.2 ± 0.3
exper	μCP	74.5 ± 0.7	3.7 ± 0.2	20.7 ± 1.0	1.1 ± 0.2
	μCP + rinsing	76.7 ± 0.5	3.6 ± 0.4	18.5 ± 0.9	1.3 ± 0.1
	# monolayers of 3 ^[*]				
theoretical	0 (bare β-CD)	81.0	2.7	13.5	2.7
	1	79.2	4.1	14.4	2.2
	2	78.0	5.0	15.1	1.9
	3	77.1	5.7	15.6	1.6
	4	76.4	6.3	16.0	1.4
	5	75.8	6.7	16.2	1.3

Table 3.1 Elemental analysis by XPS of the β -CD printboard before and after deposition of divalent guest 3 by adsorption from solution and by μ CP, and the theoretical XPS values^[*] relating to the number of monolayers of 3 present on the printboard.

^[*] One monolayer of guest 3 is defined as consisting of a 2:1 CD:3 stoichiometry with the β-CD printboard in order to reflect the interaction of both adamantyl functionalities with the β-CD printboard.

The close-to-theoretical XPS values in Table 3.1 confirm the atomic composition of bare β -CD printboards. For the purpose of this study, divalent guest 3 was deposited on the entire surface of the β -CD printboard by printing with featureless PDMS stamps and by adsorption from solution. Physisorption due to electrostatic interactions was ruled out by rinsing the substrates with 50 mM aqueous NaCl solution (200 mL) and water (50 mL). The amounts of guest present on the printboards were estimated by comparing experimental atomic compositions with the corresponding theoretical values. The latter relate to a number of guest monolayers, in which a guest monolayer is defined as the amount of guest that leads to full saturation of available
host sites by using the highest possible valency. For 3, this corresponds to divalent binding.^[18] To correct for the small deviations in atomic composition of the bare β -CD printboard between the experimental and theoretical values, the (absolute) changes in atomic composition were taken for comparison. From the clear trends shown in Table 3.1, the carbon and oxygen data appeared to offer the best estimation.

Approximately one monolayer of guest molecules on the surface (expected: $\Delta C\% -1.8$, $\Delta O\% +0.9$) was left after adsorption from solution ($\Delta C\% -1.7$, $\Delta O\% +0.9$), with also the N and S changes in close agreement. Conversely, the experimental values indicate that (*i*) up to 5 guest monolayers (expected: $\Delta C\% -5.0$, $\Delta O\% +4.5$) were transferred during physical contact of the stamp with the printboard ($\Delta C\% -5.2\%$, $\Delta O\% +2.7$), and that (*ii*) 1-2 guest monolayers ($\Delta C\% -2.8$, $\Delta O\% +2.3$) were remaining after rinsing the substrate with 50 mM aqueous NaCl (200 mL) (expected: $\Delta C\% -3.0$, $\Delta O\% +1.6$). Submonolayer coverages were the result after rinsing with competitive rinsing solutions (e.g., 1-10 mM native β -CD at pH 2, data not shown).

The stability of the molecular patterns of a series of different guest molecules was studied by comparing the patterns on the β -CD printboard directly after μ CP with the patterns after rinsing with water, 1 mM β -CD, or 10 mM β -CD solutions (200 mL; Fig. 3.1). By contact-mode AFM imaging of the β -CD printboard after μ CP, clear patterns of different friction contrast were seen, confirming the transfer of the guest molecules from the PDMS stamp onto the substrates (Figs. 3.1a, 3.1d and 3.1g).^[25] Complete removal of the patterns of monovalent guest 2 from the β -CD printboard was already induced by rinsing with water, as shown by the disappearance of the friction contrast (Figs. 3.1a-b). However, this rinsing procedure did not affect the patterns of divalent guest 3 (Figs. 3.1d and 3.1e). Additionally, even rinsing with substantial amounts of 10 mM β -CD solution did not result in complete removal of the patterns of 3 (Fig. 3.1f), even though competition is known to enhance multivalent dissociation kinetics.^[26] The high stability of the molecular patterns of 3 to rinsing with 10 mM β -CD solution stems from the high overall interaction strength if a guest is capable of forming more than one supramolecular interaction with β -CDs on the printboard.^[18,19] On a qualitative basis, it is clear from the relative friction contrast changes before and after rinsing with 10 mM β -CD solution, that molecular patterns

of multivalent guest 4b (Figs. 3.1g and 3.1i) are even more stable towards competitive rinsing than the divalent guest 3 (Figs. 3.1d and 3.1f).



Figure 3.1 Contact-mode (CM) AFM friction images of patterns of monovalent guest 2 (top), divalent guest 3 (center), and multivalent guest 4b (bottom) showing patterns present on the β -CD printboard (image sizes 50 μ m × 50 μ m; friction forces [a.u.] increase from dark to bright contrast): after μ CP of the guests (a, d, g) (brighter areas), after rinsing with water (b, e, h) and after rinsing with 10 mM β -CD at pH 2 (c, f, i).

The fact that the transfer of 4b, as its per- β -CD complex,^[27] by μ CP onto the β -CD printboard is successful and the fact that its patterns are very stable during competitive rinsing emphasize its strongly multivalent nature. The association of this type of multivalent guest to the printboard most probably occurs through seven Ad-CD interactions, as has been recently shown for similar ferrocenyl-functionalized dendrimers.^[21] AFM height measurements (not shown) illustrate that multilayers of guest 4b (feature height about 3 nm) were transferred onto the β -CD printboard during μ CP. Friction contrast variations within the areas of transfer (Fig. 3.1g)

correlate with nonuniformities in layer thickness. Additionally, rinsing with 10 mM β -CD solution was found to effectively suppress physisorption, resulting in ~1 monolayer of 4b on the β -CD printboard (feature height after rinsing was about 1 nm). Apparently, the interaction strength of guest 4b with the β -CD printboard is sufficient to apply the present receptor surfaces as molecular printboards for the printing of stable molecular patterns of multivalent guests by supramolecular μ CP.

Electrochemical impedance spectroscopy (EIS) measurements were carried out in order to obtain more information on the degree of deposition of guest 4b on the printboard. For this purpose, multivalent guest 4b was deposited on the entire surface of β -CD printboards by printing with featureless PDMS stamps and by adsorption from solution. The effect of rinsing with 10 mM CD solution (200 mL) at pH 2 and water (50 mL) at pH 2 was also taken into account (Table 3.2).

Guest deposition	R _{CT} ^[*] [k Ω]
no guest	42
adsorption	8.4
μCP	75
μCP + rinsing	7.3

Table 3.2 EIS results of the β -CD printboard before and after deposition of multivalent guest 4b by adsorption from solution and by μ CP.

^[*] Charge-transfer resistance towards $[Fe(CN)_6]^{4-}/[Fe(CN)_6]^{3-}$.

The initial charge-transfer resistance (R_{CT}) of the β -CD printboard (42 k Ω) towards the negatively charged [Fe(CN)₆]⁴⁻/[Fe(CN)₆]³⁻ reporter redox couple is similar to R_{CT} values reported previously in the literature^[13] and reflects a high degree of order of this monolayer to block the redox current to the gold efficiently. The presence of guest 4b on the printboard is known to significantly decrease this R_{CT} since electrostatic attraction of the anionic reporter redox couple with the positively charged dendritic core facilitates the oxidation/reduction at the electrode surface.^[9] This was found to be the case for deposition of 4b onto the printboard by adsorption from solution and by μ CP and subsequent rinsing (7-8 k Ω), resulting in monolayer

coverages of guest molecules on the printboard. Conversely, the two-fold increase in the R_{CT} value upon deposition by μCP (without rinsing) suggests, in agreement with AFM analysis, the transfer of multilayers of guest 4b onto the β -CD printboard during μCP . In this case, the increase of the layer thickness of guest molecules on top of the printboard impedes the redox current to the electrode surface and, therefore, results in a higher R_{CT} value.

To rule out any interference from nonspecific interactions on the β -CD printboard, printing experiments under the same conditions were carried out on SAMs of 11-mercapto-undecanol (OH SAMs) (Figure 3.2).



Figure 3.2 CM AFM friction images of micrometer-size line patterns of multivalent guest 4b (brighter areas) on 11-mercaptoundecanol SAMs (bottom) showing patterns present on the β -CD printboard (image sizes 50 μ m × 50 μ m; friction forces [a.u.] increase from dark to bright contrast): after μ CP of the guests (brighter areas) (a), after rinsing with 1 mM β -CD at pH 2 (b) and after rinsing with 10 mM β -CD at pH 2 (c).

OH SAMs were chosen as reference layers, since these SAMs have a similar polarity as SAMs of β -CD, but are incapable of forming inclusion complexes with the guest molecules. Figs. 3.1g-i and 3.2a-c illustrate the concept of the molecular printboard by the difference in stability of the patterns of multivalent guest **4b** on the β -CD printboard and on OH SAMs upon subsequent rinsing with different concentrations of β -CD solutions.

Again, patterns with a clear friction contrast are apparent in contact-mode AFM images of the OH SAM after μ CP. This indicates that the transfer of guest molecules from the stamp onto the β -CD printboard does not rely on specific interactions, but that physical contact is sufficient to generate patterns of molecules (Figs. 3.1g and 3.2a). The requirement of *specific* supramolecular interactions for the stability of the molecular patterns on the printboard is, however, evident from the complete removal of the patterns of multivalent guest 4b from the OH SAMs upon rinsing with 1 mM

and 10 mM β -CD solutions (Figs. 3.2b and 3.2c), a result confirming physisorption in this case. Patterns of **4b** on the β -CD printboards were stable even after rinsing with substantial amounts of a 10 mM β -CD solution (Fig. 3.1i).

Poly(propylene imine) (PPI) dendrimers such as **4b** have already been employed as nanocontainers for dye molecules on the β -CD printboard^[28] and as water-soluble nanoreactors for the formation of gold nanoparticles in solution.^[29] In the present study, the latter dendrimer-stabilized gold nanoparticles were exploited to facilitate the fabrication of metal patterns on the printboard by incorporating an additive technique, namely electroless deposition (ELD) into the present supramolecular patterning strategies.

3.2.2 Electroless deposition - fabrication of metal patterns on the printboard

 $\mathrm{ELD}^{[30]}$ is a routine and cost-effective fabrication technique for the selective deposition of metal on surfaces. It is an autocatalytic redox process that comprises the reduction and deposition of metal ions (as the source of metal) from solution to a substrate in the presence of a reducing agent. To initiate ELD of metal on surfaces, the only requirement is a surface containing a catalyst. For this purpose, G5-PPI dendrimer-stabilized gold nanoparticles were prepared by a literature procedure,^[29] and subsequently transferred as catalysts for ELD onto the molecular printboard by supramolecular μ CP. Fig. 3.3 shows AFM height images of this printboard before and after immersion in a copper ELD bath for 5 min and indicates average heights of 2 and 64 nm (see cross-section AFM analysis in Fig. 3.3), respectively. The fact that the patterns of dendrimer-stabilized nanoparticles were stable during competitive rinsing with 10 mM β-CD (200 mL) at pH 2 (Fig. 3.3a) supports the idea that the gold particles reside inside the dendrimers^[29] and that multivalent interactions of several Ad functionalities at the outer interface of the nanoparticle with the printboard account for the pattern stability. As argued before, the dense, insulating shell of supramolecular Ad- β -CD complexes protects the nanoparticles inside the dendrimer from coagulation.^[29] The ELD experiment shown here illustrates that these nanoparticles are still accessible to act as catalytic centers for the ELD of copper. Fig. 3.3b also illustrates the high selectivity for copper deposition in the target areas containing the dendrimer-stabilized gold nanoparticles. The direct placement of these

catalytic centers and the protective nature of the β -CD SAM for the underlying gold substrate accounts for this observation.



Figure 3.3 CM AFM height images of patterns on the β -CD printboard (image sizes 30 μ m × 30 μ m): (a) patterns of G5-PPI dendrimer-stabilized gold nanoparticles after supramolecular μ CP and subsequent rinsing with 10 mM β -CD solution at pH 2, and (b) the corresponding copper structures by subsequent electroless deposition for 5 min.

The combination of ELD and μ CP has been reported previously to offer a routine and cost-effective fabrication technique for metal patterns.^[31,32] However, the distinct difference between the present work and these previous reports is the use of multivalent supramolecular interactions between the catalyst and the surface that can subsequently serve as a strong connection between the metal patterns and the substrate.

3.2.3 Supramolecular DPN – writing local molecular patterns at the sub-100 nm scale

DPN is a promising lithographic technique that allows the extension of the supramolecular patterning strategy on the printboard to the sub-100 nm scale (Scheme 3.2).^[6,7] Previously, it has been shown that patterns of **3** on the β -CD printboard transferred by DPN are similar to those prepared by supramolecular μ CP, with regard to the issues of ink transfer and pattern stability upon rinsing.^[22] To perform DPN, wet inking of clean silicon nitride AFM tips was done by immersion for 15 min in an aqueous solution of the guest. The writing experiments on β -CD printboards were carried out in ambient atmosphere (T = 25 °C, relative humidity = 40-50 %) by scanning the tip in contact mode along a line on the substrate for a

certain period of time. To investigate the potential of supramolecular DPN, the lateral resolution was investigated by writing patterns with different width-to-length ratios (Fig. 3.4a).^[33]



Figure 3.4 CM AFM friction images of patterns present on the β -CD printboard: (a) an array of lines, 7 µm long with mean widths (± standard deviation) of 290 ± 20 nm, 480 ± 30 nm, and 880 ± 50 nm, of divalent guest 3 after scanning the tip at a velocity of ~21 µm/s across the β -CD printboard for 10 min for each line (image size 15 µm × 15 µm; friction forces [a.u.] increase from dark to bright contrast), and (b) an array of lines, 3 µm long with average widths of approximately 60 ± 20 nm, of multivalent guest 4b after scanning the tip at a velocity of ~4 µm/s across the β -CD printboard for 10 min for each line (image size 6 µm × 6 µm; friction forces [a.u.] increase from dark to bright contrast). Readout of the patterns was done with the same tip by increasing the scan size and the scan velocity (approximately 15 times the writing speed).

Fig. 3.4a shows a friction AFM image of a pattern of 3 consisting of three lines, 7 μ m long with targeted widths of 220 nm, 440 nm, and 880 nm (top to bottom), respectively, written by DPN on the β -CD printboard over 10 min for each line. Inversion of the relative friction contrast in the image in comparison to the friction contrasts seen in Figs. 3.1 and 3.2 is probably related to the different character of the contacting surfaces (i.e., ink-containing tip versus bare tip), as was shown before for similar β -CD systems,^[22] as well as for poly(amidoamine) (PAMAM) dendrimers on other SAMs.^[34]

Overinking during the writing stage as well as the effect of instrumental drifts are parameters that could cause the pattern broadening in this particular experiment to be more significant at high-resolution writing (ca. 30% for the 220 nm-wide line, 10% for the 440 nm-wide line). The sub-100 nm writing capability of supramolecular DPN is shown in Fig. 3.4b with the array of lines of guest 4b on the printboard at a resolution of 60 nm. In addition to writing periodic line patterns, this specific pattern was written in four stages by adjusting the scan angle of the tip by 45° each time with respect to the previous line. In this particular experiment, gold-coated tips containing a poly(ethylene glycol) SAM were used to allow a better transfer of 4b to the printboard. Scanning with a bare silicon nitride AFM tip over the β -CD printboard under the same experimental conditions did not form any visible pattern, thus proving that pattern formation arises from the transfer of guest molecules and not from mechanical forces applied by the tip to the printboard.

3.3 Conclusions

The results in this chapter demonstrate the successful application of micro- and nanopatterning strategies, *supramolecular* μ CP and *supramolecular* DPN, respectively, that employ the concept of the molecular printboard.

Supramolecular μ CP with guests of different valencies was carried out on the β -CD printboard for testing the stability of the patterns during rinsing with substantial amounts of water and competitive aqueous solutions. It was found that all kinetic stabilities can be seen: rapid removal by water in the case of a monovalent guest, slower partial removal by competitive rinsing in the case of a divalent guest, and complete stability for truly multivalent guests. Interference of nonspecific interactions in the formation of molecular patterns on the printboard was ruled out by the complete removal of the molecular pattern during mild rinsing conditions on reference OH SAMs. Clearly, the novelty of the present patterning strategy over existing fabrication strategies for molecular patterns on substrates lies in the tunable pattern stability of printboard-compatible guests on the molecular printboard through the use of specific, multivalent supramolecular interactions.

Supramolecular DPN was exploited on the β -CD printboard to extend the supramolecular patterning strategy into the sub-100 nm range, as shown by the nanoscale patterns with 60 nm line width.

ELD of metal patterns on the printboard demonstrates that molecular printboards constitute a useful tool for the fabrication and application of supramolecular architectures on surfaces.

3.4 Experimental

Chemicals and materials: 11-Mercaptoundecanol, native β -cyclodextrin, sulfuric acid (H₂SO₄), hydrogen peroxide (H₂O₂), methylamine, 1-adamantyl isocyanate, NaCl,

CuSO₄, Na₂SO₄, NaOH, formaldehyde, sodium formate, ethylenediaminetetraacetate sodium salt (Na₄EDTA), toluene, chloroform, ethanol, dichloromethane, and diethylether were purchased from commercial sources in the highest available purity and used without further purification. A PDMS prepolymer kit (Sylgard 184 silicone elastomer) including PDMS curing agent (Sylgard silicone elastomer curing agent) was purchased from the Dow Corning Corporation. 100 mm p-type OSP silicon wafers were purchased from Okmetic and the 20 nm gold-on-silicon wafers from Ssens BV (Hengelo, The Netherlands).

The synthesis of the β -CD heptathioether adsorbate **1b** and the preparation of SAMs on gold substrates were reported previously.^[13,23] The synthesis of the fifth-generation adamantyl-terminated poly(propylene imine) dendrimer (G5-PPI-(Ad)₆₄) **4b** and its per- β -CD complex were carried out according to literature procedures.^[28]

1-Adamantyl-3-methyl-urea (2): A solution of methylamine (0.105 g, 3.4 mmol) in chloroform (20 mL) was added to 1-adamantyl isocyanate (0.5 g, 2.8 mmol). This mixture was stirred overnight at room temperature under argon. The solvent was removed under vacuum. Subsequently, diethyl ether was added to the residue to isolate the product as a white solid (0.260 g, 1.25 mmol; 45%). ¹H NMR (300 MHz, CDCl₃): 4.13 (br s, 1H), 4.09 (br s, 1H), 2.71 (d, J = 5.1 Hz, 2H), 2.09-2.03 (m, 3H; CH₂CHCH₂[Ad]), 1.98-1.94 (m, 6H; CHCH₂C[Ad]), 1.68-1.64 (m, 6H; CHCH₂CH[Ad]); ¹³C NMR (100 MHz, CDCl₃): 158.24, 50.75, 42.58, 36.48, 29.60, 26.89; FAB-MS: m/z 209.2 ([M+H]⁺, calcd. 209.2).

Substrate and monolayer preparation: Prior to use, all glassware was cleaned by immersion in piraña (3:1 ratio of conc. H_2SO_4 and 33 wt% H_2O_2) for 30 min and rinsing with substantial amounts of water. Gold substrates of 1 cm × 1 cm were cleaned by oxygen plasma treatment for 5 min and the resulting oxide layer was reduced by dipping the substrates for 10 min in absolute EtOH.^[35] Subsequently, the clean gold substrates were immersed in an adsorbate solution (0.1-1.0 mM) with a minimal delay of 16 h. After incubation, the substrates were rinsed with substantial amounts of dichloromethane, ethanol, and water.

Supramolecular microcontact printing: Stamps were fabricated by casting a 10:1 (v/v) mixture of PDMS and curing agent (Sylgard 184, Dow Corning) against a silicon master. Curing for 1 h at 60 °C and release of the PDMS stamps was done at this

curing temperature to avoid buildup of tension due to thermal shrinkage.^[36] PDMS stamps were left in the oven at 60 °C for at least 18 h to ensure complete curing.

By nature, PDMS is very hydrophobic ($\theta_{adv} = 120^\circ$) and in order to transfer polar inks oxidation of the stamps is essential. Oxidation of PDMS stamps was carried out in a commercial UV/ozone plasma reactor (Ultra-Violet Products Inc., model PR-100) for 60 min at a distance of about 2 cm from the plasma source. This type of reactor contains a low-pressure mercury UV light operating with UV emissions at 185 nm (1.5 mW·cm⁻²) and 254 nm (15 mW·cm⁻²) to generate molecular oxygen. UV/ozone (UVO) treatment^[37] of the PDMS stamp was chosen over other techniques^[38,39,40] because this milder treatment results in similar changes of the surface chemical properties of the PDMS as oxygen plasma treatment and in surfaces that were found to be more resistant towards mechanical stress (i.e., crack formation^[41]) during μ CP. The PDMS stamps were kept hydrophilic by immersing the stamps in an aqueous ink solution immediately after UVO treatment.^[42] Hydrophobic recovery of PDMS after the UVO treatment^[43] restricts the use of this type of surface modification for μ CP.^[44] Contact angle measurements over time illustrate that PDMS was moderately hydrophilic ($\theta_{adv} = 68^\circ$) after the UVO exposure and that hydrophobic recovery was insignificant for at least several hours ($\theta_{adv} \approx 10^{\circ}$ after 4 h) in the case the stamp was kept under water. Additionally, this hydrophobic recovery was found to level off, as seen by the contact angle increase of about 4° within the next 20 h. For reproducibility, all printing experiments were carried out within the first 4 h after UVO treatment, without reactivation of the stamps.

Subsequently, the ink was applied on the stamps by immersion into the adsorbate solution (0.1-1.0 mM in hydrophobic constituents) for 15-30 min. After withdrawal from the solution and drying under a continuous stream of nitrogen for 1 min, the stamps were manually applied onto the substrate and left in contact with the surface for 1-2 min onto the substrate (the β -CD printboard or reference 11-mercapto-1-undecanol SAM). Re-inking was carried out after each printing step. Finally, the substrates were systematically rinsed with 200 mL of aqueous solutions of native β -CD (1 or 10 mM at pH 2), NaCl (50 mM) or Milli-Q water. Additional rinsing with Milli-Q water was done in the first two cases in order to remove any excess of β -CD or salt on the printboard.

Supramolecular DPN: Si₃N₄ tips were cleaned in chloroform overnight and the ink was applied onto the tip by immersion into the adsorbate solution (0.5-1.0 mM in hydrophobic constituents) with minimal delay of 15 min. After withdrawal and drying, the tip was scanned in contact mode across the surface of the β -CD printboard for a certain period of time (T = 25 °C, relative humidity = 40-50 %). The written patterns were recorded with the same tip by increasing the scan size and the scan velocity (ca. 15 times the writing speed).

Electroless deposition (ELD) of copper: To trigger the ELD of Cu on the printboard, G5-PPI dendrimer-stabilized gold nanoparticles were prepared according to the literature procedure^[29] and transferred onto the printboard by conducting supramolecular μ CP. After rinsing with 200 mL of aqueous solutions of native β -CD (10 mM at pH 2) and Milli-Q, the substrate was immersed in the copper ELD bath containing 40 mM CuSO₄, 140 mM Na₂SO₄, 120 mM Na₄EDTA, 300 mM NaHCOO, and 30 mM HCHO at pH 13. After taking the substrate out of the ELD bath, the substrate was rinsed with Milli-Q and dried under a continuous stream of nitrogen for 1 min.

Characterization: AFM analyses were carried out with a NanoScope III multimode AFM (Veeco/Digital Instruments, Santa Barbara, CA, USA) equipped with a J-scanner, in contact mode using Si_3N_4 cantilevers (Nanoprobes, Veeco/Digital Instruments) with a nominal spring constant of 0.1 N·m⁻¹. To ensure maximum sensitivity for lateral forces in the friction-force images, the sample was scanned in 90° with respect to the long axis of the cantilever. AFM imaging was performed in ambient atmosphere (25 °C, relative humidity = 40-50 %).

XPS analyses were carried out with a Physical Electronics Quantum2000 equipped with an Al K monochromatic excitation source (source energy = 1486.7 eV, take-off angle set to 30°), a spherical sector analyzer and a multichannel plate detector (16 detector elements). For the survey scan (pass energy of 117 eV), the X-ray beam was set to high power mode (100 Watt per 100 μ m) to scan a total area of 1000 μ m × 500 μ m; for element scans (pass energy of 29.35 eV), the X-ray beam was set to 25 W per 100 m to scan a total scan area of 1000 μ m × 500 μ m (298 K and 1·10⁻⁸-3·10⁻⁸ Torr). The sensitivity factors used for C, N, O and S for calculating the

atomic concentration are respectively 0.314, 0.499, 0.733 and 0.717. The hydrocarbon C(1s) signal at 284.8 eV was used as the reference to correct for surface charging.

Electrochemical impedance spectroscopy measurements were carried out with an AUTOLAB PGSTAT10 (ECOCHEMIE, Utrecht, The Netherlands) in a custombuilt three-electrode setup consisting of a platinum counter electrode, a mercurous sulfate reference electrode ($V_{MSE} = 0.61 V_{NHE}$) and a gold working electrode (held by a screw cap to the bottom of the cell and exposing a geometric area of 0.44 cm² to the electrolyte solution). Scans were carried out using a frequency range from 10 kHz to 0.1 Hz in 1 mM K₃Fe(CN)₆/K₄Fe(CN)₆ containing 0.1 M K₂SO₄ at -0.2 V_{MSE} with a 5 mV amplitude. The charge-transfer resistance (R_{CT}) of the monolayer on gold was determined from the Nyquist plot by fitting the experimental data to an equivalent Randles circuit containing a charge-transfer resistance of the monolayer in parallel with the capacitance of the monolayer and in series with the resistance of the solution.^[45,46]



This figure is taken as an example of a Nyquist electrochemical impedance spectroscopy plot of a β -CD printboard after adsorption of guest 4b from solution and the corresponding fit to an equivalent Randles circuit (R_{CT} = 8.1 k Ω).

NMR spectroscopy was carried out at 25 °C using a Varian Unity Inova 300 spectrometer and Varian Unity 400 WB spectrometer respectively. ¹H NMR

(300 MHz) and ¹³C NMR (100 MHz) chemical shifts are given relative to residual CHCl₃ (7.27 ppm and 77.0 ppm, respectively).

FAB mass spectrometry was carried out with a Finnigan MAT90 spectrometer with *m*-nitrobenzylalcohol (NBA) as the matrix.

3.5 References and notes

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Stamps for high-resolution soft lithography^{*}

This chapter describes a novel soft-lithographic technique by combining capillary force lithography (CFL) and replica molding (REM) for the fabrication of high-resolution, second-generation stamps for soft lithography. The fabrication of different gold patterns at a resolution of ~125 nm by application of these stamps in several microcontact printing (μ CP) experiments illustrates the applicability of the technique for high-resolution soft lithography, without the need for cleanroom facilities.

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4.1 Introduction

Photolithography has been the main technology for integrated circuit (IC) fabrication during the last decades,^[1] however, its extension to the sub-100 nm range requires the development of state-of-the-art lithographic techniques: e.g., deep-UV and extreme-UV photolithography,^[2,3] soft X-ray lithography,^[4] electron-beam writing,^[5] and ion-beam lithography.^[6] The costs render these techniques less suitable for exploratory research applications, in which requirements such as pattern uniformity, reproducibility, and accurate alignment are not stringent. The development of complementary non-photolithographic techniques, including soft lithography,^[7] nanoimprint lithography (NIL),^[8] and capillary force lithography (CFL)^[9] has been successful during the last decade in the field of micro- and nanofabrication.

Soft lithography^[7] is a versatile and cost-effective patterning technique for the routine fabrication of structures of \geq 500 nm. Soft lithography uses elastomeric poly(dimethylsiloxane) (PDMS) stamps to transfer a relief pattern to the surface of a substrate by conformal contact. Generally, high-resolution soft lithography still depends on the availability of state-of-the-art lithographic techniques for stamp fabrication. Several elegant and cost-effective methods for stamp fabrication with minimum feature sizes of about 200 nm have been reported by Whitesides.^[10,11] The basic strategy is to start from elastomeric PDMS stamps with micrometer-size features and exploit non-photolithographic techniques to fabricate submicrometer-size patterns. Several methods exploit the elastomeric properties of commercial PDMS: by microcontact printing (μ CP) with stamps under lateral compression,^[12] under uniaxial stretching in the plane of the stamp or under vertical pressure normal to the plane of the stamp;^[10] or by replica molding (REM) against PDMS stamps under lateral compression.^[13] Of a greater impact are alternatives like lithographic molding^[14] and repetitive REM that combine the facile fabrication of robust high-resolution masters with that of *second–generation* stamps.

One technique that also has the potential to fabricate these second-generation masters is capillary force lithography (CFL).^[9,15] CFL is an emerging non-photolithographic technique that combines the essential feature of NIL^[8] – molding a polymer melt – with the key element of soft lithography^[7] – molding with an

elastomeric stamp for large-area patterning of polymer films. At a temperature (T) above the glass-transition temperature (T_g) of the polymer, capillary forces drive the polymer to conform to the mold pattern. The initial polymer film thickness and the feature dimensions of the mold determine the type of the resulting polymer patterns. CFL of thick polymer layers result in a negative copy of the mold, with a residual layer that remains on the substrate. Upon decreasing the initial polymer film thickness, the (concave) meniscus cannot rise to completely fill the spaces between the mold and the substrate, owing to polymer insufficiency, resulting in the formation of high-resolution polymer structures *at the feature edges* of the original stamp patterns. To the best of our knowledge, this chapter describes the first practical application of these CFL polymer structures as potential masters for high-resolution soft lithography. Structural analysis was carried out by atomic force microscopy (AFM) and scanning electron microscopy (SEM) to reveal information on the mechanical stability of the CFL polymer templates during stamp processing and the pattern transfer fidelity by application of the resulting stamps in soft lithography.

4.2 Results and discussion

4.2.1 General procedure for the fabrication of high-resolution polymer templates

Scheme 4.1 depicts the basis for the present soft-lithographic procedure for generating high-resolution polymer templates on substrates by CFL of thermoplastic polymers.



Scheme 4.1 Schematic representation of the general (two-stage) fabrication procedure of second-generation polymer templates: (1) replica molding (REM) of the first-generation master into PDMS stamps ($w = 5 \mu m$, $s = 3 \mu m$, $h = 1.2 \mu m$); (2) CFL^[9] on thin polymer layers at T > T_g of the polymer for several hours.

A liquid prepolymer of PDMS (Sylgard 184, Dow Corning Corporation) was cast against a silicon master. All the masters in this study were fabricated by standard photolithography and contain either continuous (lines) or discontinuous (pillars) features of micrometer size (width, $w = 5 \mu m$, spacing, $s = 3 \mu m$, height, $h = 1.2 \mu m$). The PDMS was cured for 1-18 h at 60 °C in order to obtain stamps of minimal (~0.5 N·mm⁻²) to medium modulus (~2 N·mm⁻²),^[16] respectively, suitable for conducting CFL over large areas. Special care was taken to prevent the interference of dust particles by bringing the substrate and stamp into contact with minimal delay. Larger dust particles in particular were found to act as sites at which conformal contact was gradually lost during CFL. High-temperature processing is known to limit the performance of PDMS stamps.^[9] Without taking proper precautions, the contact between the substrates and PDMS stamps cannot withstand the thermal stress that builds up owing to differences in thermal expansion coefficients ($\alpha_{PDMS} \approx \alpha_{Si}$). To prevent separation, the thickness of the PDMS stamps was kept down to about 1 mm, in order to ensure sufficient flexibility.^[17] This measure was found to improve the overall performance of commercial PDMS for CFL processing of polymer layers effectively, even though the stamp gradually hardens by thermal curing during processing.

To obtain high-resolution polymer templates, thin layers (~35-200 nm) of thermoplastic polymers, e.g., poly(methylmethacrylate) (PMMA) and polystyrene (PS) were applied onto silicon substrates by spin coating. At this range of polymer film thicknesses, no dewetting effects by physical confinement – which could impair the alignment of the polymer features at the feature edges of the PDMS stamp – were to be expected.^[18] The initial conditions for CFL on thin polymer layers are crucial for the formation of useful symmetrical template structures. Of all the different procedures, pre-equilibration of the PDMS stamp and the substrate to the annealing temperature before bringing the PDMS stamp into conformal contact with the substrate was found to result in the most-symmetrical polymer structures over large areas. The annealing temperature T was set sufficiently above the glass-transition temperature T_g (T ~ T_g + 40 °C) in order to ensure polymer mobility for patterning the polymer layers during CFL. Typical annealing times were chosen in the range of 2-20 h. It was found by optical inspection that cooling down the stamp and substrate

for 5 s (instead of 30 min) at room temperature was already sufficient to release the stamp from the substrate, without generating (microscopic) distortions to the polymer template.

Capillary kinetics of a polymer melt in permeable microchannels^[19] have been shown to depend on several parameters, such as time, polymer flow (temperature, type of the polymer, wetting capabilities of the stamp) and the amount of polymer accessible to fill the spaces between the mold and the substrate (initial polymer film thickness, fill factor of the stamp). As a result, one can expect to have control over the lateral and vertical dimensions of the resultant polymer features by changing any of these parameters (Fig. 4.1).



Figure 4.1 Contact-mode (CM) AFM height images (z range: 1000 nm; image size: 25 μ m × 25 μ m) of different high-resolution PS templates. These templates were fabricated by CFL on initial polymer thicknesses of 35 nm (a), 80 nm (b), and 150 nm (c) at 150 °C for 20 h. The feature widths (full width at half maximum, FWHM) of these PS structures, respectively 440 nm, 680 nm, and 850 nm, were determined from the average height cross-sectional analysis of the respective meniscus of each template (d). (e) SEM image of the cross-sectional view of a symmetrical PS template on silicon. From the close-up SEM image, a thin residual layer is seen on the silicon substrate.

Fig. 4.1 shows the result of increasing the initial polymer film thickness from 35 nm to 150 nm with the other process parameters kept constant (PS, $M_w = 240 \text{ kDa}$, 150 °C, 20 h). Cross-sectional AFM analyses of the resulting PS templates (Fig. 4.1d) reveal that it is possible to exert certain control over the feature line width of the

polymer template, ranging in this case from 440 nm to 850 nm (Figs. 4.1a-c). Adjusting the other process parameters should result in polymer templates with an even larger range of feature widths. Under the present process conditions, a thin residual layer of < 100 nm is always present in the template, protecting the underlying substrate from direct post-processing (Fig. 4.1e).

4.2.2 Fabrication of second-generation PDMS stamps

These CFL polymer templates of thermoplastic polymers are directly applicable in the stamp processing stage by replica molding (REM) (Fig. 4.2a). The only requirement for thermoplastic polymer templates in order to function as robust masters for the fabrication of second-generation stamps is to have a T_g that is several tens of degrees higher than the thermal curing temperature of PDMS (T = 60 °C). The cross-sectional AFM analysis of a representative CFL master (in this case PMMA, M_w = 38 kDa) illustrates the excellent uniformity of the polymer structures, on which the positions of the ~440-nm-high PMMA structures (FWHM is ~680±100 nm) correlate with the periodicity of the PDMS stamp (Fig. 4.2b).



Figure 4.2 (a) Schematic representation of the procedure for the fabrication of second-generation stamps using CFL polymer templates as masters. CM AFM height images (z range 800 nm) of the high-resolution second-generation master (b) and the corresponding second-generation PDMS stamp (c) (image size 50 μ m × 50 μ m), including the corresponding cross-sectional analysis. The PMMA masters were fabricated by CFL with flexible PDMS stamps on a ~150 nm PMMA layer at 150 °C for 6 h. Second-generation PDMS stamps were fabricated by casting prepolymer PDMS against the PMMA master and curing for 1 h at 60 °C. Post-curing of the second-generation stamps was done for another 18 h at 60 °C after release from the master.

The general procedure (Section 4.2.1) is, therefore, ideal for the fabrication of second-generation masters using thermoplastic polymers. The remaining irregularities in the width and height of the PMMA edge patterns present in the master most probably originate from dust particles interfering with the alignment of the stamp to the substrate since CFL was carried out outside the cleanroom environment.

The fabrication of second-generation PDMS from CFL templates was done using the same REM procedure for the generation of the first-generation PDMS stamps. Fig. 4.2c shows an AFM image of the resulting PDMS stamp. The corresponding cross-sectional AFM analysis confirms that REM of second-generation PMMA masters was achieved with high fidelity, as shown by the ~420 nm deep indentation (FWHM is ~950±100 nm) in the PDMS stamps. The low interfacial free energy, the elasticity of PDMS, and its chemical inertness to PMMA allow the generation of multiple copies of the PMMA master into second-generation PDMS stamps. No noticeable degradation or damage of the PMMA master was observed after fabrication of more than ten copies.

4.2.3 Application of second-generation stamps in soft lithography and the selective etching of gold

To illustrate the applicability in soft lithography, the second-generation PDMS stamps were used in a series of microcontact printing (μ CP) experiments. As part of soft lithography, μ CP is a non-photolithographic patterning technique for the facile formation of patterns of self-assembled monolayers (SAMs) of alkanethiols on surfaces of coinage metals (e.g., gold, silver, palladium, copper) upon conformal contact of an ink-containing elastomeric PDMS stamp with the metal surface.^[7] Additionally, the resulting SAM patterns are known to comprise of a class of efficient ultrathin resists in the selective wet etching of the underlying metal layer. The entire μ CP process for the routine fabrication of metal patterns on silicon surfaces is shown in Scheme 4.2.



Scheme 4.2 Schematic representation of two different μ CP processes using second-generation stamps to fabricate gold features on silicon. (-) μ CP includes printing with octadecanethiol (ODT) (left). (+) μ CP includes printing with pentaerythritol-tetrakis(3-mercaptopropionate) (PTMP) and subsequent protection of the bare gold by dipping the in ODT solution (right). Selective wet-chemical etching of the gold results in trenches in gold (in case of (-) μ CP) and gold lines (in case of (+) μ CP).

Many selective etch solutions are known for the wet-chemical etching of different coinage metals using SAMs as etch resists.^[7] For gold, the most selective etch solution comprises a basic solution containing $K_2S_2O_3/K_3Fe(CN)_6/K_4Fe(CN)_6$ at pH 14.^[20] This aqueous etch solution is not only less hazardous, but also superior to the conventional gold etch solution (aqueous KCN at pH 14) in terms of etch rate and selectivity (i.e. defects and line-edge resolution). The function of the different components of this etch solution during the etching of gold has been shown in an extensive study.^[20] The AFM images in Fig. 4.3 illustrate a higher degree of definition of the gold features with time. From the etching plot (Fig. 4.3d) it is clear that the gold etching in this solution is monotonous in time, representing an etch rate of 3.5 nm·min⁻¹. The origin for the apparent offset in etching (~30 s) is unknown.



Figure 4.3 CM AFM height images (z range: 40 nm; image size: 27 μ m × 27 μ m) of the progression of gold etching using ODT SAMs as etch resists, including the average height cross-sectional analyses at etch times of 1 min (a), 2 min (b), and 5 min (c). (d) Plot of the feature height versus the etch time of gold in the etch solution at room temperature.

The experimental time for complete etching of a 20 nm-thick gold layer $(t_1 = 6-6.5 \text{ min}, \text{ including the offset})$ is close to the literature value $(t_1 = 7-8 \text{ min})$,^[20] and therefore the total etch time was set to 7 min (= 20% overetching with respect to the theoretical etch time) to ensure complete and reproducible etching of the bare gold regions. Overetching of the gold layer is possible, as the etch window, i.e. the additional time of protection of the gold by SAMs, of this etch solution is known to take several minutes (depending on the type of SAM).^[20] Fig. 4.4 illustrates different







Arrays of ~350 nm-wide trenches in gold layers (Fig. 4.4a) were fabricated by μ CP of octadecanethiol (ODT) on a 20 nm-thick gold layer and subsequent selective wetchemical etching. Since only the edges of the original (first-generation) master pattern are transferred into the gold layer, the present CFL technique to fabricate secondgeneration stamps in combination with μ CP and wet-chemical etching is a new alternative to the general class of edge-lithographic techniques.^[21,22,23,24] The major advantage of this technique over the existing edge-lithographic techniques is that it does not require any cleanroom facilities for the fabrication of the structures.

The discrepancy between the lateral dimensions of the trenches in gold (~350 nm) and the indentations present on the second-generation PDMS stamp (Fig. 4.2c, cross-sectional analysis FWHM ~950 nm) suggests that diffusion^[25] and deformation of the PDMS stamp^[26] during printing contribute to the reduction of the feature dimensions. The inverse structures, arrays of ~350 nm-wide gold lines, were fabricated by positive microcontact printing ((+) μ CP)^[27] with PDMS stamps from the same second-generation master (Fig. 4.4b). (+) μ CP has been recently developed by IBM to invert the final pattern in gold without the need for new masters^[27] and employs printing with pentaerythritol-tetrakis(3-mercaptopropionate) (PTMP), which is compatible with the requirements of μ CP, but incapable of forming etch-blocking SAMs. Adsorption of octadecanethiol (ODT) from solution to the bare gold areas and subsequent wet-chemical etching yields the positive pattern. It was found that PTMP SAMs retard the etching of gold and longer etching (+40% in terms of optimal etch time) was necessary to obtain good-quality patterns.

Cross-printing is a convenient approach to reduce the second lateral feature dimension,^[10] and accordingly to increase the inherent low pattern density of this edge lithographic technique. Figs. 4.4c and 4.4d illustrate the resulting gold patterns by 90° cross-printing with the same stamp prior to wet-chemical etching of the gold.

Although the ultimate resolution of the present technique was not the aim of this study, patterns of alternating ~125 nm and ~800 nm-wide trenches in gold (Fig. 4.4e) were fabricated by printing with stamps containing very asymmetric features in order to illustrate the potential of printing sub-150 nm with second-generation stamps of commercial PDMS.

4.2.4 Pattern transfer by reactive ion etching

The resulting 20 nm-thick gold patterns are sufficiently robust to serve as secondary etch masks in reactive ion etching (RIE) of the underlying silicon substrate, resulting in high aspect ratios. To achieve anisotropic etching of silicon, the black silicon method (BSM)^[28] was applied on the etch apparatus prior to etching of the substrates.

BSM is an experimental procedure that makes use of the fact that the parameter settings for anisotropic etching are close to the ones for the formation of black silicon (i.e. micro spikes) owing to micromasking. For the present RIE apparatus, black silicon was found on bare silicon wafers at the following parameter settings: 10 mTorr, 50 W, 40 sccm SF₆ and 20 sccm O₂, 10 °C. At this point, only minor changes to the parameter settings were necessary to obtain anisotropic profiles such as the one shown in Fig. 4.5 (anisotropy of ~0.85).^[29]



Figure 4.5 (a) Schematic representation of the RIE process. (b) High-resolution cross-sectional SEM image of 500 nm-wide trenches after etching 650 nm into silicon using SF_6/O_2 RIE (APC open, 50 W RF power, 40 sccm SF_6 , and 20 sccm O_2 , 90 s). A clear undercut on the order of 100 nm was found, resulting in a widening of the silicon trenches with respect to the lateral dimensions in the gold mask (350 nm). (c) Low-resolution SEM image of the same structures in top view.

Under these RIE conditions, the silicon etch rate was about 0.34 μ m/min at a silicon over gold etch selectivity of more than 170.

4.3 Conclusions

The present results in this chapter illustrate that the combination of capillary force lithography (CFL) and replica molding (REM) is a convenient and inexpensive approach for the fabrication of high-resolution, second-generation PDMS stamps from the corresponding second-generation masters without the need of state-of-the-art lithographic techniques. The successful application of these stamps in μ CP for the fabrication of ~125 nm features in gold demonstrates its potential to extend the utility of soft lithography in general. Additionally, the general applicability of these templates in high-resolution nanofabrication procedures will allow fabrication of sub-100 nm features (see Chapter 5).

4.4 Experimental

Chemicals and materials: All experiments were carried out in a regular chemical laboratory. Octadecanethiol (ODT), pentaerythritol-tetrakis(3-mercaptopropionate) (PTMP), ethanol, NaOH, Na₂S₂O₃, K₃Fe(CN)₆, and K₄Fe(CN)₆ were purchased from commercial resources in the highest available purity and used without further purification. A PDMS prepolymer kit (Sylgard 184 silicone elastomer) including PDMS curing agent (Sylgard silicone elastomer curing agent) was purchased from Dow Corning Corporation. Poly(methylmethacrylate) (PMMA, M_w= 38 kDa and 350 kDa) and polystyrene (PS, M_w= 240 kDa) were purchased from Acros Organics. 100 mm p-type OSP silicon wafers were purchased from Okmetic and the 20 nm gold-on-silicon wafers from Ssens BV (Hengelo, The Netherlands).

Stamp fabrication: Stamps were fabricated by casting a 10:1 (v/v) mixture of PDMS and curing agent (Sylgard 184, Dow Corning) against a relief pattern of interest (i.e., silicon master or polymer template). After curing for 1 h at 60 °C, the stamps were released at this curing temperature to avoid buildup of tension due to thermal shrinkage.^[26b] Only the PDMS stamps for μ CP were left in the oven at 60 °C for at least 18 h to ensure complete curing.

Capillary force lithography: Silicon substrates of 2 cm \times 2 cm were cleaned by ultrasonic treatment in acetone for 15 min and dried in a continuous stream of nitrogen. Polymer layers were applied onto these silicon substrates with minimal delay by spin coating a polymer solution in toluene at 3000 rpm for 30 s.

PDMS stamps for CFL were fabricated without additional post-curing in order to obtain very flexible stamps. The thickness of these PDMS stamps was about 1 mm. In order to prevent separation, the PDMS stamps and the polymer layers were equilibrated to the annealing temperature for 30 min prior to bringing the stamp into contact with the polymer layer. The system was left at 40 °C above the T_g of the respective thermoplastic polymer for several hours. After cooling to ambient temperature (for ~5 s), the PDMS stamps were released from the substrates. The resulting polymer edge structures could function in this case as masters for the fabrication of second-generation PDMS stamps using the common molding and curing procedure.

Microcontact printing: Cleaning of fresh gold substrates was performed by immersion into ethanol for 10 min. The ink was applied to the second-generation PDMS stamps by exposing the stamps to a drop of ODT (0.3 mM) or PTMP (1.8 mM) solution in ethanol for 1 min. The gold substrates and stamps were dried with a continuous stream of nitrogen prior to printing. All the printing experiments were carried out manually with a print duration between 30-60 s in all cases. Reinking of the stamp was only necessary in between printing steps for cross-(+) μ CP experiments. To complete the (+) μ CP procedure, the gold substrates were immersed into a 0.2 mM solution of ODT for 5 s.

Wet-chemical etching of gold: After μ CP, complete etching of the (bare) gold was achieved by immersing the gold substrates in an aqueous solution of 1.0 M NaOH, 0.1 M Na₂S₂O₃, 0.01 M K₃Fe(CN)₆, and 0.001 M K₄Fe(CN)₆ at room temperature for 7 min (in case of (-) μ CP), or 10 min (in case of (+) μ CP). [CAUTION: potassium ferricyanide is light sensitive. Photodecomposition releases products that can contain free cyanide. Potassium ferricyanide is also incompatible with acids and liberates HCN].

Reactive ion etching: Reactive ion etching (RIE) experiments were carried out using an Electrotech Plasmafab 310-340 twin deposition/etch system to transfer the gold patterns into the underlying silicon. The RIE part consists of a parallel-plate system in an aluminum reactor, containing an RF generator operating at 13.56 MHz and an automatic RF matching network. The pumping system consists of a turbopump in series with a rotary pump. The spacing between the electrodes is 8 cm, with an RF electrode (styros) of 19 cm in diameter. The temperature of this target electrode was set at 10 °C and is controllable by backside heating/cooling using an oil bath system. Pressure control is possible in automatic mode (by the capacitive manometer) and in manual mode by adjusting the throttle valve position. Anisotropic etching of silicon was found for the following conditions: Automatic Pressure Control (APC) open, 50 W, 40 sccm SF₆, and 20 sccm O₂.

Characterization: PMMA film thickness determination was carried out with a Plasmos SD 2002 ellipsometer at a wavelength of 632.8 nm. AFM imaging was carried out with a NanoScope III Multimode AFM (Digital Instruments, Santa Barbara, CA, USA) operating in contact mode using Si₃N₄ cantilevers (Nanoprobes,

Digital Instruments) with an approximate spring constant of 0.32 N·m⁻¹. For height measurements, the scanner was calibrated in z-direction using a calibration standard with known step height. Low-resolution SEM imaging was carried out with a JEOL JSM-5610 SEM operating in secondary electron detection. High-resolution SEM imaging was carried out with a LEO Gemini 1550 FEG-SEM.

4.5 References and notes

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annealing temperature (in combination with the use of low-modulus and flexible PDMS stamps) were successful measures to prevent separation, but at the expense of asymmetric CFL structures.

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anisotropy $= 1 - \frac{\binom{d_2 - d_1}{2 \cdot h}}{2 \cdot h}$ with d_1 and d_2 the lateral width of the silicon feature at the base and top respectively, and *h* the height of the silicon feature.

Fabrication of functional polymer templates as versatile tools for nanolithography^{*}

5

This chapter describes the fabrication of high-resolution polymer templates by capillary force lithography (CFL) and its implementation in nanoimprint lithography (NIL) and in the wetchemical etching of metals. Fabrication of gold patterns on silicon substrates with a lateral resolution of 100–150 nm demonstrates the potential of CFL templates as versatile tools for nanolithography.

5.1 Introduction

The development of low-cost, high-throughput lithographic techniques for the fabrication of sub-100 nm patterns has taken a dominant position in nanofabrication during the last decade. The unrelenting efforts that are undertaken in this field are the

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result of the corresponding high cost and low accessibility of state-of-the-art lithographic techniques, e.g., deep-UV (DUV) and extreme-UV (EUV) photolithography,^[1] soft X-ray lithography,^[2] electron-beam writing,^[3] and ion-beam lithography^[4] for large-area fabrication of high-resolution patterns. Although these techniques will remain the main technology for the semiconductor industry, soft lithography^[5] and nanoimprint lithography (NIL)^[6] have made enormous progress as non-photolithographic techniques in several areas of exploratory and scientific research in terms of sub-100 nm resolution, reproducibility, and uniformity of the patterns over large areas. Generally, the concept of these non-photolithographic techniques lies in the exact replication of the structures on a master by (conformal) contact with the substrate of interest. To overcome the reliance on expensive state-of-the-art equipment for the fabrication of the necessary high-resolution templates, several innovative strategies have been put forward that extend the capabilities of soft lithography and NIL into the sub-100 nm range. These inexpensive strategies basically exploit the combination of different (aspects of) soft- and/or photo-lithographic techniques in order to downscale the feature sizes of the master. From the ever increasing list of existing strategies, the most promising set of lithographic techniques to date is edge lithography. The general feature that relates the lithographic techniques in this set is the translation of the structural characteristics of the master into resultant edge patterns. Edge-lithographic techniques include near-field phase-shifting photolithography,^[7] maskless lithography,^[8] undercutting by isotropic wet-chemical etching,^[9] patterning local disorder in monolayer resists,^[10] edge-spreading lithography,^[11] and multiplication by sidewall-patterning.^[12,13] The development of these techniques has led to the facile fabrication of features with ultimate lateral sizes down to 50 nm.

A novel variant of edge lithography was shown in Chapter 4 by combining capillary force lithography (CFL)^[14,15] of thin (with respect to the intrusion depth of the stamp) polymer layers with replica molding (REM)^[5] for the fabrication of second-generation stamps suitable for conducting high-resolution soft lithography.^[16] Although no special attention was given to the resulting high-resolution structures by Lee et al.,^[14] it was shown in Chapter 4 that these polymer templates have potential in edge lithography, for example, the use of these templates as masters in soft lithography

as versatile tools for nanolithography

(Scheme 5.1, route A). Fabrication of gold patterns with a resolution of ~125 nm was feasible by application of the resulting stamps in microcontact printing (μ CP) and subsequent wet-chemical etching.^[16]

This chapter reports recent developments of CFL as a nanofabrication technique by utilizing the resulting CFL polymer templates in two additional patterning techniques (Scheme 5.1): as molds for NIL (Scheme 5.1, route B) and as etch masks for the selective wet-chemical etching of metals (Scheme 5.1, route C) by incorporation of a UV-curable polymer and the use of gold-on-silicon substrates in the CFL procedure, respectively.



Scheme 5.1 Schematic representation of the different functions of a high-resolution CFL template depending on the type of polymer and substrate: (A) template as master for stamp fabrication; (B) template as mold for NIL; (C) template as etch mask for the wet-chemical etching of metals.

The (mechanical) stability and integrity of the template during processing and the ultimate resolution of the resulting metal patterns are of prime importance in our process evaluation, which was carried out by atomic force microscopy (AFM) and scanning electron microscopy (SEM). Therefore, emphasis was put on the process

conditions and the etch behavior of the polymer templates (and of the underlying substrate) in order to achieve these goals.

5.2. Results and discussion

5.2.1 CFL templates as high-resolution molds for NIL

The requirements for polymer templates to successfully function as robust NIL molds (Scheme 5.1, route B) are more stringent than for use as masters for μ CP stamp fabrication (Scheme 5.1, route A), because the imprint conditions in standard NIL processing schemes demand the template to be resistant to high temperatures – in the range of 70-80 °C above the T_g of the polymer – and high pressures for several minutes.

To date, only low-viscosity thermoplastic polymers, e.g., poly(methylmethacrylate) (PMMA), polystyrene (PS), and some block copolymers have been exploited in CFL. However, the CFL process is universal in that it also allows patterning of other types of polymers, e.g., UV-curable polymers. The use of this type of polymers offers the advantage over thermoplastic polymers of post-processing stabilization of the polymer template by UV crosslinking (Fig. 5.1a).



Figure 5.1 (a) Schematic representation of the CFL procedure for the fabrication of NIL molds using the UVcurable resist mr-L 6000. (b/c) CM AFM height images (z range: 800 nm; image size: 40 μ m × 40 μ m) of the high-resolution polymer template before (left) and after UV crosslinking (right), including the average height cross-sectional analyses. The polymer template was fabricated by CFL on a ~150 nm mr-L 6000 layer at 70 °C for 2 h. Subsequently, the template was exposed to a dose of 360 mJ·cm⁻² (at an intensity of 9 mW·cm⁻²) in order to induce crosslinking on a hotplate at 100 °C for 10 min.
5.2.1.1 UV crosslinking for post-processing stabilization

The commercially available UV-curable polymer mr-L 6000 is a chemically amplified negative-tone photoresist with a very low glass-transition temperature (T_g) of 35 °C, permitting moderate processing conditions for patterning by CFL (T = 70 °C, 2 h). Special care was taken that, prior to UV exposure, the mr-L 6000 was not exposed to white light (i.e., processing was only carried out in a yellow light environment). Cross-sectional AFM analysis of a representative mr-L 6000 template (Fig. 5.1b) reveals the typical transient meniscus (W-shape) of capillary rise.^[17,18] It has been shown that (i) any meniscus evolves in a uniform U-shape upon long annealing times,^[17] and that (ii) splitting of the meniscus may give rise to exposure of the underlying substrate in the noncontact areas.^[18] Therefore, the observation of a transient meniscus in our template implies that, under the present processing conditions, the polymer pattern could not fully develop the characteristic U-shape meniscus.

UV crosslinking of mr-L 6000 templates typically involves a flood-exposure in a UV exposure system, followed by a post-exposure bake on the hotplate. For an exposure dose of 360 mJ·cm⁻² and a post-exposure bake at 100 °C for 10 min, the structural integrity of the template was not affected by this post-processing stabilization procedure (Fig. 5.1 b/c, template height is ~100 nm and FWHM is ~400 nm). UV exposure of this type of resist activates a photo-acid generator responsible for releasing an acid that initiates the cationic polymerization of the epoxy resist upon heating. At this stage, both crosslinking and thermal flow may occur simultaneously, which can have a significant effect on the pattern fidelity upon curing, in particular in case thermal flow dominates over crosslinking.^[19] The results shown in Fig. 5.2 are the outcome of an AFM study that was carried out in order to address the interplay between these two processes during a post-exposure bake at 100 °C for 10 min in relation to the exposure dose.

The response of unexposed mr-L 6000 templates to thermal treatment excludes the interference of thermal crosslinking with the stabilization of polymer patterns during UV-crosslinking at 100 $^{\circ}$ C (Fig. 5.2a). Fig. 5.2 illustrates a clear trend in pattern stability, in terms of feature height and width, as a function of the exposure dose.



Figure 5.2 Exposure dose dependence of the stability of the polymer templates (CFL at 70 °C for 2 h) towards a standard post-exposure bake at 100 °C for 10 min as shown by CM AFM height images (z range: 250 nm; image size: 30 μ m × 30 μ m), including the average height cross-sectional analyses, after no exposure (a), and at exposure doses of 36 mJ·cm⁻² (b), 72 mJ·cm⁻² (c), and 360 mJ·cm⁻² (d). (e) Plot of the feature height and FWHM of the polymer template as a function of the exposure dose. The trend lines in the plot are only drawn as a guide to the eye; a-d refer to the corresponding AFM images.

At low exposure doses thermal flow was the dominating process, causing significant sagging of the features of the template (Fig. 5.2b). Upon increasing the exposure dose (Fig. 5.2c) the template structures became more and more heat resistant up to the point that the feature dimensions were independent of the exposure dose (Fig. 5.2d). At these high exposure doses, the polymer template was resistant to thermal flow as crosslinking was the dominating process in this regime. The critical exposure dose of

~100 mJ·cm⁻², at which UV crosslinking of the polymer template is still successful, was estimated from the plot in Fig. 5.2e. Quantitatively, this exposure dose (100 mJ·cm⁻² for 150 nm thick layers) is ~33% larger in comparison to the one from dissolution experiments of full resist layers after exposure (500 mJ·cm⁻² for 1 μ m-thick layers).^[19] However, no difference in pattern stability between templates that have undergone a standard post-exposure bake (100 °C for 10 min) and a gradual post-exposure bake^[19] (from 30 °C to 100 °C with a ramp of ~4 °C·min⁻¹) was found by AFM analysis. This result implies that the initiation of crosslinking on the hot plate is rather fast on the timescale of thermal flow, and therefore that the rate of crosslinking (resulting in pattern stability) is totally dependent on the exposure dose.

5.2.1.2 Thermal imprinting and pattern transfer

An additional long exposure and hard bake at 120 °C for 10 min was carried out prior to imprinting in PMMA to further increase the thermal and mechanical stability. The structural integrity of the mold was not affected by this hard bake. At a FWHM of ~400 nm, the pattern density of the as-fabricated polymer mold was about ~10%. For imprinting of such low-pattern-density molds in high-molecular weight PMMA (Mw = 350 kDa), the NIL conditions were set to 180 °C and 100 bar at an imprint time of 1 min (Fig. 5.3a). Figs. 5.3b/c illustrate the high fidelity of NIL as a technique for the pattern replication of the polymer mold in (thermoplastic) PMMA. The feature dimensions of this polymer imprint (Fig. 5.3c, 120 nm in height and FWHM of 390 nm) approximate the dimensions of the polymer mold (Fig. 5.3b, 125 nm in height and a FWHM of 400 nm, fabricated by CFL at 70 °C for 4 h).

AFM analysis of the mold during a durability test of tens of imprints (Figs. 5.3b and 5.3d) also illustrates the high mechanical stability of the mold to imprinting under the present conditions. The anti-adhesive properties of this low-density polymer mold were also outstanding. Specific efforts that were undertaken in the past to eliminate the adhesion of polymer to the mold include the use of ultrathin anti-adhesive layers by plasma deposition,^[20] anti-adhesion additives in polymers,^[21] and the formulation of novel resists and low-surface energy mold materials. An additive, the formulation of mr-L 6000, or a combination of both factors could account for the excellent anti-adhesive properties of the polymer molds during NIL in our case.^[22]



Figure 5.3 (a) Schematic representation of the NIL procedure with CFL molds. (b/c/d) CM AFM height images (z range: 300 nm; image size: 40 μ m × 40 μ m) of the polymer mold (b), an imprint in PMMA (c), and the same polymer mold after tens of imprints at 180 °C and 100 bar (d), including average height cross-sectional analyses.

In most NIL applications, additive (deposition) or subtractive (etching) postimprint processing steps usually follow the imprinting step. To use the functionality of the imprint as a template, it is essential to remove the residual polymer layer in the trenches of the imprint by O_2 reactive ion etching (O_2 RIE). The thickness of this residual layer was kept to a minimum by choosing the thickness of the initial PMMA layers (~140 nm) close to the imprint depth of the polymer mold. Cross-sectional AFM analysis of the imprint (Fig. 5.4a) illustrates a positive taper that is, in general, not the ideal lift-off profile^[23] for performing metal lift-off. To prevent tearing and detachment of the metal during lift-off, metal lift-off on these imprints was performed for thin metal layers only (~10% of the imprint depth). Special care was taken to position the substrate vertically to the evaporation source during the evaporation of

10 nm of gold. Subsequent sonication in warm acetone was carried out to remove the polymer template. Gold lines of ~150 nm wide were fabricated on silicon using this procedure (Fig. 5.4b). The imprint can also function as an etch mask in case the imprinting is done on continuous metal layers on silicon substrates. 150 nm-wide trenches were fabricated on gold-on-silicon substrates by wet-chemical etching the gold using a basic ferro/ferricyanide-containing etch solution^[24] after the O₂ RIE (Fig. 5.4c).



Figure 5.4 (a) CM AFM height image (z range: 250 nm; image size: 8 μ m × 8 μ m) of a PMMA imprint, including the average height cross-sectional analysis. The imprint was fabricated by imprinting with a CFL mold in 140 nm PMMA at 180 °C and 100 bar. The processing time for O₂ RIE (50 mTorr, 50 W, 20 sccm O₂, 15-20 s) was set to over-etch ~20% of the actual residual layer thickness in order to correct for any differences in residual layer thickness over the entire area of the imprint (etch rate of 350 kDa PMMA is ~1.7 nm[•]s⁻¹). (b/c) SEM images of 150 nm-wide gold lines on silicon after subsequent evaporation of 10 nm of gold and sonication of the substrate in acetone at 40 °C for 15 min (b) and of 150 nm-wide trenches in gold-on-silicon substrates after wet-chemical etching for 10 min and removal of the PMMA in acetone (c). The inserts show the higher magnification SEM images of the individual features.

As a result of the positive taper in the PMMA imprint, a discrepancy exists between the feature dimensions of this imprint (after O_2 RIE, FWHM of ~ 400 nm) and the feature dimensions of the metal structures. By changing the time of O_2 RIE treatment, it should, in principal, be feasible to create different opening sizes to the substrate, resulting in further reduction of the feature dimensions of the metal structures after post-processing (e.g., metal lift off, or wet-chemical etching) to the sub-100 nm scale. Therefore, these results do not represent the lower limit of this approach, as NIL has been proven to be the most promising technology for the replication of sub-10 nm features.^[25]

5.2.2 CFL templates as etch masks in wet-chemical etching of metals

One of the major drawbacks of most polymers is the poor etch resistivity toward O₂ (containing) plasmas. Recently, poly(ferrocenylsilanes) (PFS)^[26] with high etch resistance have been exploited in the CFL processing for the direct transfer of the template pattern into silicon by reactive ion etching.^[27] The requirements for polymer templates to function as etch masks in the wet-chemical etching of metals are, however, not so stringent. This procedure (Scheme 5.1, route C) differs from the fabrication of μ CP masters (Scheme 5.1, route A) in that CFL of thermoplastic polymers, in this case PMMA (Mw = 38 kDa) and PS (Mw = 240 kDa), is carried out on 20 nm gold-on-silicon substrates instead of on bare silicon substrates (Fig. 5.5a).



Figure 5.5 (a) Schematic representation of the CFL procedure for the fabrication of polymer (PMMA or PS) etch masks on gold-on-silicon substrates. (b/c) CM AFM height images (z range: 300 nm; image size: $40 \ \mu\text{m} \times 40 \ \mu\text{m}$) of a PS template (b) and the corresponding etch mask after residual layer removal (c), including the cross-sectional analyses. The PS template was fabricated by CFL on a ~120 nm PS layer at 150 °C for 8 h. Removal of the residual layer was done by O₂ RIE for 2 min in order to expose the underlying gold. This processing time was set to over-etch ~20% of the actual residual layer thickness in order to correct for any differences in residual layer thickness over the entire area of the template.

Cross-sectional AFM analysis of a representative PS template (Fig. 5.5b) illustrates again the typical transient meniscus (W-shape) of capillary rise. The present processing conditions result in polymer template features of ~180 nm high (FWHM is

~480 nm). Attempts to etch the underlying gold were futile, due to the presence of a residual layer that was protecting the gold. For this template (Fig. 5.5b), the residual layers was on the order of 80 nm, as determined by an AFM height measurement of the polymer template at a scratch made with a scalpel. Removal of the residual layer without deterioration of the template structures is crucial in the present procedure. The gradual dissolution of the residual polymer in acetone was found to comprise a convenient and fast procedure as polymer templates dissolve within about 10-15 s. However, O_2 RIE was chosen instead in order to have more control over the removal rate of the polymer, which is of the order of nm·s⁻¹. The O_2 RIE etch rates of PS and PMMA were 0.9 nm·s⁻¹ and 1.9 nm·s⁻¹, respectively, as determined from film thickness measurements by ellipsometry on full polymer layers upon etching.

5.2.2.1 Effect of PDMS transfer on the template processability

A distinct difference in adhesion of PDMS stamps to PS and PMMA was seen upon cooling of the substrate plus stamp, which appears to result in the clear O₂ RIE rate difference found upon removal of the residual layer. PDMS stamps were found to detach from PS templates within seconds whereas manual removal of the stamps was necessary in the case of PMMA templates. For the PS templates, removal of the residual layer was feasible at a process time corresponding to the etch rate for full layers. From the cross-sectional AFM analysis (Fig. 5.5c), it is clear that the PS template structure was still intact (in terms of the profile) after O₂ RIE. Additionally, the absolute reduction of ~50 nm in feature height and FWHM to ~130 nm and ~430 nm, respectively, indicates that the O_2 RIE was isotropic. For the PMMA templates, the complete removal of the residual layer in the former contact areas of the stamp with the PMMA substrate was impossible (Fig. 5.6a/b), even after longer etch times. The contrasting etching behavior of these different polymer templates in O_2 RIE was the starting point for investigating the templates and the polymer etch rates in more detail by AFM friction analysis, X-ray photoelectron spectroscopy (XPS), and ellipsometry. Only in the case of PMMA was a significant increase in friction in the contact areas found by AFM friction analysis (Fig. 5.6c). This was attributed to the transfer of low-molecular-weight silicone residues from the PDMS stamp onto the template during CFL of PMMA. Silicon-containing polymers are

known to form thin, non-volatile silicon oxide (SiO₂) layers at the surface during O_2 plasma exposure and therefore exhibit a high etch resistance toward O_2 RIE.^[28]



Figure 5.6 (a) CM AFM height image (z range: 600 nm; image size: $34 \ \mu\text{m} \times 34 \ \mu\text{m}$) of a PMMA template (CFL at 150 °C for 8 h) after 100 s of O₂ RIE, including the cross-sectional analysis. (b) Three-dimensional CM AFM height image (z-range: 600 nm; image size: 7.5 $\mu\text{m} \times 7.5 \ \mu\text{m}$) of a close-up of one individual PMMA ring. (c) CM AFM friction image of a PMMA template that was in contact with a minimal-modulus PDMS stamp for 8 h at 150 °C, prior to O₂ RIE (image size: $34 \ \mu\text{m} \times 34 \ \mu\text{m}$; friction forces [a.u.] increase from dark to bright contrast). (d) Si(2p) XPS spectra of full PMMA layers before and after O₂ RIE. The full polymer layers in this typical experiment were in contact with featureless PDMS stamps under the same CFL conditions. The vertical lines in the spectra represent the positions of the Si(2p) peak for PDMS (101.8 eV)^[29] and SiO₂ (103.3 eV).^[33]

To examine the transfer of silicone residues and its effects on the polymer etch rate, ~150 nm thick PS and PMMA layers were put in contact with two featureless PDMS stamps of different modulus at 150 °C for 8 h (standard CFL conditions). X-ray photoelectron spectroscopy (XPS) analysis was performed on these layers to determine the amount of silicone transfer, whereas ellipsometry was performed to determine the corresponding etch rate during 20 s of O₂ RIE. The results are shown in Table 5.1.

Table 5.1 XPS	analysis of j	polymer substra	tes versus	substrates	that were in	contact wit	h featureles	s PDMS
stamps under C	CFL process	conditions (150	°C, 8 h)	, and the	correspondin	g etch rates	for the po	lymers in
O ₂ RIE (50 mT	orr, 50 W, 20	0 sccm O ₂ , 20 s)						

			XPS [%]	Etch rate	
			O(1s)	Si(2p)	[nm·s ⁻¹]
PMMA	no stamp	71.4	28.6	0.0	1.9
	medium-modulus stamp	67.1	29.1	3.8	1.6
	minimal-modulus stamp	62.4	29.7	8.0	1.4
PS	no stamp	99.5	0.5	0.0	0.87
	medium-modulus stamp	96.8	1.8	1.4	0.81
	minimal-modulus stamp	97.4	1.4	1.3	0.80

For the PS (C/O = 1:0.005) and PMMA (C/O = 5:2.01) reference layers, the elemental atomic compositions are in close-to-theoretical amounts (PS C/O = 1:0, PMMA C/O = 5:2). On these reference polymer layers, no Si-containing residues were detected by XPS that could originate from the ambient (e.g., wafer dust, oil vapor from vacuum pumps). The presence and the position of the Si(2p)-peak (102.1 eV) on each polymer layer that was in contact with a PDMS stamp indicates that contamination of the polymer surface with Si-containing species from the PDMS stamp (101.8 eV)^[29] originates only from the CFL process, irrespective of the type of polymer and the type of PDMS stamp.

The most important findings of this XPS study are: (i) the transfer of Si-containing residues onto PMMA is three to six times higher than in the case of PS; (ii) the curing conditions of the PDMS stamp only affect the transfer in the case of PMMA (i.e., more curing results in less contamination); (iii) the presence of Si-containing residues on polymer layers compromises the processability of the polymer etch mask to a certain extent during the O_2 RIE, resulting in etch rate reductions of 8% for PS and 16-26% for PMMA. On the basis of the AFM height data in Fig. 5.6a, however, the reduction in etch rate on the PMMA template should have been at least 60%.^[30] This discrepancy suggests, in accordance with a previous study on other systems,^[31] that

even more Si-containing residues are transferred in the case of stamps containing patterns. The fact that complete inhibition of etching is not seen in the case of PMMA indicates that the amount of Si-containing residues on the surface is not sufficient to form a continuous, etch-protective layer of SiO₂.^[32] The clear shift of the Si(2p)-peak of the Si-containing residues to a higher binding energy (103.4 eV), which is consistent with that of SiO₂^[33] (Fig. 5.6d), and the high surface roughness^[34] (~20-40 nm) in the former contact areas (Fig. 5.5b) upon O₂ plasma exposure support this statement.

5.2.2.2 Effect of over-etching on the feature dimensions

On the basis of the processability by O₂ RIE, PS was chosen as the polymer of choice in this procedure. Fig. 5.7b shows a SEM image of 390 nm wide gold rings on silicon after wet-chemical etching of the underlying gold for 10 min. Over-etching and undercutting by isotropic wet-chemical etching are well-known techniques in the semiconductor industry to decrease feature sizes.^[35] Fig. 5.7a shows our over-etching procedure for downscaling the lateral dimensions of the resulting metal structures under the polymer etch mask to the sub-100 nm range (Fig. 5.8c; average line width: 115 nm).^[36]



Figure 5.7 (a) Schematic representation of the wet chemical (over)etching procedure. Prior to wet-chemical etching, the gold was reduced in absolute ethanol for 30 min. (b/c) SEM images of gold rings on silicon after wet-chemical etching of gold for 10 min and 60 min, respectively, at room temperature, and subsequent PS removal by sonication in warm acetone for 10 min.

The basic concept of this procedure is to over-etch the gold from both sides of the sub-micrometer polymer etch mask to obtain high-resolution metal patterns. This procedure is therefore conceptually different from the procedure that exploits the undercut by isotropic wet-chemical etching to transfer the edges of polymer patterns by metal deposition and lift-off.^[9] The two SEM images in Fig. 5.7 illustrate the potential of the present over-etching procedure by the reduction of the lateral dimensions by a factor of 3.6. Additionally, the plot of the line width of the gold features versus the etch time (Fig. 5.8a) and the SEM images (Figs. 5.8b-e) illustrate that high control over the line width of the metal patterns is achieved by the etch time.



Figure 5.8 (a) Plot of the line width of the gold features versus the etch time at room temperature. The trend line in the plot is only drawn as a guide to the eye. Each data point was calculated from 6 rings (10 data points per ring) at different locations on the substrate (with a distance of 2.5 mm between each of these rings). The SEM images (b-e) demonstrate gold rings after wet-chemical etching of the gold for 10 min (b), 20 min (c), 50 min (d), and 60 min (e).

The intrinsic etch rate of gold in the aqueous gold etch solution containing $K_2S_2O_3/K_3Fe(CN)_6/K_4Fe(CN)_6$ (pH = 14)^[24] is ~3.5 nm·min⁻¹ (see Section 4.2.3 for etch rate determination). At the onset of etching, a reduction rate (r) of the line width of about twice the intrinsic etch rate of gold was estimated from the slope of the plot (Fig. 5.8a, r = 7.5 nm·min⁻¹). This rate is consistent with the over-etching of the gold taking place at both sides of the etch mask. However, this reduction rate was found to decelerate gradually to about r = 3.6 nm·min⁻¹ (Fig. 5.8a).

As this etch solution is active for at least several hours, a possible explanation for the etching behavior in this typical procedure is that the inward movement of the etching direction underneath the etch mask limits the diffusion of etchants to the etching front. From a detailed study of this etch solution,^[24] it is known that the etch rate is sensitive to changes in the concentration of the coordinating ligand (in this case, thiosulfate) and KOH as, for these electrochemical processes, the oxidation of the gold is the rate-determining step. A reduction in the etch rate by a factor of two, as shown in Fig. 5.8a, was found to correlate to a decrease in the concentration of the coordinating ligand by a factor of ten, or to a decrease in the concentration of the coordinating ligand at the etching front is the most-probable cause.

By interpolation of the etch curve to the optimal etch time for the 20 nm-thick gold layers (~7 min), a line width of ~425 nm was found, which is close to the lateral dimensions of the PS etch mask after O_2 plasma treatment (430 nm). Ultimately, gold patterns with line widths of ~80 nm have been fabricated with this procedure, although the number of defects in the gold structures increases significantly as the feature sizes approach the grain size of the gold layer.^[37]

5.3. Conclusions

The present results in this chapter demonstrate that capillary force lithography (CFL) is a convenient and inexpensive technique for the fabrication of functional, high-resolution polymer templates with high fidelity and excellent uniformity, without the need of state-of-the-art techniques. The successful application of these templates (i) as masters for stamp fabrication in soft lithography (see Chapter 4), (ii) as molds for NIL, and (iii) as etch masks in the wet-chemical etching of metals illustrates the potential of these templates as tools for high-resolution lithography at the 100 nm scale. Regarding the ultimate resolution, limitations in the critical dimensions of the resulting patterns are set by the heterogeneity in mass transport inherent to these existing pattern transfer techniques, respectively, alkanethiol inks onto metal surfaces, evaporation of metals onto silicon surfaces and isotropic etching of polymer templates and metals.

The incorporation of UV-curable mr-L 6000 polymer in the CFL procedure was found to allow the fabrication of high-stability NIL molds by UV crosslinking. A constructive modification of this procedure would constitute crosslinking of the template *in situ* at the annealing temperature by UV illumination through the (transparent) PDMS stamp after a certain annealing time. Efforts for developing such a direct UV-CFL procedure or combining CFL and photolithography of UV-curable polymers in the fabrication of multilevel structures have not been further undertaken in this study.

The transfer of PDMS onto the template during CFL was shown by XPS analysis to depend on the curing conditions of the PDMS stamps as well as on the type of polymer. For PMMA and low modulus PDMS stamps, the transfer of stamp residues was found to severely compromise the O_2 RIE processability of the template in applications that require direct transfer of the template pattern into the underlying substrate (herein, as polymer etch masks for wet-chemical etching of metals). For PS, this was not the case, allowing the residual layer removal and subsequent wet-chemical etching to fabricate 100 nm gold structures.

The key disadvantage of the current set of edge transfer techniques is that it only allows the fabrication of low-density, straightforward patterns with a feature spacing that is set by the spacing of the original micrometer-size patterns. Although this study has also been dealing only with periodic structures, CFL is capable of fabricating irregular and more complex templates, without substantial modification of the general procedure, through physical self-organization^[38] and dewetting,^[39,40] and through capillary dynamics.^[41] Application of such processes in CFL could provide new opportunities in the fabrication *and* replication of nanostructures through the combination of top-down and bottom-up approaches.

5.4. Experimental

Chemicals and materials: All experiments were carried out in a regular chemical laboratory. Octadecanethiol (ODT), pentaerythritol-tetrakis(3-mercaptopropionate) (PTMP), ethanol, toluene, NaOH, Na₂S₂O₃, K₃Fe(CN)₆, and K₄Fe(CN)₆ were purchased from commercial sources in the highest available purity and used without further purification. A PDMS prepolymer kit (Sylgard 184 silicone elastomer)

including PDMS curing agent (Sylgard silicone elastomer curing agent) was purchased from the Dow Corning Corporation. Polystyrene (PS, $M_w = 240$ kDa) and poly(methylmethacrylate) (PMMA, $M_w = 38$ kDa and 350 kDa) and were purchased from Acros Organics; mr-L 6000 was donated by Microresist Technology GmbH, Germany. 100 mm p-type OSP silicon wafers were purchased from Okmetic and the 20 nm gold-on-silicon wafers from Ssens BV (Hengelo, The Netherlands).

Stamp preparation: Stamps were fabricated by casting a 10:1 (v/v) mixture of PDMS and curing agent (Sylgard 184, Dow Corning) against a relief pattern of interest (i.e., silicon master or polymer template). After curing for 1-18 h at 60 °C, the stamps were released at this curing temperature to avoid buildup of tension due to thermal shrinkage.^[42] For CFL, the thickness of these PDMS stamps was about 1 mm to ensure sufficient flexibility.

Polymer template preparation: Silicon substrates of 2 cm × 2 cm were cleaned by ultrasonic treatment in acetone for 15 min, and dried in a continuous stream of nitrogen. Polymer solutions were applied onto clean silicon substrates by filtering through a syringe filter with a 0.2 μ m PTFE membrane (Acrodisc CR 25mm). Spincoating was done at 3000 rpm for 30 s in order to obtain polymer layers of about 150 nm in thickness, followed by a prebake at 100-120 °C for 10 min to remove residual solvent. The stamp and the substrate were equilibrated at the annealing temperature for 30 min and contact was applied manually with minimal delay to prevent any dust interference. The stamp plus substrate were left in the oven at about 40 °C above the glass-transition temperature of the respective polymer for 2-20 h. After cooling for 5 s at room temperature, the PDMS stamp was released from the polymer substrate.

Nanoimprint lithography: Prior to imprinting, crosslinking of the mr-L 6000 polymer template was induced by flood-exposure of the template under a mask aligner (Karl Süss mask aligner MA55) at a dose of 360 mJ·cm⁻² (at an intensity of 9 mW·cm⁻²) and post-exposure baking at 100 °C for 10 min. To function as a NIL mold, additional thermal and mechanical stability was achieved by flood-exposure of this template for 10 J·cm⁻² and hard baking at 120 °C for 10 min.

For imprinting the mold pattern into a thermoplastic polymer (PMMA, $M_w = 350$ kDa), the second-generation mold plus polymer substrate were placed in

between the plates of a commercial hydraulic press (Specac, 15 tons manual press equipped with electrical heating plates and a temperature controller) and heated to the imprint temperature of 180 °C (heating rate ~35 °C·min⁻¹). Upon reaching this imprint temperature, an imprint pressure of 100 bar was applied to pattern the PMMA layer. After 1 min imprinting at 180 °C and 100 bar, the stack was cooled (cooling rate ~ 20 °C·min⁻¹) to 106 °C prior to pressure reduction and manual cleavage of the mold from the polymer substrate with a scalpel. The total process cycle (i.e., heating, 1 min imprinting, and cooling) took about 7 min.

Metal evaporation was performed on a commercial evaporation system (Balzers BAK600, E-gun evaporating system). Prior to evaporation, the imprints were exposed to O_2 RIE (50 mTorr, 50 W, 20 sccm O_2) for 15-20 s. The metals were evaporated on the imprint in the following order: chromium (2-3 nm, ~1 Å·s⁻¹ at 2·10⁻⁶ mbar); gold (10 nm, ~1 Å·s⁻¹ at 3·10⁻⁶ mbar). Finally, the substrates were sonicated in acetone at 40 °C for 15 min.

Undercutting by isotropic wet-chemical etching: The PS templates were exposed to O₂ RIE (50 mTorr, 50 W, 20 sccm O₂) for 120 s in order to remove the residual layer. The resulting oxide layer on the gold was reduced by leaving the substrates in absolute ethanol for 30 min.^[43] Subsequently, etching of the underlying gold was achieved by immersing the templates in an aqueous solution of 1.0 M NaOH, 0.1 M Na₂S₂O₃, 0.01 M K₃Fe(CN)₆, and 0.001 M K₄Fe(CN)₆ for various times at room temperature. [CAUTION: potassium ferricyanide is light sensitive. Photodecomposition releases products that can contain free cyanide. Potassium ferricyanide is also incompatible with acids and liberates HCN]

Characterization: Polymer film thicknesses were determined with a Plasmos SD 2002 ellipsometer operating at a wavelength of 632.8 nm.

AFM imaging was carried out with a NanoScope III Multimode AFM (Digital Instruments, Santa Barbara, CA, USA) operating in contact mode using Si_3N_4 cantilevers (Nanoprobes, Digital Instruments) with an approximate spring constant of 0.32 N·m⁻¹. For height measurements, the scanner was calibrated in z-direction using a calibration standard with known step height.

Low-resolution SEM imaging was carried out with a JEOL JSM-5610 SEM operating in secondary electron detection. High-resolution SEM imaging was carried out with a LEO Gemini 1550 FEG-SEM.

XPS analyses were carried out with a Physical Electronics Quantum2000 equipped with an Al K Alpha monochromatic excitation source (source energy = 1486.7 eV, take-off angle set to 45°), a spherical sector analyzer and a multichannel plate detector (16 detector elements). For the survey scan (pass energy of 224 eV), the X-ray beam was set to 25 W per 100 μ m to scan a total area of 700 μ m × 300 μ m; for element scans (pass energy of 224 eV), the X-ray beam was set to 25 W per 100 μ m to scan a total area of 700 μ m × 300 μ m; for scan a total scan area of 700 μ m × 300 μ m (298 K and 1-3·10⁻⁸ Torr). The sensitivity factors used for C, O, and Si for calculating the atomic concentration are 0.314, 0.733, and 0.368, respectively. The hydrocarbon C(1s) signal at 284.8 eV was used as a reference to correct for surface charging.

5.5 References and notes

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Accurate pattern inversion and pattern transfer techniques for nanolithography

This chapter describes the local oxidation of silicon (LOCOS) as a means to convert resist patterns into inverse patterns of thermal SiO_2 by incorporation of ultrathin functional transfer layers into current fabrication processes. The high transfer accuracy and the excellent feature definition throughout the entire inversion procedure demonstrate the potential of this fabrication procedure in nanolithography. Accurate sub-500 nm deep silicon etching at high anisotropy and high etch selectivity with respect to ultrathin etch masks of thermal SiO_2 illustrates the merits of a systematic etch procedure.



6.1 Introduction

The development of micro- and nanolithography in terms of ultimate resolution, process control and economic performance generally depends upon the continuous investments in research and process development. The technological advancements in high-resolution lithography and post-processing limit the applicability of conventional resist systems due to the stringent requirements on the properties of the resist with respect to exposure sensitivity, contrast, resolution and (plasma) etch resistance. An interesting development during the last decades is the engineering of many novel resists and/or composite resist systems into the lithographic fabrication process to overcome the problems of conventional resist systems.^[1,2] Of technological importance is the invention of multilayer resist systems, allowing a decoupling of the imaging and resolution aspects of pattern transfer of high-resolution lithographic techniques (deep-UV (DUV) and extreme-UV (EUV) photolithography, scanning electron beam and ion beam lithography). Generally, a multilayer resist system comprises a thin radiation-sensitive top layer (for resolution purposes) plus a thin intermediate inorganic layer (for plasma etch resistance), or a radiation-sensitive etch-resistant resist, in combination with a thick organic base layer (for planarization and feature aspect ratio purposes). After exposure and development of the high-resolution features in the top layer, successive plasma etch steps of the intermediate and base layer are necessary to transfer the final pattern into the substrate by any additive (e.g., metal lift-off) or subtractive (e.g., reactive ion etching) technique.

As a non-photolithographic technique, nanoimprint lithography (NIL)^[3] is not prone to many of the factors that limit the resolution of these high-resolution lithographic techniques such as diffraction, scattering and interference, backscattering from a substrate, or from the chemistry of the resist and the developer. Actually, NIL is the most promising technique for becoming the next-generation high-resolution lithographic tool of the semiconductor industry owing to its wafer-scale patterning capability at sub-10 nm resolution.^[4] The reintroduction of multilayer resist systems in NIL^[5,6] 25 years after its invention seems surprising, however, this has been done for two main reasons: (i) to generate a proper undercut to facilitate metal lift-off, and (ii) to increase the aspect ratio of the polymer features. However, the advantage of using multilayer resist systems in their present form comes at the expense of process complexity arising from the increase in the number of plasma processing steps. Additionally, the accurate transfer of high-resolution features into silicon necessitate the exact formulation of an anisotropic etch procedure capable of etching silicon at a low etch rate and at a high etch selectivity with respect to mask materials.

This chapter demonstrates the successful implementation of the local oxidation of silicon (LOCOS)^[7] and ultrathin functional transfer layers to convert resist patterns into inverse patterns of thermal silicon oxide (SiO₂) and a systematic procedure for the

anisotropic etching of silicon. It is of great importance that the entire lithographic procedure is applicable to primary nanolithographic techniques as NIL (see Chapter 7, fabrication of 2D-photonic crystal waveguides (2D PhCWGs) on silicon-on-insulator (SOI)) and compatible with the conventional semiconductor device fabrication technology. The transfer accuracy and feature definition during the successive transfer steps are the most important output factors in the process evaluation by high-resolution scanning electron microscopy (SEM). Therefore, emphasis was put on the systematic use of design rules for the present lithographic procedures in terms of pattern transfer fidelity, etch rate and selectivity.

6.2 Results and discussion

6.2.1 LOCOS for pattern inversion into thermal SiO2

LOCOS is a well-known isolation technique^[8] in semiconductor device fabrication technology, patented by Philips in the 1960s after the observation that the oxidation rate of silicon is much higher than that of silicon nitride. Conventional LOCOS concerns the selective oxidation of silicon using thin silicon nitride layers as oxidation masks and is compatible with many existing technologies. The resulting field oxides serve as masks during impurity diffusion (i.e., doping) of the active areas in device fabrication. The reduction in dimension losses in LOCOS to field oxides has been the biggest challenge for process engineers to scale LOCOS into the sub-micrometer regime due to the lateral oxide encroachment under the silicon nitride mask and field oxide thinning,^[9] resulting in the development of many LOCOS type isolation techniques during the last decades.^[10]

6.2.1.1 Design rules for the transfer layers

By setting proper design rules, conventional LOCOS can still be applicable as a technique to convert resist patterns into inverse patterns of thermal SiO_2 on the nanometer scale. Scheme 6.1 illustrates this LOCOS pattern inversion procedure.



Scheme 6.1 Schematic representation of the LOCOS pattern inversion procedure in 3 steps from an arbitrary resist pattern: (1) wet-chemical etching of LPCVD-SiO₂ in 1% HF, (2) wet-chemical etching of Si₃N₄ in 85% H₃PO₄ (180 °C), and (3) dry oxidation of silicon.

A successive deposition of ultrathin layers of stoichiometric silicon nitride (Si_3N_4) and SiO_2 by low-pressure chemical vapor deposition (LPCVD) makes any substrate compatible for this LOCOS pattern inversion procedure. These two layers have the following function:

- LPCVD-SiO₂ layer: sacrificial transfer layer to convert resist patterns into more etch-resistant inorganic patterns, as resists are incompatible with the strong oxidation conditions necessary for direct patterning of the Si₃N₄ layer;
- Si_3N_4 layer: oxidation transfer layer to invert the patterns into thermal SiO₂.

The design rules set limits to the process parameters in terms of layer thicknesses and etch selectivities of the different materials (i.e., LPCVD-SiO₂, Si₃N₄ and thermal SiO₂), and oxidation resistance of the Si₃N₄. The successive pattern transfer and final inversion to thermal SiO₂ requires the use of ultrathin layers (~10 nm) in order to

minimize dimension losses due to the isotropic nature of wet-chemical etching and lateral oxide encroachment, respectively. Investigation of the etch rates of the different materials in 1% hydrogen fluoride (HF) and in 85% phosphoric acid (H₃PO₄) at 180 °C was necessary in order to optimize the inversion procedure in terms of etch selectivity. The intrinsic etch rates of LPCVD-SiO₂, Si₃N₄ and thermal SiO₂ in 1% HF and in 85% H₃PO₄ at 180 °C are shown in Table 6.1.

Table 6.1. Intrinsic etch rates of LPCVD-SiO₂, Si₃N₄, and thermal SiO₂ in 1% HF at room temperature and in 85% H₃PO₄ at 180 °C, including the etch rates of the materials after 60 min thermal annealing under dry nitrogen at three different temperatures.

	Etch rates [nm·min ⁻¹] ^[11]							
	intrinsic		800 °C annealing		900 °C annealing		1050 °C annealing	
	HF	H ₃ PO ₄	HF	H ₃ PO ₄	HF	H ₃ PO ₄	HF	H ₃ PO ₄
LPCVD-SiO ₂	31	2.9	23	1.1	12	0.55	4.6	0.26
Si_3N_4	0.30	4.2	0.29	4.1	0.24	4.2	0.15	3.9
thermal SiO ₂	4.2	0.25	-	-	-	-	-	-
Si	-	0.3 ^[12]	-	-	-	-	-	-

Etch selectivity of the mask material over the target material is important as it allows over-etching to ensure complete removal of the target material in a particular etch solution and complete removal of the mask material afterwards in another etch solution.^[13] The etch conditions were set to over-etch the LPCVD-SiO₂ and Si₃N₄ layers by 50% with respect to the (theoretical) etch time. As the pattern inversion procedure requires two consecutive etch steps, including LPCVD-SiO₂ as the target material and mask material, the etch selectivities of LPCVD-SiO₂ to resist (in 1% HF) and of Si₃N₄ to LPCVD-SiO₂ (in 85% H₃PO₄ at 180 °C) must therefore be taken into consideration at the same time.

The intrinsic etch selectivity of Si_3N_4 over LPCVD-SiO₂ in 85% H₃PO₄ at 180 °C (Table 6.1, selectivity of 1.4) was insufficient to allow substantial over-etching due to a relatively large intrinsic LPCVD-SiO₂ etch rate. Previous studies on the material

properties of LPCVD-SiO₂ have shown that high-temperature annealing can cause a strong reduction of the LPCVD-SiO₂ etch rate due to reduction of the layer porosity by desorption of internal water and structural rearrangements within the LPCVD-SiO₂ layer as shown by infrared spectroscopy.^[14] The effect of thermal annealing on the etch rates of LPCVD-SiO₂ (and Si₃N₄) in the two etch solutions is also shown in Table 6.1 (and Fig. 6.1a) at three different annealing temperatures.



Figure 6.1 Etch rate dependence of LPCVD-SiO₂ in 1% HF and 85% H_3PO_4 (180 °C) as a function of the annealing temperature (a) and the corresponding decrease in layer thickness (b).

The corresponding decrease in thickness of the LPCVD-SiO₂ layers upon thermal annealing at 800, 900 and 1050 °C was found to conform to a linear correlation with the resulting LPCVD SiO₂ etch rate in both etch solutions (Fig. 6.1b). In accordance with the literature, the LPCVD SiO₂ etch rate approximates the etch rate of thermal SiO₂ at high-temperature thermal annealing (Table 6.1, $T_{anneal} = 1050$ °C). The results in Table 6.1 illustrate that the etch selectivity of Si₃N₄ over LPCVD-SiO₂ in 85% H₃PO₄ (180 °C), as also shown in Fig. 6.1a, is in fact only susceptible to thermal annealing by the LPCVD-SiO₂ etch rate. The resulting etch selectivity of Si₃N₄ over

LPCVD-SiO₂ in 85% H₃PO₄ (180 °C) serves the over-etching process condition at any of the thermal annealing temperatures. As a result, the etch rate of LPCVD-SiO₂ in 1% HF was the decisive factor in the engineering process of the present lithographic procedure. It was found to decrease with annealing temperature (Fig. 6.1a). Thermal annealing at 800 °C was chosen after taking into account that the ultimate aim is to exploit this novel inversion process in thermal NIL of 2D PhCWGs on SOI, i.e., the resulting LPCVD-SiO₂ etch rate in 1% HF has to tolerate patterning of this transfer layer within the resistance time (< 1-2 min) of sub-100 nm thick polymer imprints to 1% HF (see Chapter 7). With the etch rate and selectivity set, the last parameter that determines the final layer thickness of the transfer layer is the oxidation resistance of Si₃N₄ in dry oxidation conditions. Fig. 6.2 illustrates the etch plot of a Si₃N₄ layer in 1% HF after a 10 h oxidation run at 950 °C.



Figure 6.2 Etch rate dependence of a Si_3N_4 layer in 1% HF after a ten hour oxidation run at 950 °C. All data points come from ellipsometer measurements on the same substrate after successive etching in 1% HF. For the measurements, due to the heterogeneous nature of the layer after oxidation, the refractive-index of Si_3N_4 (n = 2.00) was taken to estimate (open dots) or determine (solid dots) the total layer thickness.

The partial conversion of Si_3N_4 into silicon oxynitride (SiN_xO_y) under dry oxidation conditions is known in literature.^[15,16] Estimation of the oxidation resistance is feasible

due to the large drop in the etch rate in 1% HF due to the heterogeneous nature of the layer after oxidation. The intrinsic etch rate of ~ 2 nm·min⁻¹ drops by a factor of 20 to about 0.1 nm·min⁻¹, indicating entry to the core Si_3N_4 (Fig. 6.2).

Ellipsometer measurements on a second substrate were carried out to estimate the thickness of the top (SiN_xO_y) layer using a two-layer model with the thickness of the core Si_3N_4 and the refractive-indices of Si_3N_4 (n = 2.00) and SiO_2 (n = 1.465) as input parameters. The top layer was found to range from 1.2 nm to 1.4 nm, indicating the lower limit of the thickness of the Si_3N_4 layer to resist oxidation. However, the present LPCVD system limits the layer thickness of the Si_3N_4 layer to about 12 nm.^[17] The conditions for the low-pressure chemical vapor deposition (LPCVD) of the two transfer layers were set to result in a layer thickness of about 12 nm.

6.2.1.2 Pattern inversion of resist patterns into thermal SiO2

After an introductory standard cleaning procedure of the 4-inch silicon wafers and native oxide removal in 1% HF, immediate deposition of very thin layers of Si₃N₄ (14 nm) and LPCVD-SiO₂ (12 nm) was performed by LPCVD in order to prevent re-oxidation of the silicon to native oxide. Prior to processing the substrate by conventional photolithography, the wafers were annealed under dry nitrogen at 800 °C for 60 min. After exposure and development of the mask patterns (hexagonal arrangement of 100 µm circular features at 200 µm periodicity) into positive photoresist (Fig. 6.3a), transfer of this pattern into LPCVD-SiO₂ was done by etching in 1% HF for 1 min (Fig. 6.3b). After a subsequent photoresist strip in acetone/ isopropanol, the LPCVD-SiO₂ pattern was transferred into Si₃N₄ by etching in 85% H₃PO₄ at 180 °C for 5 min. The resulting Si₃N₄ oxidation mask for LOCOS is shown in Fig. 6.3c after stripping the LPCVD-SiO₂ in 1% HF for 2 min. The low intrinsic etch rates of the underlying layers, i.e., Si₃N₄ in 1% HF and silicon in 85% H₃PO₄ (Table 6.1) during the patterning of respectively LPCVD-SiO₂ and Si₃N₄, allow substantial over-etching without the problem of undercutting and/or damage of the underlying layer. Feature dimension analysis of nanometer-scale features in Chapter 7 illustrates a total increase of about 20 nm in feature dimension (= 8.6 % of the initial feature dimensions) during the successive transfer of the resist pattern into the underlying two transfer layers. Finally, the substrates were put in a dry

oxidation furnace at 950 °C to perform the actual pattern inversion step by LOCOS using the ultrathin Si_3N_4 patterns as oxidation masks. Under these conditions, the oxidation rate of silicon into thermal SiO_2 was about 1.2 nm·min⁻¹, resulting in 14 nm of thermal SiO_2 after 12 min oxidation.^[18] The resulting thermal SiO_2 etch mask after stripping the Si_3N_4 in 85% H₃PO₄ at 180 °C is shown in Fig. 6.3d.



Figure 6.3 SEM images of 100 μ m circular features in a hexagonal pattern at a periodicity of 200 μ m: circular holes in photoresist Olin 907 (a), in LPCVD-SiO₂ (b), and in Si₃N₄ (c) and inverse circular features in thermal SiO₂(d).

The entire inversion procedure was found to occur with very high accuracy due to the lateral encroachment of thermal SiO₂ under the oxidation mask (deviation in lateral feature dimensions of < 4 nm, see Chapter 7). The time to strip the Si₃N₄ oxidation mask from the substrate after LOCOS took longer than the theoretical etch time (Table 6.1) due to partial oxidation of Si₃N₄ during the oxidation run in the furnace. An additional 2 min strip (at a total of 7 min) was necessary to overcome the initial etch resistance to 85% H₃PO₄ at 180 °C, using the hydrophobicity of the silicon as a visual etch stop during rinsing of the substrates with water. As a result, the final thermal SiO₂ etch mask was about 12 nm thick. Complete removal of the Si₃N₄ oxidation mask was carried out just before silicon etching to prevent re-oxidation of the silicon to native oxide.

6.2.2 Profile control by cryogenic etching

The requirements of anisotropic etching on the sub-500 nm scale using ultrathin (12 nm) thermal SiO_2 etch masks are stringent. These dimensions necessitate the development of a suitable anisotropic etch recipe that combines a low silicon etch rate with a silicon to thermal SiO₂ etch selectivity of at least 30 for achieving accurate silicon etching. For several reasons, the Oxford Plasmalab 100 plus system was chosen to cryogenically etch the final patterns into silicon. This system includes helium backside cooling by liquid nitrogen in combination with a mechanical clamping tool that allows efficient control of the temperature over the entire 4-inch wafer. Additionally, the dual-power plasma source of this system enables independent control of the plasma density (set by the inductive coupled plasma (ICP) power) and bias voltage (set by the capacitive coupled plasma (CCP) power) that affect, respectively, the chemical (by radicals) and physical (by ions) components of the silicon etching. The following describes the procedure to formulate a suitable recipe by complying with the guidelines for profile control at cryogenic temperatures using SF₆/O₂ plasmas.^[19] At first, some of the input parameters were intentionally set for specific reasons: (i) ICP power of 550 W (= minimum ICP power of the etch system) in order to generate a low plasma density, resulting in low silicon etch rates, (ii) process pressure of 10 mTorr in order to achieve directional ion bombardment during etching, and (iii) electrode temperature of -120 °C in order to obtain high etch selectivity. After setting these input parameters, the flow of SF_6 and O_2 and the CCP power are the only active input parameters necessary to tune the profile. The second stage was to determine the (spontaneous) silicon etch rate as a function of the SF₆ flow on bare 4-inch silicon wafers.

Fig. 6.4a illustrates that the silicon etch rate in this particular etch regime is proportional to the SF₆ flow at a rate dependence of 20 nm·min⁻¹·sccm⁻¹. One drawback and limiting factor of this system is the quartz dome construction inside the reactor. Erosion of this type of dome during etching is known to cause oxygen emission, thereby affecting the etching properties of the plasma.^[20] As a result, the third stage was to quantify this effect by determining the silicon etch rate as a function of the CCP power.



Figure 6.4 Plot of the etch rate dependence on different parameter settings using the Oxford Plasmalab 100 plus system. General parameter settings: 10 mTorr, 550 W ICP power, -120 °C (no O_2 addition). (a) Silicon etch rate as a function of SF₆ flow at 6 W CCP power. (b) Silicon and thermal SiO₂ etch rates as a function of CCP power at 30 sccm SF₆. Etch rate determination was done on 4-inch wafers by weight measurement in case of silicon and by ellipsometry in case of thermal SiO₂. The resulting etch rates apply to a 100% load.

Fig. 6.4b illustrates that the passivation by oxygen from the dome was only effective at low CCP powers (CCP power < 6 W). At higher CCP powers (> 6 W), the silicon etch rate was independent of the CCP power due to the fact that the corresponding higher bias voltages overpower the threshold energy for physical sputtering of the oxygen-containing passivation layer. In contrast, the etch rate of the thermal SiO₂ was dependent on the CCP power over the entire regime, as shown in Fig. 6.4b, as erosion of this masking material is physical by nature. On the basis of these findings, the profile test experiments were carried out on a 4-inch wafer scale using 12 nm thick patterns of thermal SiO₂ as etch masks (for the fabrication, see Fig. 6.5 (left)).

Fig. 6.5 (profile I) illustrates a positive profile under the starting etch conditions (30 sccm SF₆, no O₂ addition, 6 W CCP power), indicating that the oxygen content in the plasma was already sufficient to cause strong sidewall passivation. Following the guidelines for profile control,^[19,20] tuning of this profile into a more directional profile (Fig. 6.5, profile II) is possible by either increasing the SF₆ flow (Fig. 6.5, step 1b) or the CCP power (Fig. 6.5, step 1a):^[21] increasing the SF₆ flow (at constant pressure) reduces the strength of the passivation layer, whereas increasing the CCP power intensifies the sputtering of this passivation layer.



Figure 6.5 Schematic representation of the entire profile test experiment (left) and the resulting diagram of the procedure for ultimate profile control including high-resolution SEM cross-sectional profiles (right). The diagram illustrates different possible etch profiles:^[22] positive profile (I), directional profile (II and III), and semi-isotropic profile (IV).

However, Fig. 6.6 illustrates that the resulting etch selectivity of silicon over thermal SiO₂ is different in these two cases, due to the difference in etch rates of silicon and thermal SiO₂ (Fig. 6.4). Additionally, Fig. 6.6 illustrates that, at the set conditions in stage 1 (550 W ICP power, 10 mTorr, -120 °C), there is a large process latitude in the cryogenic etching of silicon in terms of the selectivity of silicon over thermal SiO₂, i.e., in the present range of CCP power (6-12 W) and SF₆ flow (30-50 sccm) the selectivity is sufficient to formulate an anisotropic etch recipe with a suitable selectivity (> 30). An increase of any of the two parameters out of this range was found to result in semi-isotropic profiles (Fig. 6.5, step 3a/b and profile IV).



Figure 6.6 Etch selectivity of silicon over thermal SiO_2 as a function of the SF_6 flow (30-50 sccm) and CCP power (6-12 W). Selectivities in this etch regime vary from 50 (at 30 sccm SF_6 and 12 W CCP power) up to 225 (at 50 sccm SF_6 and 6 W CCP).

Fig. 6.5 and 6.6 indicate that increasing the SF₆ flow or the CCP power will result in two different anisotropic etch recipes. The former action results in a high etch selectivity at the expense of high silicon etch rates, whereas the latter action results in a low silicon etch rate at the expense of low etch selectivities. Upon considering that the target is to formulate a recipe with a low silicon etch rate, preference was given to increase the CCP power first for substantial tuning (Fig. 6.5, step 1a and 2: + 6 W) of the profile prior to increasing the SF₆ flow (+ 4 sccm) for final fine-tuning of the profile. Conditions necessary for close-to-anisotropic etching (Fig. 6.5, profile III) were found using this systematic procedure with the following conditions: 10 mTorr, 550 W ICP power, 12 W CCP power, 34 sccm SF₆, -120 °C (quartz dome). Using these settings, the silicon etch rate is about 600 nm·min⁻¹ and the selectivity of silicon over thermal SiO₂ is about 50.

6.3 Conclusions

The results in this chapter illustrate that the present LOCOS pattern inversion procedure to convert resist patterns into inverse patterns of thermal SiO₂ is very accurate in terms of pattern transfer accuracy (total deviation in lateral dimension of $< 4 \text{ nm}^{[23]}$) and feature definition owing to the use of ultrathin functional layers, wetchemical etching and process engineering of the etch rate and selectivity. This lithographic procedure offers advantages over the conventional multilayer resist systems for metal lift-off in terms of transfer accuracy and process complexity (no need for undercut profile, no successive plasma etch steps) in case the resulting pattern serves as an etch mask for the anisotropic etching of silicon.

One technological improvement in the present inversion procedure includes the use of hydrogen silsesquioxane (HSQ) instead of the combination of photoresist/ LPCVD-SiO₂. HSQ (structural formula: H₂(SiO)₃) is commercially available in solution as FOX[®] flowable oxide (Dow Corning^[24]) and is of interest for four reasons: (i) potential application in room-temperature NIL (RT-NIL),^[25] pattern alignment and step-and-repeat patterning, (ii) high etch resistance, (iii) tunable properties by thermal curing,^[26] and (iv) effective reduction of the number of transfer layers.

Finally, the cryogenic RIE of silicon in the sub-500 nm scale using the ultrathin thermal SiO_2 etch masks was successful by application of a systematic etch procedure to formulate a suitable anisotropic etch recipe (silicon etch rate of 600 nm·min⁻¹ at etch selectivity with respect to thermal SiO₂ of about 50).

6.4 Experimental

Chemicals and materials: All chemicals and materials were purchased from commercial resources: 4-inch OSP <100> p-type silicon wafers (Okmatic), Olin 907/12 (Fujifilm Arch), hexamethyldisilazane (HMDS) (100% Selectipur: Merck), OPD 4262 resist developer (Arch Chemicals), HNO₃ (100% Selectipur: Merck, and 69% VLSI: Merck), HF (1% VLSI: Merck), H₃PO₄ (85% VLSI, Merck), acetone and isopropanol (Merck, VLSI), tetraethylorthosilicate (TEOS) (Schumacher Company), dichlorosilane (SiH₂Cl₂) (PraxAir, 2.0) and NH₃ (PraxAir, 5.0). All experiments were carried out in cleanroom environment.

Substrate preparation: Prior to processing the silicon wafers, the native oxide was removed in 1% HF for 60 s. Afterwards, a very thin layer of stoichiometric silicon nitride (Si₃N₄, 14 nm) was deposited by low-pressure chemical vapor deposition (LPCVD, Tempress LPCVD furnace: 70 sccm SiH₂Cl₂, 18 sccm NH₃, 850 °C, 200 mTorr, 150 s deposition time) with minimal delay. On top of this layer, a very thin layer of LPCVD-SiO₂ (12 nm) was deposited by LPCVD (Tempress LPCVD furnace: 50 sccm TEOS, 700 °C, 400 mTorr, bubbler at 40 °C, 90 s deposition time). After the two LPCVD runs, the wafers were annealed under dry nitrogen at 800 °C for 60 min in a furnace (Tempress, dry oxidation furnace) with minimal delay.

Fabrication of resist patterns: Resist patterns were fabricated on the 4-inch wafer scale by conventional optical lithography (ElectroVisions Group mask aligner (EVG), model EV620). The wafers were cleaned by a standard cleaning procedure (10 min in 70% HNO₃, 10 min in 100% HNO₃ at 95 °C). A dehydration bake on the hotplate (120 °C, 5 min) was applied on the wafers before priming with HMDS. Spin-coating of the positive photoresist Olin 907/12 was done at 4000 rpm for 20 s. A softbake (95 °C, 60 s) was preceding the optical exposure of the resist (3 s, 12 mW·cm⁻², hard contact with 0.7 bar contact pressure) through a mask. After a post-exposure bake at 120 °C for 60 s, the resist was developed by mild agitation in OPD 4262 resist developer for 50 s.

Local oxidation of silicon (LOCOS): LOCOS requires the transfer of the photoresist pattern into two different layers: first into the LPCVD-SiO₂ layer by etching in 1% HF for 45 s and, after removal of the PMMA from the substrates in acetone/ isopropanol, into Si₃N₄ by etching in hot H₃PO₄ of 180 °C for 5 min. After removal of the LPCVD-SiO₂ mask in a 1% HF solution for 2 min, the silicon was locally oxidized in a dry oxidation furnace (Tempress, dry oxidation furnace, atmospheric pressure, 4000 sccm O₂, 950 °C, 12 min) using the Si₃N₄ as the oxidation mask. Removal of the Si₃N₄ mask in H₃PO₄ at 180 °C for 7 min was performed just before the cryogenic etching of the silicon to prevent the formation of native silicon oxide.

Cryogenic reactive ion etching of silicon: Silicon etching in the sub-500 nm scale was carried out on the Plasmalab 100 plus system from Oxford Instruments. After loading the wafer into the etch reactor, a holding time for etching was set to 2 min for the system to equilibrate the temperature of the substrate. For the formulation of an

anisotropic etch recipe, the resulting cross-sectional profiles were examined by highresolution SEM after etching and manual cleavage of the silicon wafer. After setting the ICP power (550 W), the pressure (10 mTorr) and the temperature (-120 °C), adjustment of the SF₆ flow and CCP power was done systematically to reach the anisotropic profile. Parameter settings for the anisotropic etching of silicon are found at 10 mTorr, 550 W ICP power, 12 W CCP power, 34 sccm SF₆, -120 °C. At these settings, the silicon etch rate is about 600 nm·min⁻¹ and the selectivity of silicon over thermal SiO₂ is about 50.

Characterization: Layer thicknesses on silicon were determined with a Plasmos SD 2002 ellipsometer operating at a wavelength of 632.8 nm. The weight measurements were carried out on a Sartorius weighing scale with an accuracy of 0.1 mg (conversion factor: 1 mg = 55 nm). Low-resolution SEM imaging was carried out with a JEOL JSM-5610 SEM operating in secondary electron detection. High-resolution SEM imaging was carried out with a LEO Gemini 1550 FEG-SEM operating in the INLENS mode for secondary electron detection.

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Fabrication, replication and characterization of 2D photonic crystal waveguides^{*}

This chapter describes a novel inverse imprinting procedure for the fabrication and replication of two-dimensional photonic crystal waveguides with a transfer accuracy and feature definition that is comparable to state-of-the-art nanofabrication techniques. Local examination using photon scanning tunneling microscopy (PSTM) demonstrates that the resulting nanophotonic structures have excellent guiding properties at wavelengths in the telecommunications range, indicating a high quality of the local structure and the overall periodicity.

7.1 Introduction

Photonic crystals (PhCs)^[1,2] have been a fascinating and active field of research in the past decade owing to the ability of these artificial nanostructures to influence the generation and propagation of light.^[3,4] In particular, due to the periodic dielectric

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structure, PhCs may prohibit the propagation of photons of a certain range of frequencies (known as the photonic band gap). The flexibility to tune the optical properties by changing the local geometry in PCs results in novel types of filters, couplers, lasers,^[5] waveguides,^[6] etc. For example, by incorporating structural line defects within a (perfect) two-dimensional PhC in thin dielectric slabs one can fabricate low-loss two-dimensional photonic crystal waveguides (2D PhCWGs). The strong confinement of light in the line defect of 2D PhCWGs in principle surpasses the guiding properties of conventional waveguides in terms of propagation losses owing to the in-plane band gap confinement. The fabrication of these 2D PhCWGs on oxide claddings of commercially available high-quality silicon-on-insulator (SOI) wafers has become the most dominant in scientific research as it allows the direct e-beam lithography (EBL)^[7] and deep-UV (DUV) lithography,^[8] for high-resolution pattern generation in combination with any deposition and/or dry etch tool to transfer the final devices into SOI.^[9]

Up to now, most research has been focusing on the theoretical and experimental investigation of the guiding properties of 2D PhCWGs in terms of specific design rules of different elements of 2D PhCWGs, propagation losses, and light coupling efficiencies. Less attention has been given to the low-cost fabrication of 2D PhCWGs so far. Generally, nanophotonic devices pose very stringent requirements with regard to the quality of the nanofabrication technique. Not only does the definition of the local features, typically nanoholes in a background of high-index material, need to be perfect, but also the period of the crystal lattice has to be constant throughout the entire device. While local imperfections will lead to scattering losses, long-range imperfections, such as a non-constant period, will lead to undesirable variations in the propagation constant and potentially in reflections and out-of-plane losses. From the set of emerging nanofabrication techniques, nanoimprint lithography (NIL)^[10] has the highest potential for becoming the next-generation high-resolution lithography because of its high transfer accuracy, cost effectiveness and high throughput even for structures that require sub-10 nm resolution. The research and development of NIL as a tool in the fabrication process of 2D PhCWGs is only very recent. After the first successful demonstration of low-cost replication of PhCWGs in polymers by thermal

NIL^[11] in 2005, only two research groups have put effort into the replication of 2D PhCWGs on SOI by exploiting NIL.^[12,13]

The research in this chapter elaborates on the LOCOS inversion procedure and design rules set in Chapter 6, resulting in the highest pattern transfer accuracy (in terms of feature dimension loss and feature definition) to date in the replication of 2D PhCWGs into SOI. The novelty with respect to the current multilayer resist systems^[14,15] lies in the fact that this procedure implements, for the first time, local oxidation of silicon (LOCOS),^[16] a microfabrication isolation technique for very-large-scale integration (VLSI), in the inversion of NIL resist patterns into thermal silicon oxide (SiO₂). As the pattern transfer accuracy was the most important output factor in the entire process evaluation, high-resolution scanning electron microscopy (SEM) imaging of the resulting pattern was carried out after every process step in the replication procedure. Additionally, photon scanning tunneling microscopy (PSTM) was carried out in order to study the 2D PhCWG in more detail by capturing the propagation of light inside the line defect of the 2D PhCWG to determine the actual propagation losses and band structure.

7.2 Results and discussion

7.2.1 Structural design of the 2D PhCWG

The structural design of the 2D PhCWG was taken from the excellent work of Notomi^[17] on single-mode transmission within the PBG of narrow line-defect PhCWGs on SOI. Apart from setting up several guidelines for structural tuning of the waveguide modes of line-defect PhCWGs by theoretical analysis of the dispersion curve of these modes within the PBG,^[18] superior single-mode transmission efficiencies for near-infrared light of transverse-electric (TE) polarization in the telecommunications range have been experimentally shown for two novel line-defect PhCWG structures on SOI. This was the result after structural optimization of the line defect^[19] and (ii) by addition of smaller holes inside the single line defect of the 2D PhCWG.^[20] Fig. 7.1a shows the schematic representation of the structural

design of the 2D PhCWG used in this study, which is close to the narrow line-defect 2D PhCWG of Notomi.^[19a]



Figure 7.1 (a) Structural design of the 2D PhCWG. (b) Theoretical dispersion curve of this specific 2D PhCWG by calculation using a 2D plane-wave expansion method. The gray lines represent the TE-like waveguide modes, with the transmission mode in black. The light line of the SiO_2 cladding layer indicates the border of the region inside the light cone of the cladding layer. The light gray areas correspond to the region outside the measurement range of the tunable laser.

The 2D PhCWGs consist of a triangular lattice of air holes of diameter *d* at a lattice constant *a* perforating a 200 nm thick silicon slab that is on top of 2 µm thick SiO₂ cladding layer (Fig. 7.1a). Ridge waveguides (2 µm wide) connect the 5×22 µm² large 2D PhCWG to form the waveguide outline structure. At the fabrication stage of generating these waveguide outline structures, a lateral distance of 300 µm in between the waveguides was set in the optical mask design for photolithography (Section 7.2.2). The line defect is along the Γ – K crystal direction of the 2D PhCWG and has a line-width *W* that is 70% (α = 0.7) of a normal single-line defect. This was done at the pre-fabrication stage of generating a stream file for FIB (Section 7.2.2) by removing a single line of air holes and shifting the structures in perpendicular direction with respect to the line defect.

A recalculation of the dispersion curve was necessary for the present 2D PhCWG as the specifications of the SOI substrate were different from the 2D PhCWG of

Notomi ($t_{Si} = 200$ nm and $t_{SiO_2} = 3 \mu m$) due to the availability of commercial SOI wafers at SOITEC. The geometric parameters of the final 2D PhCWG on SOI (see Section 7.2.3) were taken into account in this calculation. Fig. 7.1b illustrates the dispersion curve of the waveguiding modes using the 2D plane-wave expansion method of Johnson and Joannopoulos^[21] in combination with effective refractive-index calculations.^[22] Calculations predict a guiding mode for TE polarization (electrical field in the plane of the crystal) under the light line with a transmission band between $\omega = 0.258$ and 0.264. This transmission band is sufficient to propagate light of TE polarization inside the line defect of the 2D PhCWG and lies within in the frequency range of the tunable laser of the PSTM setup (see Section 7.2.4).

7.2.2 Fabrication of the nanoimprint mold

The fabrication process starts with the fast prototyping of silicon molds containing 2D PhCWGs combining conventional optical lithography to define the micrometer-scale ridge waveguides on larger areas and focused ion beam (FIB) milling to locally introduce the photonic crystal structure into the corresponding section within the waveguide outline structures (Scheme 7.1).



Scheme 7.1 Schematic representation of the lithographic procedure for the fabrication of thermal NIL molds: (a) exposure of positive photosensitive resist through a mask containing the outline structures of the waveguides and subsequent resist development, (b) wet-chemical etching of the native SiO_2 , and removal of the photoresist patterns, (c) anisotropic wet-chemical etching of <110> silicon exploiting the native SiO_2 as the etch mask, and (d) local FIB milling of the photonic elements. After an introductory standard cleaning procedure and dehydration bake, HMDS and positive photosensitive resist Olin 907 were applied on the silicon wafer by spincoating. After a soft bake, the resulting 1.2 μ m-thick resist layer was exposed by UV light through a mask containing the waveguide outline structures. Considering the anisotropic wet-chemical etching in this procedure, care was taken to align the direction of the ridge waveguides on the mask with the primary flat of the <110> silicon wafer prior to exposure of the resist. After a post-exposure bake, the resist was developed in OPD 4262 resist developer (Scheme 7.1a), resulting in the waveguide outline patterns shown in Fig. 7.2a.



Figure 7.2 SEM images of the waveguide outline structures at two different stages of the fabrication procedure: (a) after exposure and development of Olin 907 and (b) after anisotropic etching into silicon.

The most important parameter for consideration in this fabrication procedure is the type of etch technique to transfer photoresist waveguide patterns into silicon as any surface roughness could potentially cause detrimental losses for light propagation through the resulting 2D PhCWGs.^[23] One potential silicon etch technique that is known to result in ultralow surface roughness is the anisotropic wet-chemical etching of silicon using aqueous basic solutions like commercially available resist developer OPD 4262.^[24] According to this study, OPD 4262 was superior to other aqueous basic etch solutions with respect to surface finish (surface roughness, R_a < 0.5 nm). At room temperature, a silicon etch rate in the <110> direction, i.e., the direction perpendicular to the wafer surface of the <110> silicon wafer, of 3.7 nm·min⁻¹ was found along with an excellent etch uniformity (± 1 nm) over the entire 4-inch wafer and a selectivity of (at least) 250 with respect to SiO₂ as the etch mask.^[25] As a result of the high etch selectivity, native SiO₂ was found to be a suitable etch mask in the present procedure to anisotropically etch the resist patterns into the top silicon layer of

the SOI substrate in OPD 4262. The resist patterns were transferred into native SiO₂ by etching in 1% HF (Scheme 7.1b). One notable finding in initial etch experiments was that some commercial photoresists are incompatible with 1% HF (e.g., image reversal resist Ti09 XR (MicroChemicals) is in positive mode only compatible with 1% HF after additional UV crosslinking), resulting in a failure to etch the waveguide outline structures into silicon in the next step due to the complete removal of the native SiO₂. After resist removal in acetone and cleaning of the wafer in isopropanol, the waveguide outline structures were transferred 100 nm into the silicon wafer by wet-chemical etching in OPD 4262 using the native SiO_2 as etch mask (Fig. 7.2b, Scheme 7.1c). At the correct alignment of the waveguide structures on the <110> silicon wafer, anisotropic wet-chemical etching occurs in the direction perpendicular to the wafer surface resulting in waveguide structures with perfect vertical sidewalls. After a second standard cleaning procedure, the photonic crystal structures were written into the resulting silicon waveguide outline structures of the silicon mold by FIB milling (Scheme 7.1d). The final silicon mold, consisting of narrow (W0.7) line-defect 2D PhCWGs with a total length of about 22 µm (or 56*a*) in between 2 µm-wide ridge waveguides, is shown in Fig. 7.3.



Figure 7.3 SEM images of the final NIL mold after FIB writing of the photonic element into the waveguide outline structures ($d = 228 \pm 4 \text{ nm}$, $a = 396 \pm 6 \text{ nm}$).^[26]

Several initial milling tests were done to determine (i) the offset after loading and positioning of the stream file pattern with the actual milling position in the patterning field and (ii) the magnification that matches the total lateral dimensions of the resulting photonic pattern to the dimensions set in the stream file (see Experimental). Accurate positioning and milling of the photonic elements within the silicon waveguide outline structures was the result after correction for the offset at positioning of the stream file pattern onto the target section within the ridge waveguides. In addition to accurate positioning, the close-up of Fig. 7.3 illustrates the excellent circular definition of the photonic elements. Feature dimension analysis of the silicon mold illustrates that the feature diameter was about 14 nm larger than the design geometry of the PhCWG (Section 7.2.1). Since the periodicity conforms to the design setting, some plausible causes for only the diameter to deviate from the design setting could reside in the current beam overlap^[27] and the feature definitions in the stream file.^[28]

7.2.3 Replication of 2D PhCWGs

The inverse imprinting procedure to replicate the 2D PhCWGs into SOI is shown in Scheme 7.2.



Scheme 7.2 Schematic representation of the lithographic procedure for the replication of the 2D PhCWGs on SOI substrates: (a) thermal NIL in PMMA (and removal of the residual layer by O_2 plasma, (b) wet-chemical etching of LPCVD-SiO₂ using the PMMA imprint as the etch mask, and removal of PMMA, (c) wet-chemical etching of Si₃N₄ and removal of the LPCVD SiO₂ etch mask, (d) dry oxidation of silicon and removal of the Si₃N₄ oxidation, and (e) cryogenic etching of the final 2D PhCWGs in the top silicon layer of the SOI substrate.

The process for the replication of 2D PhCWGs into SOI elaborates on the LOCOS inversion procedure and design rules set in Chapter 6 by applying this procedure to polymer imprint patterns of 2D PhCWGs by thermal NIL. Substrate preparation was done in order to allow for accurate pattern transfer: (i) deposition of two ultrathin transfer layers by low-pressure chemical vapor deposition (LPCVD) (in the order: SOI substrate, silicon nitride (Si₃N₄, 14 nm), LPCVD-SiO₂, (12 nm)) and (ii) high-temperature annealing of the substrate under dry nitrogen to facilitate accurate pattern transfer layers in terms of resolution, etch rates, and etch selectivity.

7.2.3.1 Thermal nanoimprint lithography

Prior to application in thermal NIL, a second standard cleaning procedure was applied to the silicon mold in combination with a dehydration bake. Subsequently, a monolayer of perfluorodecyltrichlorosilane was grown onto the molds by gas phase deposition under vacuum in a desiccator to provide the molds with anti-adhesion properties. Spin-coating of thermoplastic polymers was done on the substrate at a thickness of ~110 nm. Patterning of the first transfer layer (LPCVD-SiO₂) in 1% HF using such thin polymer etch masks essentially requires HF-impermeable polymers (or high LPCVD-SiO₂ etch rates) in order to exploit the intrinsic high etch selectivity in this etch step. Patterns of PMMA ($M_w = 350$ kDa) and polystyrene (PS, $M_w = 240$ kDa) were only partially resistant to HF, resulting in gradual dissolution of the LPCVD-SiO₂ and detachment of the polymer imprint patterns from the substrate in time (< 60 s). High-molecular-weight PMMA ($M_w = 996$ kDa) was found to resist 1% HF for at least 2 min and was therefore the polymer of choice in the NIL replication step.

For imprinting of the 2D PhCWG structures into this type of PMMA (Scheme 7.2a), the conditions for imprinting were set to a temperature ~80 °C above the glass-transition temperature and a pressure of 50 bar during an imprint time of 2 min. This procedure could be repeated for several tens of imprints without any observable deterioration of the silicon mold. Feature dimension analysis of the resulting 2D PhCWG structures in PMMA and comparison to the feature dimensions

of the NIL mold illustrate the high accuracy (deviation in lateral feature dimensions ~ 4 nm) of thermal NIL to replicate the photonic structures into PMMA (Fig. 7.4).



Figure 7.4 SEM images of the PMMA imprint before removal of the residual layer by O_2 plasma $(d = 232 \pm 4 \text{ nm}, a = 394 \pm 6 \text{ nm}).^{[26]}$

The residual layer was kept as thin as possible by spin-coating PMMA layers that are close in thickness to the imprint depth of the NIL mold. At the very low pattern density of the current NIL mold (< 1%), the difference between the polymer layer thickness and the imprint depth of the mold was taken (to a first approximation) as the thickness of the residual layer. Removal of this intrinsically thin residual polymer layer was achieved by anisotropic O₂ RIE to preserve the lateral dimensions of the PMMA features and, as a result, the accuracy of the present replication process. Temperature control over the electrode was excellent, resulting in a good etch uniformity at an intrinsic etch rate of 8 nm·min⁻¹ (at 100% PMMA loading) over the entire 4-inch wafer (deviation in layer thickness < 2 nm). Substantial over-etching (on the order of 100% with respect to the theoretical etch time) was chosen to assure complete removal of the residual layer and therefore accurate transfer of the imprint pattern into the underlying LPCVD-SiO₂ transfer layer. The O₂ plasma had no significant effect on the surface roughness, as observed by AFM (data not shown) and as was shown before.^[29] For process considerations, the only important side effect of the O₂ plasma treatment is the increase in the hydrophilicity of the PMMA layer. This effect could facilitate etching of the first pattern transfer layer, i.e., LPCVD-SiO₂, in the aqueous etch solution using PMMA as the etch mask.

7.2.3.2 LOCOS inversion procedure

After the removal of the residual layer by O_2 plasma, the PMMA imprint was transferred into LPCVD-SiO₂ by etching in 1% HF (Scheme 7.2b). Subsequent to stripping the PMMA in acetone/isopropanol, the LPCVD-SiO₂ pattern was transferred into Si₃N₄ by etching in 85% H₃PO₄ at 180 °C (Scheme 7.2c). The resulting Si₃N₄ oxidation mask for LOCOS is shown in Fig. 7.5a (after stripping the LPCVD-SiO₂ in 1% HF).



Figure 7.5 SEM images of two different stages in the pattern inversion process: (a) after successive transfer of the imprint pattern into Si_3N_4 ($d = 252 \pm 2$ nm, $a = 396 \pm 4$ nm) and (b) after local oxidation of silicon into thermal SiO_2 ($d = 232 \pm 2$ nm, $a = 394 \pm 4$ nm).^[26]

Feature dimension analysis of the Si₃N₄ oxidation mask illustrates a total increase of 20 nm in feature dimensions during the successive transfer of the PMMA imprint pattern into the underlying two transfer layers. Finally, the SOI substrates were put in a dry oxidation furnace at 950 °C for 12 min to perform the pattern inversion by LOCOS using the ultrathin Si₃N₄ patterns as oxidation masks (Scheme 7.2d). The resulting thermal SiO₂ etch mask after stripping the Si₃N₄ in 85% H₃PO₄ at 180 °C is shown in Fig. 7.5b. Feature dimension analysis of the thermal SiO₂ etch mask illustrates that the well-known lateral encroachment of thermal SiO₂ under the Si₃N₄ mask during LOCOS effectively was capable of cancelling the 20 nm increase in

feature dimensions during the preceding etch steps of the transfer layers. The pattern transfer accuracy at this stage of the replication procedure is excellent with a deviation in lateral feature dimensions of < 4 nm.

7.2.3.3 Anisotropic etching of waveguide structures into SOI

The last step in the replication of the 2D PhCWGs was the anisotropic etching into SOI at cryogenic temperatures (Scheme 7.2e). Etching photonic structures through $SiO_2/Si/SiO_2$ substrates in the micrometer range has been shown with this apparatus in previous studies.^[30] Additionally, for accurate etching of smaller SOI substrates (18 mm × 18 mm), efficient thermal contact of the SOI substrate with the substrate electrode of the etch apparatus was achieved by mounting the SOI substrate onto a 4-inch substrate silicon wafer with a thin layer of vacuum grease in between the two surfaces. Just before mounting, the native oxide on the substrate silicon wafer was removed in 1% HF in order to attain close to 100% silicon loading (as in the case of the test experiments).

At a silicon etch rate of ~600 nm·min⁻¹ and an etch selectivity of silicon over thermal SiO₂ of ~50 (see Chapter 6, Section 6.2.2), the current anisotropic etch recipe allows for accurate etching with sufficient latitude to intercept the effect of RIE lag. RIE lag is the aspect ratio-dependent etching that occurs during the simultaneous etching of different features sizes due to ion depletion and redeposition.^[31] The magnitude of RIE lag in the etching of the present PhCWG, i.e., the 300 μ m interspaces between the waveguides and the ~230 nm holes of the photonic element, was ~5% according to cross-section examination of a substrate by high-resolution SEM. Some over-etching (on the order of 20% with respect to the theoretical etch time) was done to ensure complete transfer into the top silicon layer, with the SiO₂ layer acting as an etch stop.

Feature dimension analysis of the final 2D PhCWG on SOI by SEM (Fig. 7.6, after stripping the thermal SiO_2 etch mask in 1% HF) illustrates that overall pattern transfer accuracy the 2D PhCWGs on SOI by the inverse imprinting procedure is competitive in terms of feature lateral dimensions (within 4 nm in comparison to the dimensions of the silicon mold) and feature definition to the accuracy of state-of-the-art nanolithographic techniques, e.g., direct e-beam lithography and focused ion-beam

lithography. Additionally, the current critical feature dimension of NIL of 5 nm^[32] implies that the present inversion procedure could also become competitive in terms of pattern resolution capability.



Figure 7.6 SEM images of the final 2D PhCWG on SOI ($d = 228 \pm 2 \text{ nm}$, $a = 392 \pm 4 \text{ nm}$).

SEM cross-section analysis in Fig. 7.6 illustrates the merits of the systematic etch procedure (Chapter 6) in achieving anisotropic etching at high etch selectivity of silicon over thermal SiO_2 .

7.2.4 Optical characterization by PSTM

Photon scanning tunneling microscopy (PSTM)^[33] was exploited to investigate the optical quality of the present 2D PhCWGs, as this technique has the capacity to investigate the propagation of light inside the line defect of 2D PhCWGs with subwavelength resolution.^[34] The phase-sensitive PSTM setup consists of a collection mode scanning near-field optical microscope (SNOM) in combination with a Mach-Zehnder interferometer. A sensitive (photon) detector measures the interference signal (from the interferometer) between the reference signal and the signal from the near-field probe. The lock-in amplifier finally processes the detector signal and allows the simultaneous measurement of the local amplitude and phase of the electromagnetic field propagating inside a 2D PhCWG. Additionally, the electronic feedback system keeps the probe at a constant height (~20 nm) above the 2D PhCWG and enables the correlation of the light intensity distribution to the resulting topographical image of the 2D PhCWG (see Fig. 7.7 for a schematic representation).



Figure 7.7 Schematic representation of the PSTM setup. The light source is a tunable laser (HP 8168F by Atecorp^[35]), operating in the wavelength range of 1440-1592 nm, and is capable of delivering laser light of a specific polarization to the 2D PhCWG through a single-mode fiber (SMF-28e(R) by Corning^[36]). A nanoscopic near-field probe senses the evanescent field of the propagating light by coupling of this field to the modes of the probe, while a piezo actuator enables raster scanning of a probe over the 2D PhCWG (max. scan size 200 × 200 μ m²) in order to obtain a local intensity distribution of the light inside the 2D PhCWG at sub-wavelength resolution.

Recently, slow light propagation in 2D PhCWGs has been shown by exploiting the scanning near-field optical microscope.^[37] The visualization of the optical propagation allows a direct 'eye-balling' of scattering at imperfections. Additionally, loss measurements on one structure are feasible, without requiring multiple structures for a (virtual) cut-back measurement.^[38] Here, advantage of the phase-sensitive PSTM was taken to elucidate the dispersion properties of 2D PhCWGs by measuring the local

amplitude and phase of the light. In this way the accuracy in the periodicity of the crystal becomes accessible.

7.2.4.1 Amplitude analysis of the 2D PhCWG

Fig. 7.8 illustrates the outcome of a typical PSTM measurement on our 2D PhCWGs.



Figure 7.8 Phase-sensitive PSTM measurement of the 2D PhCWG under investigation: (a) topographical image of the 2D PhCWG revealing the triangular arrangement of the air holes; (b) optical field distribution (amplitude) image of the propagating light ($\lambda_o = 1575$ nm). The light is incident into the 2D PhCWG from the left. The scale bar in the images denotes a lateral dimension of 400 nm.

The triangular lattice of the air holes (*a*) as well as the narrow line defect of the 2D PhCWG (W = 472 nm) are clearly resolved in the topographical image (Fig. 7.8b). The optical amplitude image (Fig. 7.8b) reveals that the propagating light is highly confined to the line defect in the 2D PhC; the amplitude outside the defect is more than 1 order of magnitude smaller at a distance equal to a single lattice unit. Apart from the high amplitude at the transition between the access waveguide and the 2D PhCWG, no high amplitude spikes are present, which would have revealed scattering at local imperfections. On closer examination, a periodicity is apparent in the amplitude signal which is equal to the lattice constant of the 2D PhCWG. This periodicity was found to remain constant irrespective of the excitation frequency of the light.^[39] This is a clear manifestation of the fact that the light propagating in the periodic waveguide is governed by Bloch's theorem,^[40] which is indicative of a high definition of the periodicity of the 2D PhCWG. Another striking feature, albeit faint, in the amplitude image is the presence of a larger periodic modulation along the

waveguide with a periodicity of about 10a (or ~4 µm) as indicated in Fig. 7.8b. This feature can be attributed to the quasi-interference between TE-like and TM-like modes.^[40]

Phase-sensitive PSTM also enables the quantification of the propagation losses of the light inside a single line defect. By selecting specific modes in reciprocal space, one can determine the losses of only the mode of interest, i.e., the TE mode. By measuring the decay of the amplitude directly as a function of position, the need for investigating other reference structures is not necessary and the measurement is insensitive to the potential influence of coupling losses (the so-called insertion losses) at the interface of the access ridge waveguide and the 2D PhCWG. Fig. 7.9a illustrates the intensity plot for light of TE polarization along the line defect of the 22 μ m long 2D PhCWG at a specific frequency ($\lambda_0 = 1575$ nm).



Figure 7.9 Propagation loss in the 2D PhCWG. (a) Intensity profile of light ($\lambda_0 = 1575$ nm) in the center of the line defect as a function of the distance from the entrance. (b) Propagation loss as a function of the wavelength over the frequency range of the tunable laser up to the cut-off frequency.

The propagation losses for light of TE polarization were determined at different excitation wavelengths (see Fig. 7.9b) from a linear fit of the intensity profiles analogous to Fig. 7.9a, assuming that the interaction between the probe and the waveguide remains constant over the entire scan area. Below the cut-off frequency, the penetration length of the light inside the line defect of the 2D PhCWG was found to

be ~2 µm. For frequencies above the cut-off frequency and below the SiO₂ light line, the forward propagating mode was selected in reciprocal space^[41] and, from its decay as a function of position, losses down to 36 dB·mm⁻¹ were found close to the cut-off frequency.^[42] By selecting the backward propagating mode, it was found that the loss induced by reflection (due to a combination of small defects inside the photonic structure and insertion loss at the end of the photonic crystal waveguide and the start of the exiting ridge waveguide) is only 4 %. The main source of losses is due to out-ofplane scattering. While these losses are higher than the reported propagation losses by Notomi,^[19a] these results are highly encouraging. Firstly, because our measurements represent the first loss measurements on high-index photonic crystal waveguides fabricated with NIL. Secondly, because it is anticipated that tuning the silicon slab thickness from the 0.8*a* in the present 2D PhCWGs to the optimal slab thickness of 0.4-0.5*a*^[43] and transforming into an air-bridge type waveguide should significantly reduce the propagation losses.

7.2.4.2 Band structure analysis of the 2D PhCWG

From the phase information of the PSTM, it is possible to study the Bloch nature of the light in the waveguide. Fourier transformation enables the determination of the spatial frequencies of the propagating light: the wavevectors. By repeating this procedure as a function of the optical frequency through tuning of the narrow bandwidth tunable laser, the photonic band diagram of the 2D PhCWG was measured in the excitation wavelength range from 1496 nm ($\omega = 0.262$) up to 1592 nm ($\omega = 0.246$).^[44] Fig. 7.10 illustrates the experimental band diagram of the 2D PhCWG in the excitation wavelength range from 1440 nm ($\omega = 0.272$) up to 1592 nm ($\omega = 0.246$) after plotting the frequency against the resulting k vectors. Several separate photonic modes are apparent in Fig. 7.10: the fundamental TE-like mode and the TM-like modes, as indicated by the white and black arrows, respectively, propagating in forward direction. That these modes are found at spatial frequencies k and k - 2/a is a direct proof that the light propagating inside the 2D PhCWG experiences a periodic modulation of the dielectric constant and a confirmation of the phase analysis concerning the periodic amplitude modulation (Section 7.2.4.1, Fig. 7.8b). The TE-like mode bends towards the edge of the

Brillouin zone ($k = \pi/a$), near $\omega = 0.245$. Apart from a slight shift of $\Delta \omega = 0.005$, the measured photonic band structure is in qualitative agreement with the calculations of Notomi.^[19]



Figure 7.10 Experimentally determined photonic band diagram of the 2D PhCWG. The arrows in the band diagram indicate the dispersion curves of different modes of the waveguide: the dispersion curves indicated with the white arrows are both part of the TE-like mode and the curves indicated by the black arrows both belong to the TM-like mode. The light gray and dark grey lines are the light lines in vacuum and in SiO₂, respectively. The black dotted lines serve as a guide to the eye for the TE mode.

A strong dispersion is apparent at the Brillouin zone edge leading to slow light between λ_0 1575 nm ($\omega = 0.249$) and 1590 nm ($\omega = 0.247$). The measured photonic band structure reflects only a minimal amount of light propagating in the backward direction. This observation again attests to the high quality of the present 2D PhCWG.

7.3 Conclusions

This chapter demonstrates the entire lithographic procedure for the fabrication and replication of 2D PhCWGs on SIO substrates. The combination of conventional photolithography with local FIB writing was shown to result in fast prototyping of silicon masters containing 2D PhCWGs with ultra-smooth sidewalls by incorporating anisotropic wet-chemical etching of silicon in OPD 4262 into the fabrication procedure.

The combination of thermal NIL, LOCOS and cryogenic RIE is very accurate in terms of pattern transfer accuracy (deviation in lateral dimension of < 4 nm) and feature definition of the final 2D PhCWG on SOI owing to the use the lithographic procedures in Chapter 6. This process shows the highest pattern transfer accuracy to date in the replication of 2D PhCWGs into SOI by NIL and is therefore a viable alternative nanofabrication technique for nanophotonics.

The present 2D PhCWG exhibits excellent optical characteristics, as shown by near-field microscopy, by illustrating good confinement of propagating light in the line defect at wavelengths in the telecommunications range with encouragingly low losses.

7.4 Experimental

Chemicals and materials: All experiments except the nanoimprint experiments were carried out in cleanroom environment. All chemicals and materials were purchased from commercial resources: 4-inch DSP <110> p-type silicon wafers (Okmatic), 8-inch SOI wafers (SOITEC, UNIBONDTM: 340 nm silicon on 2000 nm SiO₂), Olin 907 (Fujifilm Arch), hexamethyldisilazane (HMDS) (100% Selectipur: Merck), OPD 4262 resist developer (Arch Chemicals), HNO₃ (100% Selectipur: Merck, and 69% VLSI: Merck), HF (1% VLSI: Merck), H₃PO₄ (85% VLSI, Merck), acetone and isopropylalcohol (Merck, VLSI), PMMA (Aldrich), toluene (Acros), 1*H*,1*H*,2*H*,2*H*-perfluorodecyltrichlorosilane (ABCR), and vacuum grease (Crawford fitting company).

Imprint mold fabrication: the outline structures of the 2D PhCWGs on the mold were prepared by conventional optical lithography (ElectroVisions Group mask aligner (EVG), model EV620). <110> DSP silicon wafers were cleaned by a standard cleaning procedure (10 min in 70% HNO₃, 10 min in 100% HNO₃ at 95 °C). A dehydration bake on the hotplate (120 °C, 5 min) was applied on the wafers before priming with HMDS. Spin-coating of the positive photoresist Olin 907 was done at 4000 rpm for 20 s. A softbake of the resist (95 °C, 60 s) was preceding the optical exposure of the resist (3 s, 12 mW·cm⁻², hard contact with 0.7 bar contact pressure) through a mask. After a post-exposure bake at 120 °C for 60 s, the resist was developed by mild agitation in OPD 4262 resist developer for 50 s and the native oxide was removed by etching the wafers in a 1% aqueous HF for 60 s. Without delay, the wafers were immersed for 60 s in acetone to strip the resist and 60 s in isopropanol to clean the wafer, and blown dry by nitrogen before immersion in OPD 4262 resist developer. Etching of the outline structures into the silicon wafer was done for 28 min to result in etch depth of about 100 nm.

FIB milling in the photonic section of the waveguides was carried out with a stateof-the-art NOVA600 FIB dual-beam writer (FIE Company). Photonic designs were made in CorelDraw12 (d = 214 nm, a = 390 nm), and exported as a 240 dpi tiff-file (at this resolution, the photonic structures just fit in the 12-bit patterning field of the FIB resulting in maximum milling resolution). Conversion of the tiff-file into this patterning field was done in LabVIEW6 in order to translate the pixel information into coordinates for the stream file.

FIB settings on the NOVA600 dual-beam FIB writer: 30 kV accelerating voltage, 93 pA beam current, accumulative dwell time is ~1 s. The offset in the position of the stream file pattern with the actual milling position in the patterning field was 50 nm in x-direction and 150 nm in y-direction and the correct magnification was 5400. At this magnification, the field of view (FOV) is 47 µm resulting in a digital-to-analog converter (DAC) value of ~11.5 nm (= FOV/2¹²). The DAC value is the limiting factor with respect to the maximum beam current setting as accurate milling requires a reasonable beam overlap. A beam current of 93 pA was chosen for the ion beam milling, as this setting still let us to use practical mill times in combination with a good beam overlap (~50%, as the beam diameter was 22.4 nm at 93 pA). To eliminate the effect of stage drift during on the final definition of the photonic elements, the software was set to execute a dynamic drift correction (DDC) every 40 s on a reference feature (9×9 μ m² frame) within the FOV. DDC locks the position of the reference feature prior to ion beam milling and corrects for differences in its position due to stage drift. At these current settings, the time for milling a 22 µm long 2D PhCWG 100 nm deep into silicon was set by calculation:

mill time =
$$\frac{\text{number of elements} \times \text{volume per element}}{\text{beam current} \times \text{sputter rate}} = \frac{630 \times 3.6 \cdot 10^{-3} \, \mu \text{m}^3}{93 \cdot 10^{-12} \, \text{C} \cdot \text{s}^{-1} \times 0.18 \cdot 10^9 \, \mu \text{m}^3 \cdot \text{C}^{-1}} = 136 \, \text{s}^{-1} \times 0.18 \cdot 10^{-12} \, \mu \text{m}^3 \cdot \text{C}^{-1}$$

Finally, the as-fabricated molds were cleaned in a standard cleaning procedure and put on the hot plate at 120 °C for 5 min for dehydration prior to application of 1H, 1H, 2H, 2H-perfluorodecyltrichlorosilane to the molds by gas phase deposition for 60 min in a desiccator under vacuum.

Substrate preparation: Prior to processing the SOI wafers, the native oxide was removed in 1% HF for 60 s. Afterwards, a very thin layer of stoichiometric silicon nitride (Si₃N₄, 14 nm) was deposited by low-pressure chemical vapor deposition (LPCVD, Tempress LPCVD furnace: 70 sccm SiH₂Cl₂, 18 sccm NH₃, 850 °C, 200 mTorr, 150 s deposition time) with minimal delay. On top of this layer, a very thin layer of LPCVD-SiO₂ (12 nm) was deposited by LPCVD (Tempress LPCVD furnace: 50 sccm TEOS, 700 °C, 400 mTorr, bubbler at 40 °C, 90 s deposition time). After the LPCVD runs, the wafers were annealed under dry nitrogen at 800 °C for 60 min in the furnace (Tempress, dry oxidation furnace) with minimal delay and cut into smaller substrates (18×18 mm²) for further processing using the Disco dicing saw (model: DAD-321, NBC-Z dicing saw (Disco), spindle at 30000 rpm, sawing speed at 1 mm·s⁻¹). Prior to cutting, a thick (~3 µm) layer of photoresist (Olin 907) was applied on the wafer by spin-coating to protect it from damage from particles during sawing. After cutting, the substrates were cleaned using the standard cleanroom cleaning procedure.

Nanoimprint lithography: Polymer solutions were applied onto the substrates by filtering through a syringe filter with a 0.2 μ m PTFE membrane (Acrodisc CR 25 mm). Spin-coating was done at 2000-4000 rpm for 30 s in order to obtain polymer layers of about ~110 nm thickness, followed by a prebake at 100-120 °C for 10 min to remove residual solvent. For imprinting the mold pattern into a thermoplastic polymer (PMMA, M_w = 996 kD), the mold and substrate were placed in between the plates of a commercial hydraulic press (Specac, 15 tons manual press equipped with electrical heating plates and a temperature controller) and heated to the imprint temperature of 180 °C (heating rate ~30 °C·min⁻¹). Upon reaching this imprint temperature, an imprint pressure of 50 bar was applied to pattern the PMMA layer. After 2 min imprinting at 180 °C and 50 bar, the stack was cooled (cooling rate ~25 °C·min⁻¹) to 106 °C prior to pressure reduction and manual separation of the mold from the

polymer substrate with a scalpel. The total process cycle (i.e., heating, imprinting, and cooling) took about 7.5 min.

The residual polymer layer was removed by low-pressure O_2 plasma (Plasmatherm SLR 770). Of fundamental importance to the etch uniformity was the conditioning of the plasma etcher after a high-power plasma cleaning procedure (20 mTorr, 800 W ICP power, 20 W CCP power, 40 sccm O_2 , 40 sccm Ar, 25 °C). Conditioning was done by etching a thick PMMA layer on silicon for 10 min using similar conditions for etching of the residual layer. The O_2 RIE conditions (5 mTorr, 5 W, 20 sccm O_2 , 25 °C) were chosen in order to achieve directional etching (P < 10 mTorr) that is non-damaging for the polymer structures (low CCP power). At these settings the PMMA etch rate was 8 nm·min⁻¹, resulting in etch times of 2.5 min.

Local oxidation of silicon (LOCOS): The PMMA imprint patterns were transferred into the LPCVD-SiO₂ layer by etching in a 1% HF solution for 45 s. After removal of the PMMA from the substrates in acetone/isopropanol, the Si₃N₄ layer was patterned by etching in hot H₃PO₄ of 180 °C for 5 min. After removal of the LPCVD-SiO₂ mask in a 1% HF solution for 2 min, the silicon was locally oxidized in a dry oxidation furnace (Tempress, dry oxidation furnace, atmospheric pressure, 4000 sccm O₂, 950 °C, 12 min) using the Si₃N₄ as the oxidation mask. Removal of the Si₃N₄ mask in H₃PO₄ at 180 °C for 7 min was done just before the cryogenic etching of the silicon to prevent the formation of native SiO₂.

Cryogenic reactive ion etching of silicon: Etching of the final 2D PhCWGs into the top silicon layer of the SOI substrates were carried out on a Plasmalab 100 plus system from Oxford Instruments. Prior to mounting the SOI substrate onto the substrate holder (= standard 4-inch p-type silicon wafer), the native oxide was removed in 1% HF for 60 s to assure close to 100% silicon loading. Mounting of the SOI substrate was done onto the substrate holder with minimal delay using vacuum grease on the backside of the substrate. After loading the wafer plus substrate holder into the reactor, the holding time for etching was set to 2 min for the system to equilibrate the temperature of the substrate. Parameter settings for the anisotropic etching of the final waveguide structures are: 10 mTorr, 550 W ICP power, 12 W CCP power, 34 sccm SF₆, -120 °C. At these settings, the silicon etch rate is about 600 nm·min⁻¹

and the selectivity of silicon over thermal SiO₂ is about 50. Typical etch times range from 30-40 s.

Characterization: Layer thicknesses on silicon were determined with a Plasmos SD 2002 ellipsometer operating at a wavelength of 632.8 nm. Step height measurements of the silicon molds were carried out with a Sloan Dektak II surface profiler. High-resolution SEM imaging was carried out with a LEO Gemini 1550 FEG-SEM operating in the INLENS mode for secondary electron detection. PSTM measurements were carried out with a Phantom setup, consisting of a scanning near-field optical microscope and a Mach-Zehnder interferometer, at the Amolf institute.

7.5 References and notes

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- [26] Feature dimension analysis of the diameter (*d*) and periodicity (*a*) was done by statistical calculation of about 20 measurements from the high-resolution SEM images. Each feature was fit by a circle in order to determine the feature diameter. The periodicity was taken as the distance of the centers from two adjoining features.
- [27] At the perimeter of each circular feature the ion beam is milling with 25% of the current beam diameter (~ 6 nm) outside the feature region

- [28] The deviation between the design diameter and the actual diameter therefore is a result of the accuracy of the milling process. The cause of the deviation could reside in the current beam overlap of the milling process and the feature definition in the stream file. At exporting the vector design in CorelDraw12 into a 240 dpi tiff-file, the program approximates the circular elements into an integer number of dots. At 240 dpi (i.e., 94 dots per cm), the diameter of each element consists of 19-20 dots. Theoretically, the resulting diameter of the circular elements after translation into a stream file is in the range of 218-230 nm (number of dots × DAC value). See Experimental (Section 7.4) for further details.
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Summary

This thesis contributes to the continuous development of patterning strategies in several different areas of unconventional nanofabrication, from the transfer of molecules into periodic patterns onto surfaces (Chapter 3), to the structuring of thermoplastic polymers into functional templates (Chapters 4 and 5), and towards the fabrication of functional nanophotonic devices such as two-dimensional photonic crystal waveguides (2D PhCWGs) (Chapters 6 and 7). The advancements shown in these chapters have been the result of incorporating several potential patterning strategies into existing nanofabrication techniques. These advancements include the precise control over the pattern stability of molecular patterns in microcontact printing (μCP) and dip-pen nanolithography (DPN) by the use of multivalent supramolecular interactions of functional molecules with receptor surfaces (i.e., molecular printboards), the *facile generation of high-resolution templates* in (general) lithography by the use of capillary forces in the molding of thin polymer films at high annealing temperatures, and the *accurate pattern transfer* in nanoimprint lithography (NIL) by the use of local oxidation of silicon (LOCOS) in combination with ultrathin transfer layers to convert resist patterns into inverse patterns of silicon oxide (SiO₂).

An extensive overview of the field of unconventional nanofabrication (from 1995 to mid-2008) with a focus on significant technological advancements has been presented in Chapter 2. A differentiation in the current set of nanofabrication techniques was made between pattern generation and pattern replication, as these represent two distinct patterning concepts. Special attention was given to contributions in the field of unconventional nanofabrication that result in the generation and/or replication of photonic nanostructures. The results throughout this thesis elaborate on several patterning strategies shown in Chapter 2 to further advance the patterning capability of current nanofabrication techniques in terms of the applicability to structure different materials, transfer fidelity, or feature resolution.

Chapter 3 describes the selective transfer of adamantyl-functionalized guest molecules to β -cyclodextrin (β -CD) receptors on surfaces by means of conformal contact with elastomeric stamps or by scanning across the surface with atomic force microscopy (AFM) tips. The degree of guest deposition by transfer of elastomeric poly(dimethylsiloxane) (PDMS) stamps was systematically studied by X-ray photoelectron spectroscopy (XPS), illustrating (in case of a divalent guest) a significant decrease from ~5 monolayers of guest molecules after transfer down to 1-2 monolayers and even to submonolayer coverages after rinsing with 50 mM NaCl and 1-10 mM β -CD solutions, respectively. Additionally, all kinetic stabilities were observed in additional printing and rinsing experiments using monovalent, divalent and multivalent guests by contact-mode AFM imaging, from fast removal during rinsing with water (in case of monovalent guests) to complete stability during rinsing with competitive β -CD solutions (in case of multivalent guests). The tunable pattern stability in combination with the absence of nonspecific interactions, as shown by the complete removal of multivalent guests on non-receptor monolayers, are an explicit demonstration of the concept of multivalency and moreover of the applicability of the molecular printboard in nanofabrication by supramolecular μCP and DPN. This applicability was furthermore supported by the successful electroless deposition (ELD) of copper on top of patterns of catalytically active nanoparticles and the fabrication of molecular patterns at a resolution of 60 nm by DPN.

Chapter 4 presents the successful combination of capillary force lithography (CFL) and replica molding (REM) to generate second-generation high-resolution stamps for

soft lithography. Several measures were taken to improve the overall performance of CFL in patterning thin polymer films over large areas, including the use of thin (~1 mm thick) PDMS stamps and pre-equilibration of the stamps and substrates to the annealing temperature before conformal contact. Additionally, the process of capillary kinetics during CFL of thin layers of poly(methylmethacrylate) (PMMA) was examined as a function of the initial polymer thickness by AFM analysis, illustrating that some control over the lateral feature dimensions of the resulting polymer templates was feasible in the range of several hundreds of nanometers. The highresolution templates were resistant to the REM conditions, as there was no evidence of noticeable degradation or damage on the AFM scale after generating more than 10 PDMS copies. The applicability of these second-generation stamps in soft lithography was illustrated in a series of different μCP experiments of alkanethiols on gold and successive wet-chemical etching of the gold to generate metal patterns at a resolution of ~125 nm. The accompanying discrepancy between the lateral dimensions of the metal features and the indentations present on the stamp was not further investigated, but is most probably the result of ink diffusion and deformation of the stamp during printing. By optimization of the etch conditions using the experimental black silicon method (BSM), nanochannels with vertical sidewalls were fabricated using the resulting metal patterns as secondary etch masks in the RIE of the silicon substrate.

Chapter 5 describes two additional applications for the high-resolution templates, specifically, as masters in NIL and as etch masks in the wet-chemical etching of metals. For the first time, a UV-curable polymer (mr-L 6000) was implemented in CFL in order to meet the stringent requirements of high temperature and pressure stability for NIL molds. The exposure dose dependence of the stability of the templates during UV curing was studied in detail by AFM analysis to determine the critical exposure dose for high-fidelity crosslinking. An additional long exposure and hard bake was carried out on the polymer templates to further increase the mechanical and thermal stability, as shown by the perfect response in durability test for tens of imprints. The applicability of these templates in NIL was shown by the fabrication of metal patterns at a resolution of ~150 nm by metal lift-off and by wet-chemical etching of the underlying metal layer. The transfer of PDMS residues during CFL

processing was unambiguously proven by XPS analysis and was found (in the case of PMMA and low-modulus stamps) to severely compromise the processability of the polymer templates in applications that necessitate the removal of the residual layer by O_2 plasma. The use of poly(styrene) (PS) and longer curing of the PDMS stamps were effective measures to increase the processability of the polymer templates, as shown by the use of these templates as etch masks in the wet-chemical etching of the underlying metal layer. Additionally, the strategy of over-etching was shown as a means to control the reduction of the lateral dimensions of the metal features to a resolution of ~80 nm.

Chapter 6 describes in detail the development of a novel pattern inversion strategy by incorporation of LOCOS in NIL processing. Proper design rules were set in order to convert resist patterns into inverse patterns of SiO₂ with high transfer accuracy, resulting in a set of two ultrathin transfer layers by optimization of the etch selectivities and layer thicknesses. High-temperature annealing of the set of transfer layers was found as a means to engineer the etch rates and therefore the etch selectivities of this system. The partial conversion of silicon nitride (Si₃N₄) in LOCOS compromises to some extent the processability, as shown by the large reduction of the initial etch rate in the first two minutes of wet-chemical etching of the Si₃N₄ layer. For the resulting ultrathin (~10 nm) etch masks of thermal SiO₂, several guidelines were shown to systematically locate the etch parameters for etching silicon at high anisotropy and high etch selectivity.

Chapter 7 presents the fabrication and successful replication of 2D PhCWGs on silicon-on-insulator substrates and elaborates on the LOCOS inversion and anisotropic etch strategies shown in Chapter 6. After the fast prototyping of the necessary master by combining focused ion-beam and conventional lithography, 2D PhCWGs were accurately replicated as shown by the very high accuracy (< 4 nm deviation in lateral feature dimensions) and feature definition of the features using the novel inverse imprinting procedure. Excellent guiding properties at wavelengths in the telecommunications range was observed by local examination of the resulting 2D PhCWGs by photon scanning tunneling microscopy (PSTM), yielding propagation losses down to 36 dB·mm⁻¹. The high quality of the 2D PhCWGs was moreover supported by the absence of high amplitude spikes (from the optical field

distribution image), and the minimal amount of light propagating in the backward direction (from the photonic band diagram).

Collectively, the results in this thesis illustrate the diversity and potential of unconventional nanofabrication in the patterning of matter on the sub-100 nm scale. The work on molecular printboards demonstrates the applicability of supramolecular interactions in μ CP and DPN in the patterning of molecular patterns with tunable kinetic stability. Especially the compatibility of this patterning strategy with DPN will facilitate the development of future detection assays by the immobilization of (bio)molecules on molecular printboards. The work on CFL and the application of the polymer templates in several lithographic applications demonstrate the potential to structure, in this particular case, metal films at high resolution. The general applicability of edge lithography in the cost-effective generation of (periodic) highresolution features will not only find its use in exploratory research, but ultimately in the fabrication of devices containing nanochannels and/or nanowires. Finally, the work on a novel pattern inversion strategy in the low-cost replication of 2D PhCWGs by NIL illustrates that unconventional nanofabrication techniques can also offer the transfer accuracy and feature definition of state-of-the-art techniques. This resolution enhancement technique is one of many that will further enforce the position of NIL as a candidate for next-generation lithographic technologies in IC manufacturing.

Samenvatting

Dit proefschrift beschrijft de continue ontwikkeling van strategieën op een aantal verschillende gebieden van niet-conventionele nanofabricage. Van de overdracht van moleculen in periodieke patronen op oppervlakken (hoofdstuk 3) en het structureren van thermoplastische polymeren in functionele sjablonen (hoofdstukken 4 en 5) tot de vervaardiging van functionele nanofotonische structuren, zoals twee-dimensionale golfgeleiders op basis van fotonische kristallen (2D PhCWGs) (hoofdstukken 6 en 7). De vorderingen die in deze hoofdstukken worden beschreven zijn het resultaat geweest van het incorporeren van een aantal strategieën in bestaande nanofabricagetechnieken. Deze vorderingen omvatten de nauwkeurige controle over de stabiliteit van *moleculaire patronen* met microcontactdruk (μ CP) en dip-pen nanolithografie (DPN) door het gebruik van multivalente supramoleculaire interacties van functionele moleculen met receptoroppervlakken (moleculaire printplaten). Daarnaast het gemakkelijk genereren van hoge-resolutie-sjablonen voor lithografie (in het algemeen) door het gebruik van capillaire krachten in het patroneren van dunne polymeerfilms bij hoge verhardingstemperaturen. Tenslotte het accuraat overdragen van patronen via nanoimprintlithografie (NIL) door lokale oxidatie van silicium (LOCOS) te combineren met uiterst dunne overdrachtslagen om patronen in polymeren te inverteren in silicium oxide (SiO_2) .

Een uitgebreid overzicht van het gebied van niet-conventionele nanofabricage (vanaf 1995 tot medio 2008), met de nadruk op significante technologische vorderingen, is beschreven in hoofdstuk 2. De huidige nanofabricagetechnieken zijn onderverdeeld in het genereren en repliceren van patronen, aangezien dit twee verschillende concepten zijn. Speciale aandacht is besteed aan bijdragen in de nietconventionele nanofabricage gericht op de vervaardiging en/of reproductie van fotonische nanostructuren.

De resultaten beschreven in dit proefschrift borduren voort op een aantal van de fabricagestrategieën uit Hoofdstuk 2 met als doel om de capaciteit in patronering van de huidige nanofabricagetechnieken verder te brengen in termen van toepasbaarheid voor het structureren van verschillende materialen, overdrachtsnauwkeurigheid, en resolutie.

Hoofdstuk 3 beschrijft de selectieve overdracht van adamantyl-gefunctionaliseerde gastmoleculen naar β -cyclodextrine (β -CD)-receptoren op oppervlakken, door middel van contact met elastische stempels of door het oppervlak te scannen met een tip van een atomaire-krachtmicroscoop (atomic force microscope, AFM). De mate van depositie door overdracht van elastische poly(dimethylsiloxaan) (PDMS) stempels werd systematisch bestudeerd met Röntgen-fotoelectron spectroscopie (X-ray photospectroscopy, XPS). In geval van een divalente gast liet dit een significante daling zien van ~5 monolagen gastmoleculen, tot 1-2 monolagen en een submonolaag, na het spoelen met oplossingen van respectievelijk 50 mM NaCl en 1-10 mM β -CD. Met de AFM in contact-modus werd in additionele experimenten een scala aan kinetische stabiliteiten waargenomen door gebruik te maken van mono-, di- en multivalente gasten. Van snelle verwijdering tijdens het spoelen met water (in geval van monovalente gastmoleculen) tot volledige stabiliteit tijdens het spoelen met competitieve β -CD-oplossingen (in geval van multivalent gasten). De instelbare patroonstabiliteit is een expliciete demonstratie van multivalentie en de toepasbaarheid van het moleculaire printboard in nanofabricage met supramoleculaire μ CP en DPN. Deze toepasbaarheid werd verder gedemonsteerd door de succesvolle autokatalytische

depositie (ELD) van koper op patronen van katalytisch actieve nanodeeltjes en de vervaardiging van moleculaire patronen met een resolutie van 60 nm door DPN.

Hoofdstuk 4 beschrijft de succesvolle combinatie van capillaire-krachtlithografie (CFL) en replica-vormgieten (REM) om hoge resolutiestempels van de tweede generatie te produceren voor zachte lithografie. Via een aantal aanpassingen werden de prestaties van CFL voor het patroneren van dunne polymeerfilms over grote oppervlakken verbeterd, met inbegrip van het gebruik van dunne (~1 mm) PDMSstempels en het vooraf thermisch equilibreren van de stempels en de substraten. De capillaire kinetiek tijdens CFL van dunne lagen poly(methylmethacrylaat) (PMMA) werd door AFM-analyse onderzocht, als functie van de initiële polymeerdikte. Dit laat een zekere controle zien van de laterale dimensies van de resulterende polymeersjablonen, over een bereik van enkele honderden nanometers. De hogeresolutie-sjablonen zijn bestand tegen de condities van REM, aangezien er geen merkbare degradatie of schade na het produceren van meer dan 10 PDMS-stempels te zien was. De toepasbaarheid van deze tweede-generatie-stempels in zachte lithografie werd geïllustreerd in een reeks van e stempel experimenten met alkaanthiolen op goud en het nat-chemisch etsen van het goud, om metaalpatronen met een resolutie van ~125 nm te produceren. De discrepantie tussen de laterale dimensies van de metaalpatronen en de patronen op de stempel is niet verder onderzocht, maar is vermoedelijk het resultaat van inktspreiding en vervormingen van de stempel tijdens contact. Door het optimaliseren van de etscondities met de experimentele zwartesilicium-methode (black silicon method, BSM) en door de metaalpatronen te gebruiken als secundair etsmasker, werden nanokanalen met verticale zijwanden gemaakt in het siliciumsubstraat met behulp van reactief ion-etsen (reactive ion etching, RIE).

Hoofdstuk 5 beschrijft twee andere toepassingen voor de hoge-resolutie-sjablonen, namelijk als mal in NIL en als etsmasker in het nat-chemisch etsen van metaallaagjes. Voor het eerst werd een UV-hardend polymeer (mr-L 6000) toegepast in CFL om aan de strenge vereisten van stabiliteit bij hoge temperatuur en druk voor NIL-sjablonen te voldoen. De afhankelijkheid van de stabiliteit van de sjablonen aan de dosis tijdens de blootstelling tijdens het UV-harden werd in detail bestudeerd door AFM-analyse om de kritieke dosis te bepalen voor een accuraat polymerisatieproces. Een extra lange UV-blootstelling en uitbakken van de polymeersjablonen mechanische en verhogen de thermische stabiliteit verder, zoals blijkt uit een duurzaamheidstest voor tientallen afdrukken. De toepasbaarheid van deze sjablonen in NIL werd aangetoond door de vervaardiging van metaalpatronen met een resolutie van ~150 nm, door selectieve metaaldepositie alsmede door het nat-chemisch etsen van de onderliggende metaallaag. De overdracht van PDMS-residuen tijdens het CFL-proces, bewezen door XPS-analyse, veroorzaakt in geval van PMMA-films en lage-modulus-PDMS-stempels beperkingen in toepassingen die een verwijdering van de residuale laag door O₂-plasma vereisen. Het gebruik van poly(styreen) (PS) en het langer uitharden van de PDMS-stempels zijn efficiënte maatregelen om de toepasbaarheid van de polymeer-sjablonen te verhogen, zoals aangetoond door het gebruik van deze sjablonen als etsmaskers in het nat-chemisch etsen van de onderliggende metaallaag. Als laatste werd een strategie beschreven voor overetsen om de laterale dimensies van metaalpatronen tot een resolutie van ~80 nm gecontroleerd te reduceren.

Hoofdstuk 6 beschrijft de ontwikkeling van een nieuwe strategie voor patrooninversie door LOCOS te integreren in het NIL-proces. Richtlijnen werden ontwikkeld voor een paar uiterst dunne overdrachtslagen om polymeerpatronen te inverteren in SiO₂ door optimalisering van de etsselectiviteit en laagdikte voor een hoge overdrachtsnauwkeurigheid. Deze overdrachtslagen werden bij hoge temperatuur verdicht om de etssnelheden en dus de etsselectiviteit aan te passen. De gedeeltelijke omzetting van siliciumnitride (Si₃N₄) in LOCOS compromitteert in zekere mate de verwerkbaarheid, zoals aangetoond door de grote reductie van de initiële etssnelheid in de eerste twee minuten tijdens het nat-chemisch etsen van de Si₃N₄-laag. Om het uiterst dunne thermisch-SiO₂ als etsmasker te kunnen gebruiken zijn er richtlijnen ontwikkeld voor het optimaliseren van de parameters voor het etsen van silicium met hoge anisotropie en hoge selectiviteit.

Hoofdstuk 7 beschrijft de vervaardiging en succesvolle replicatie van 2D PhCWGs op silicium-op-isolator-substraten, door gebruik te maken van de LOCOS-inversieprocedure en de strategie voor anisotroop etsen uit Hoofdstuk 6. Na de fabricage van de noodzakelijke master, door gefocusseerde ionen-bundellithografie (focussed ionbeam, FIB) te combineren met conventionele lithografie, werden 2D PhCWGs nauwkeurig nagemaakt, zoals aangetoond door de zeer hoge nauwkeurigheid (< 4 nm
afwijking in de laterale dimensies) en definitie van de patronen met de nieuwe procedure voor patrooninversie. Uitstekende lichtgeleidende eigenschappen met propagatie-verliezen tot 36 dB·mm⁻¹ werden waargenomen bij golflengtes in het telecommunicatiebereik door de 2D PhCWG lokaal te bestuderen met de fotonraster-tunnelmicroscoop (photon scanning tunneling microscope, PSTM). De uitstekende kwaliteit van 2D PhCWGs werd bovendien ondersteund door de afwezigheid van hoge amplitudepieken (in de ruimtelijke verdeling van het optisch veld) en de minimale hoeveelheid licht dat propageert in de achterwaartse richting (in het fotonisch bandendiagram).

De onderzoeksresultaten beschreven in dit proefschrift illustreren de diversiteit en de potentie van niet-conventionele nanofabricage in het patroneren van materie op de sub-100-nm-schaal. Het werk aan moleculaire printplaten toont de toepasbaarheid van supramoleculaire interacties in μCP en DPN aan in het genereren van moleculaire patronen met variabele kinetische stabiliteit. Het is vooral de compatibiliteit van deze strategie met DPN, die de ontwikkeling van toekomstige analysetechnieken zal stimuleren door de immobilisatie van (bio)moleculen op moleculaire printplaten. Het werk aan CFL en de toepassing van de polymeersjablonen in verscheidene lithografische toepassingen tonen de potentie aan om metaalfilms met hoge resolutie te structureren. De algemene toepasbaarheid van randlithografie in het efficient fabriceren van (periodieke) patronen met hoge resolutie zal uiteindelijk ook belangrijk zijn voor de vervaardiging van apparaten die nanokanalen en/of nanodraden bevatten. Tot slot illustreert het werk aan de nieuwe strategie voor patrooninversie met de replicatie van 2D PhCWGs, dat de niet-conventionele nanofabricatie technieken de nauwkeurigheid en de patroondefinitie van de huidige technieken kunnen bieden. Deze techniek om de resolutie te verhogen is één van vele technieken die de kansen voor NIL als kandidaat voor lithografische technologie van de volgende generatie in de IC-fabricage verder zullen versterken.

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